

FIG. 2

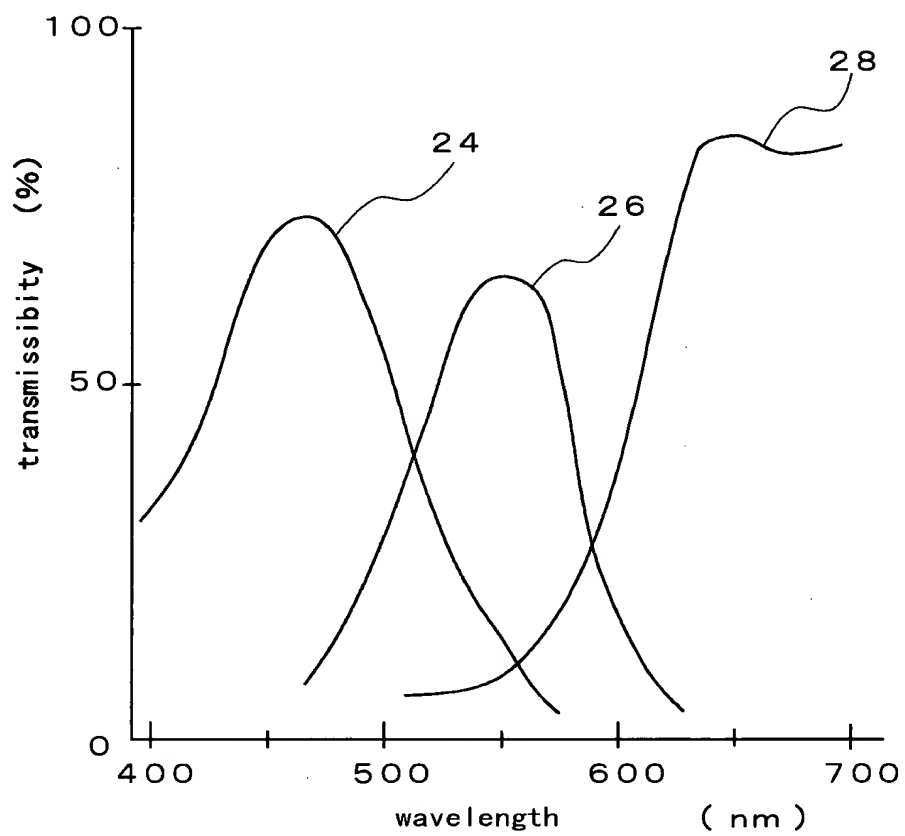


FIG. 3

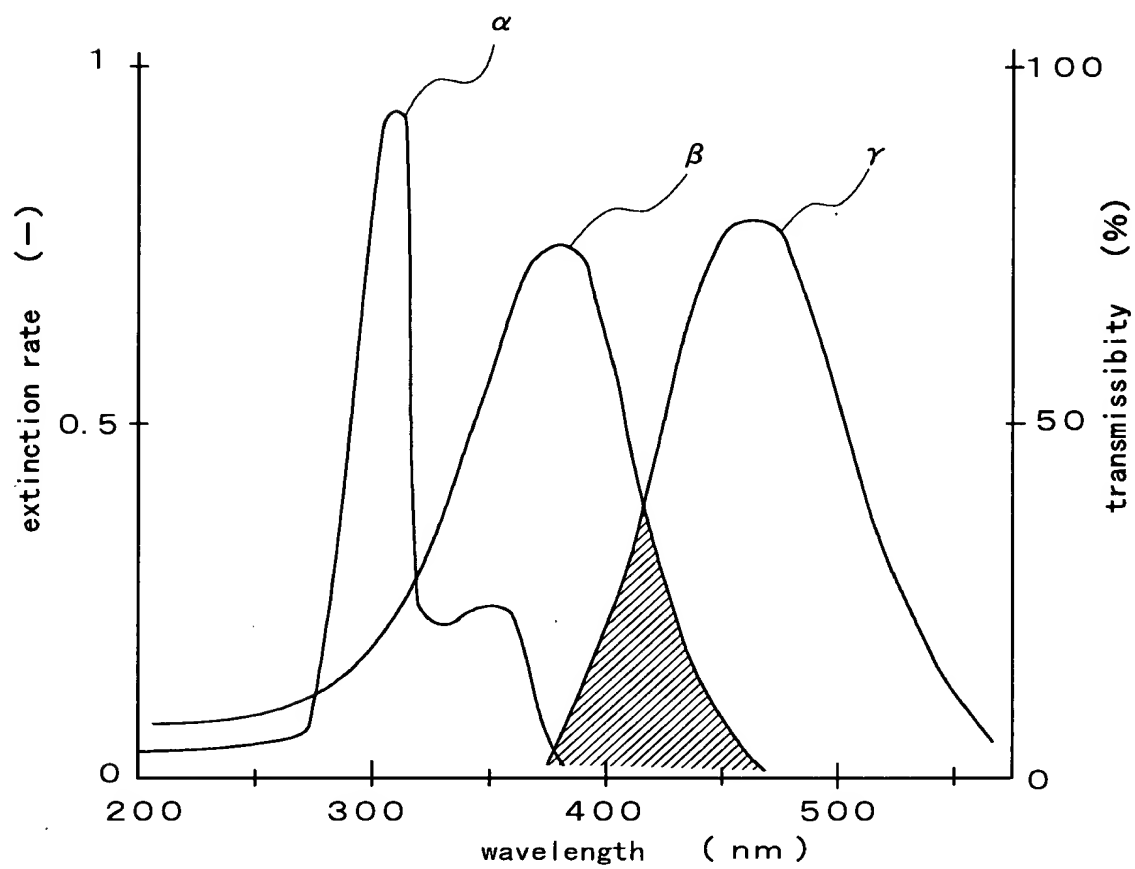


FIG. 4a

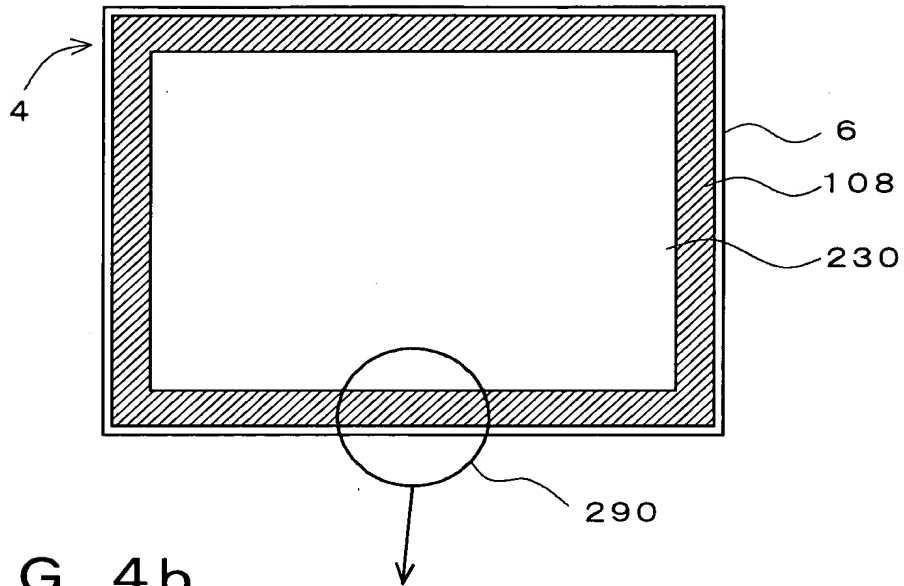


FIG. 4b

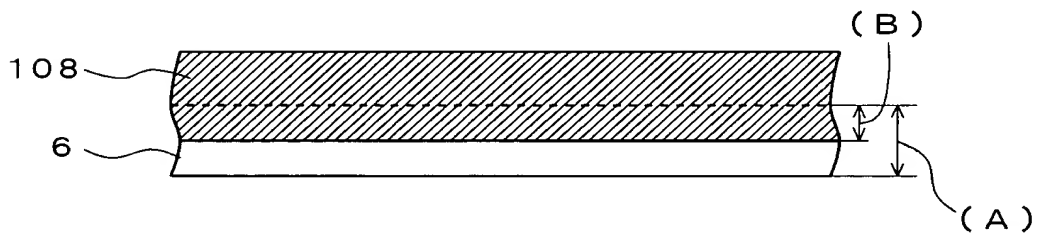


FIG. 5a

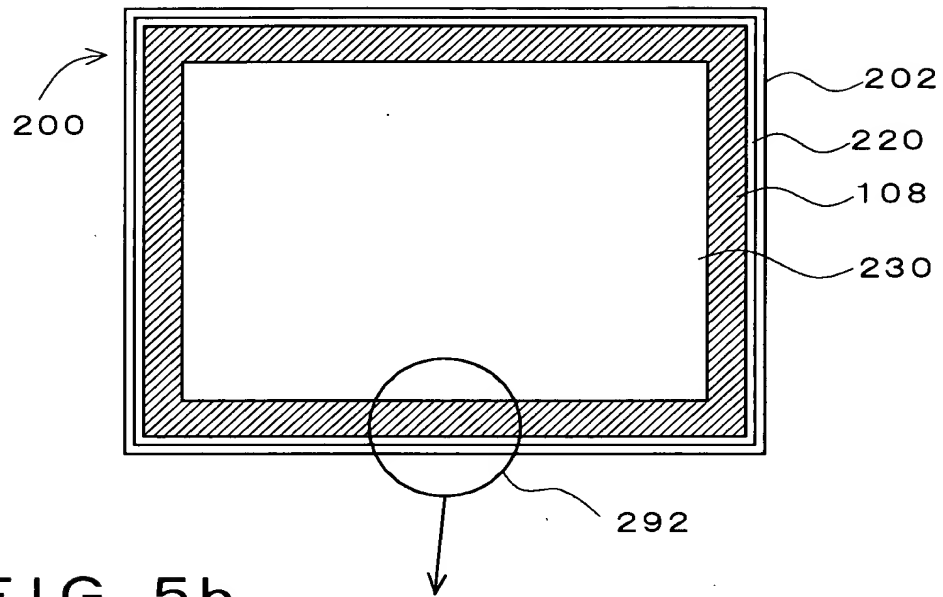


FIG. 5b

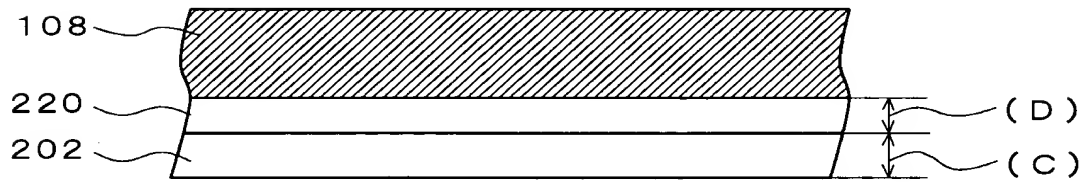


FIG. 6

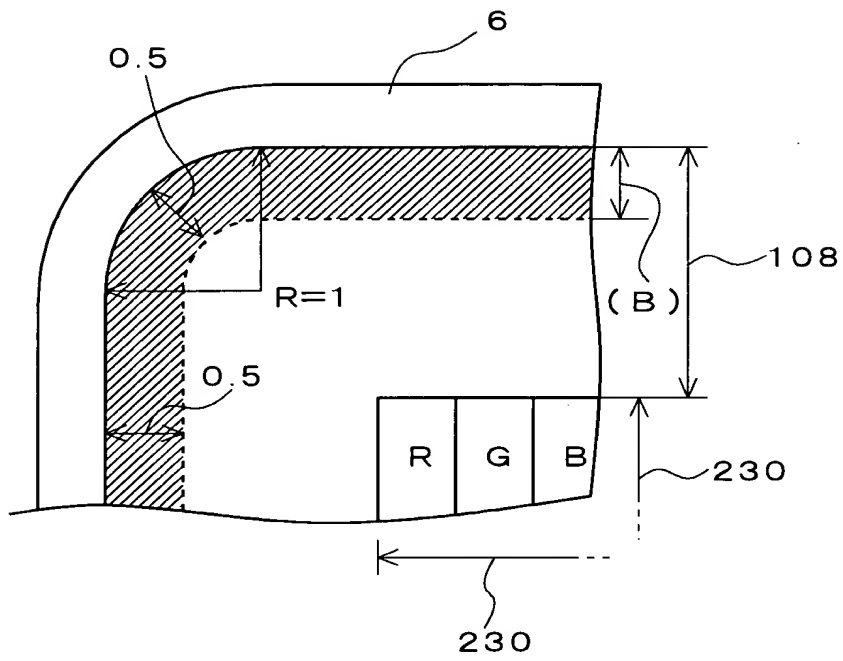


FIG. 7

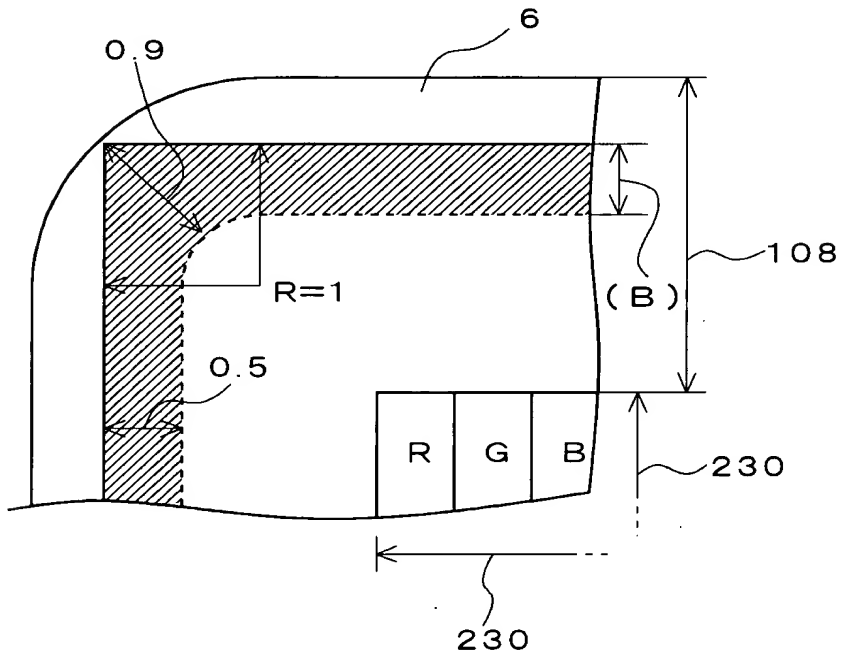


FIG. 8a

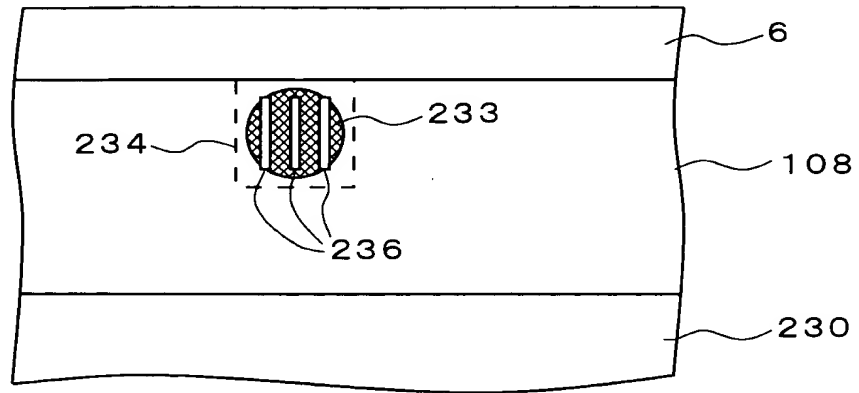


FIG. 8b

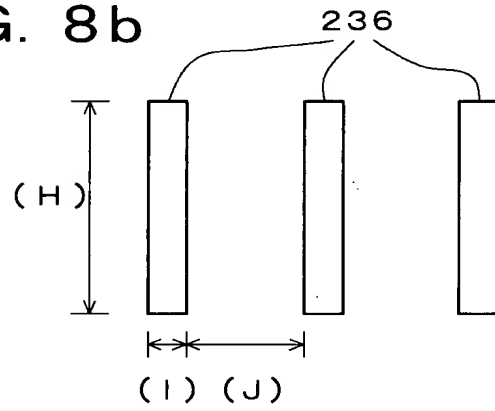


FIG. 9

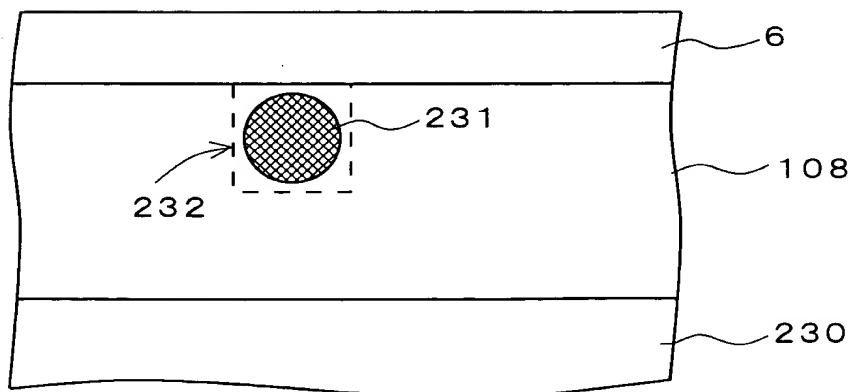


FIG. 10a

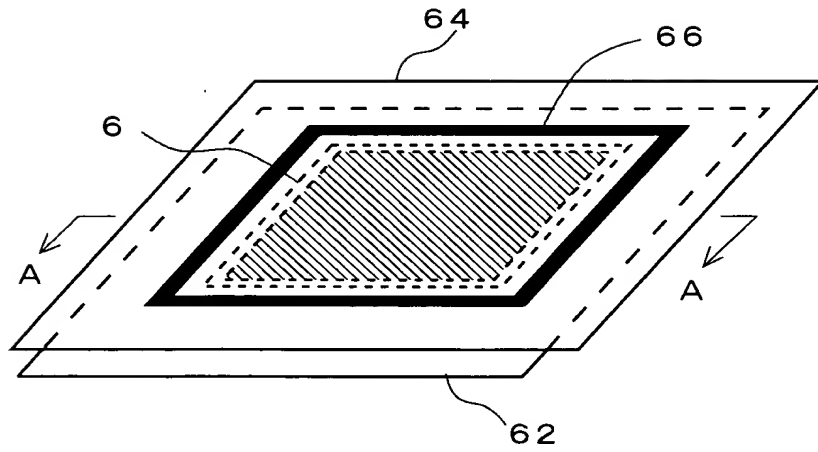


FIG. 10b

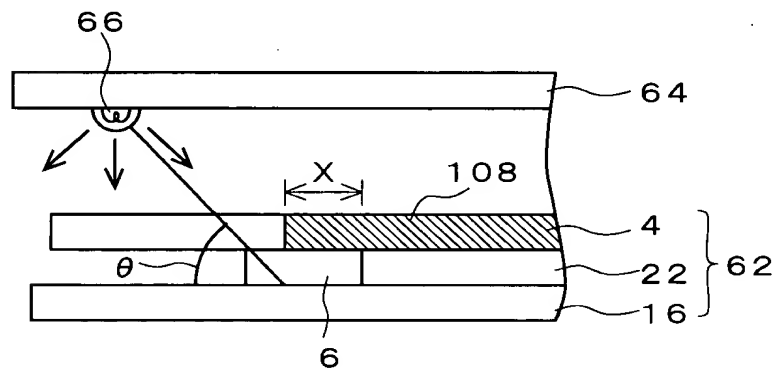
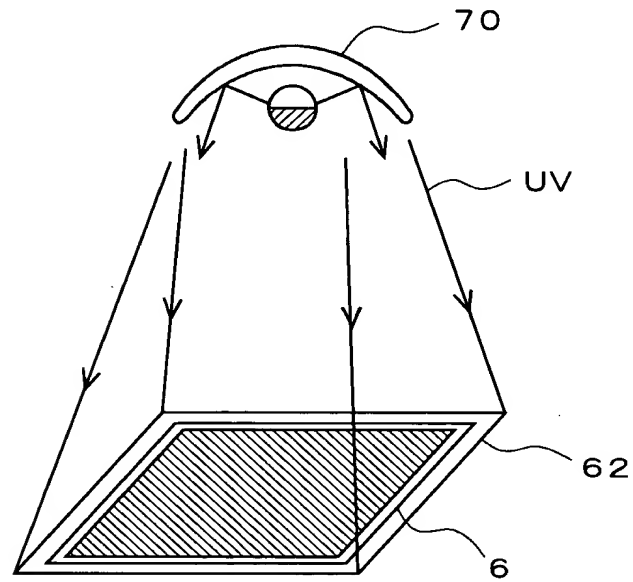


FIG. 11



This diagram shows a cross-sectional view of a multi-layered substrate. It features a top layer (1) with a wavy surface (16). Below this is a layer (2) containing a dashed line (4) and a solid line (6). A vertical dimension of 6' is indicated between the dashed and solid lines. Below the solid line is a layer (8) with a vertical dimension of 12. At the bottom is a color filter array (10) consisting of a 3x7 grid of subpixels labeled R, G, B, R, G, B, R. A vertical dimension of 14 is shown for the array, and a horizontal dimension of 10 is shown for the array width. An arrow labeled 'A' points upwards on the right side.

A cross-sectional view of a multi-layered structure. It consists of a top layer (4), a middle layer (8), and a bottom layer (16). A central region (12) is defined by two vertical lines. A curved line (6) is on the left side, and a curved line (22) is on the right side.

FIG. 13a

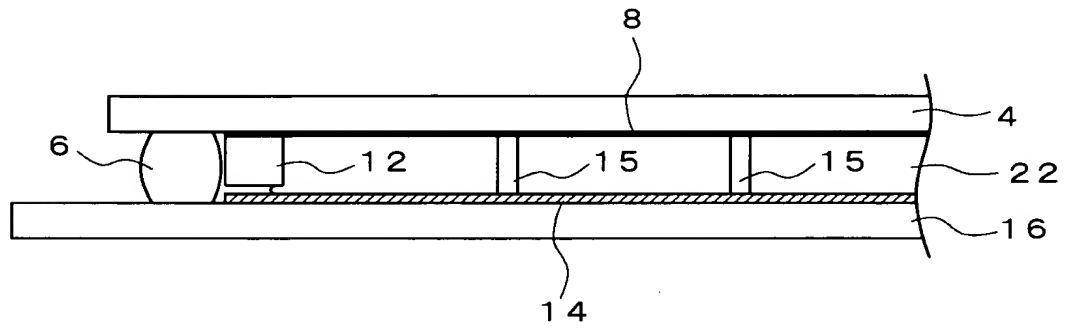


FIG. 13b

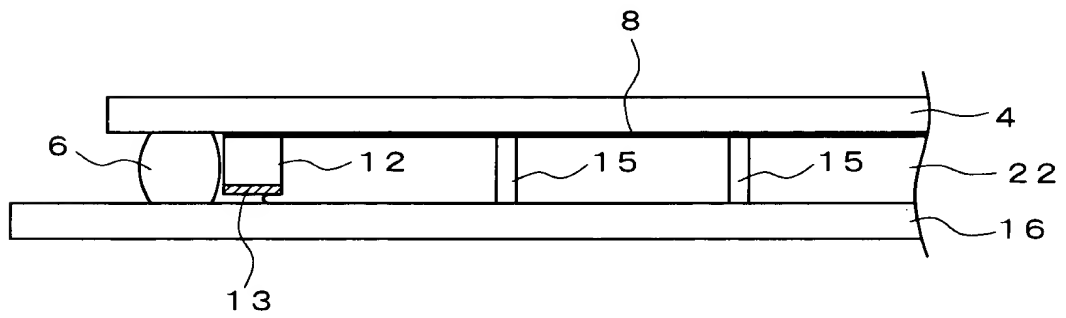


FIG. 14a

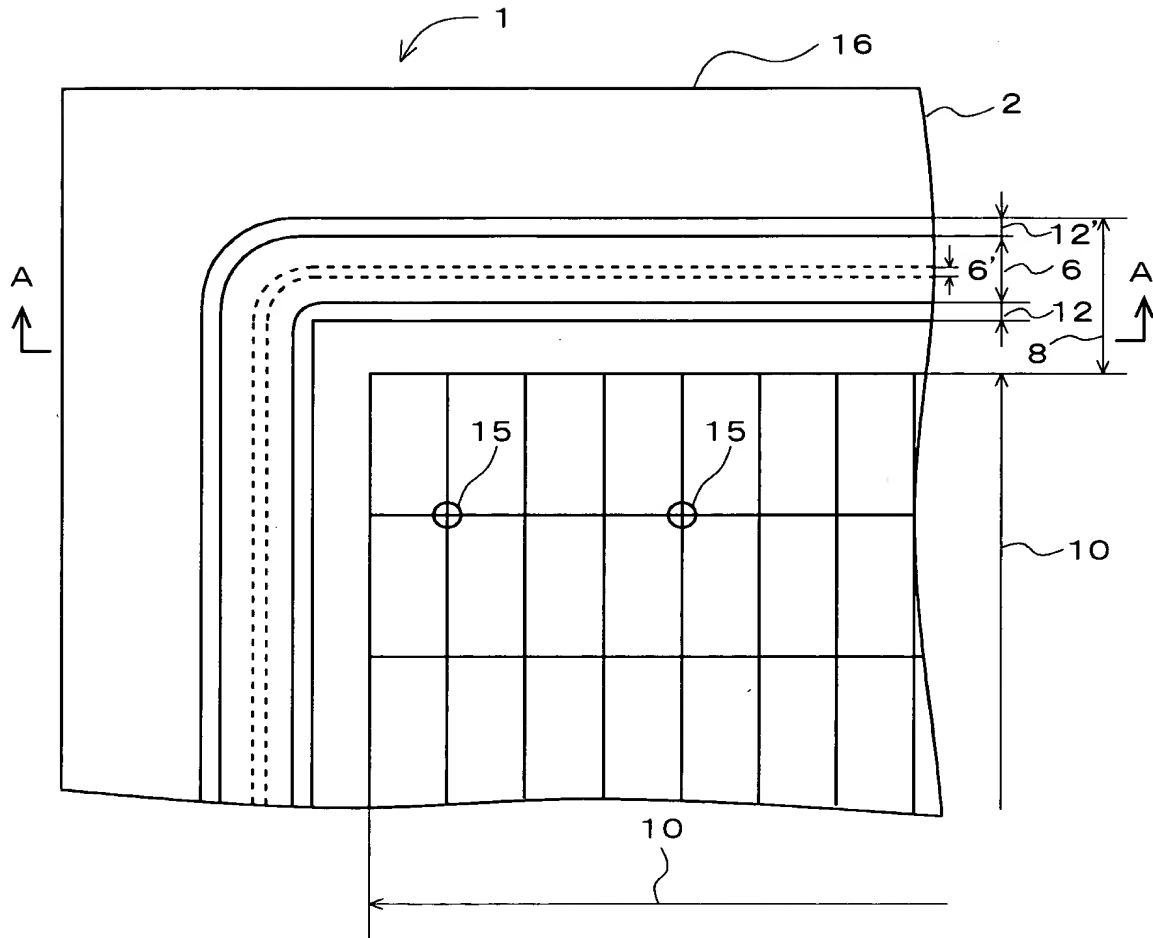


FIG. 14b

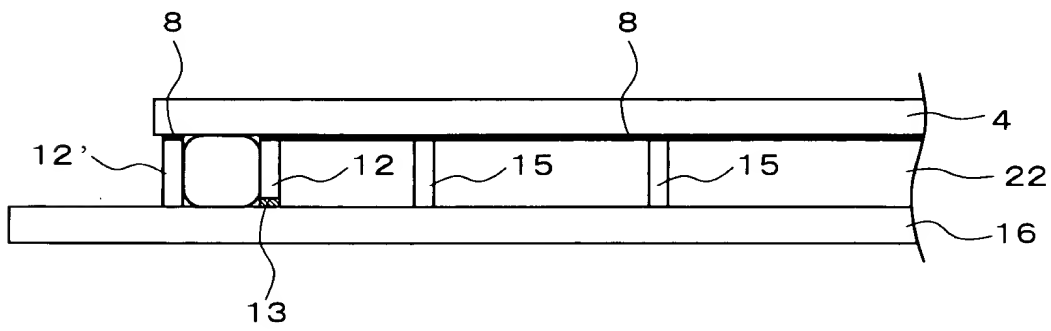


FIG. 15

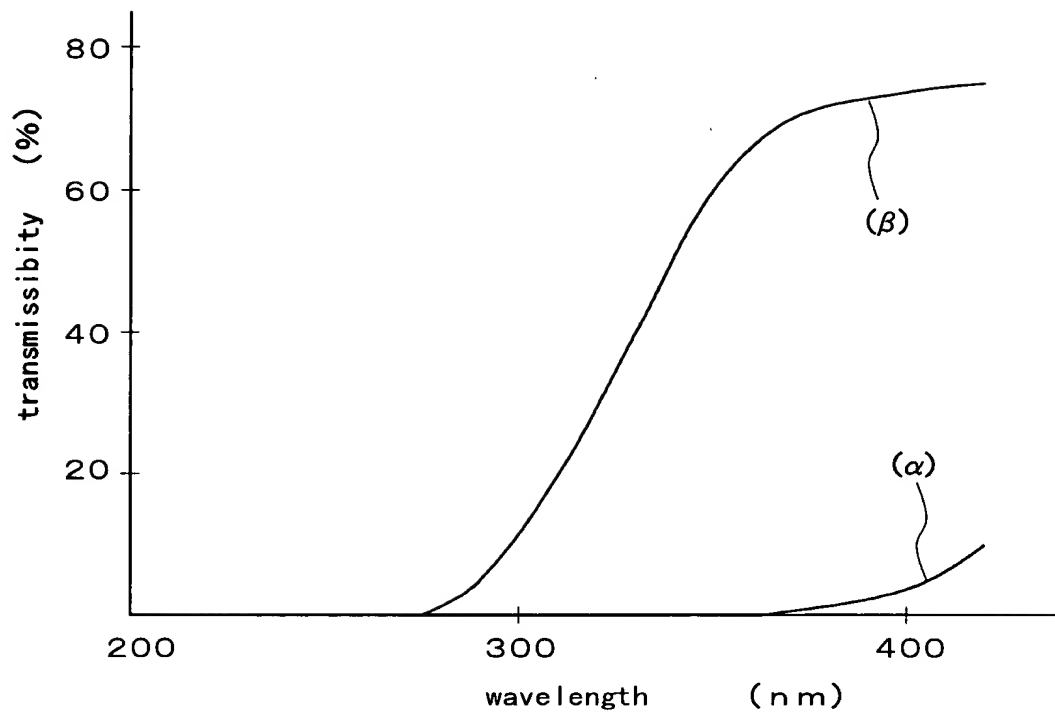


FIG. 16

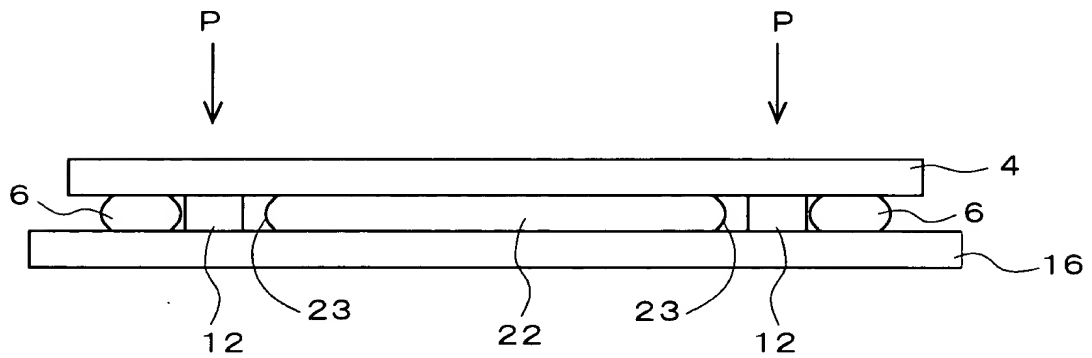


FIG. 17

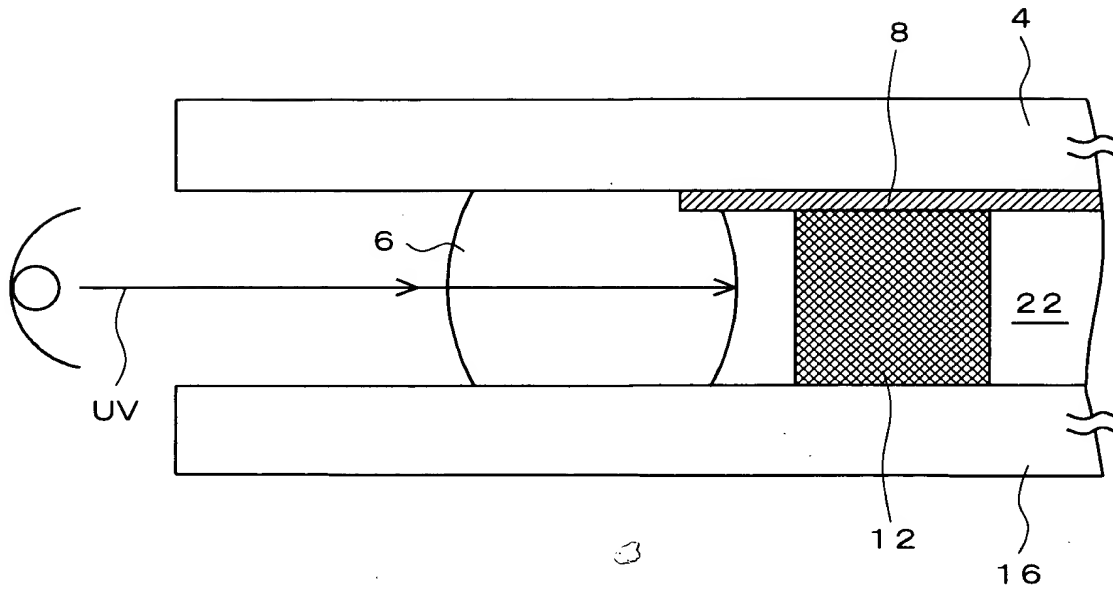


FIG. 18a

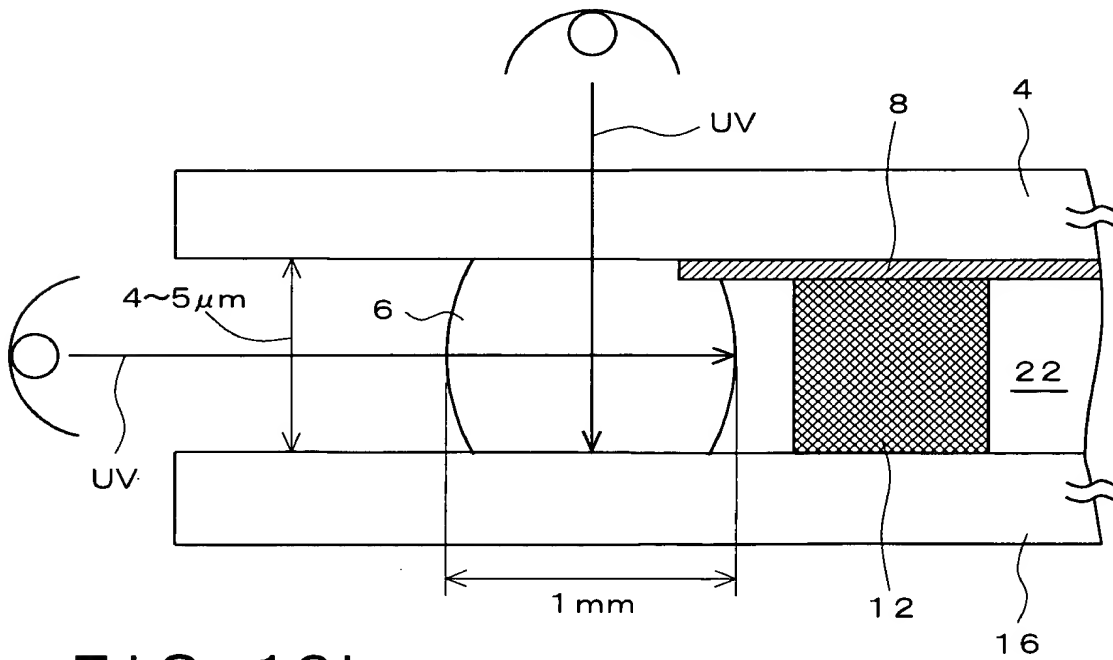


FIG. 18b

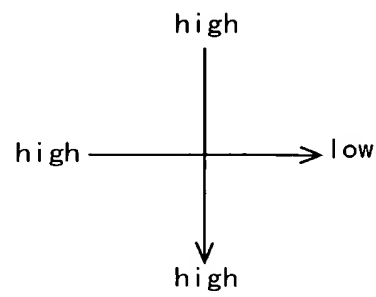


FIG. 19

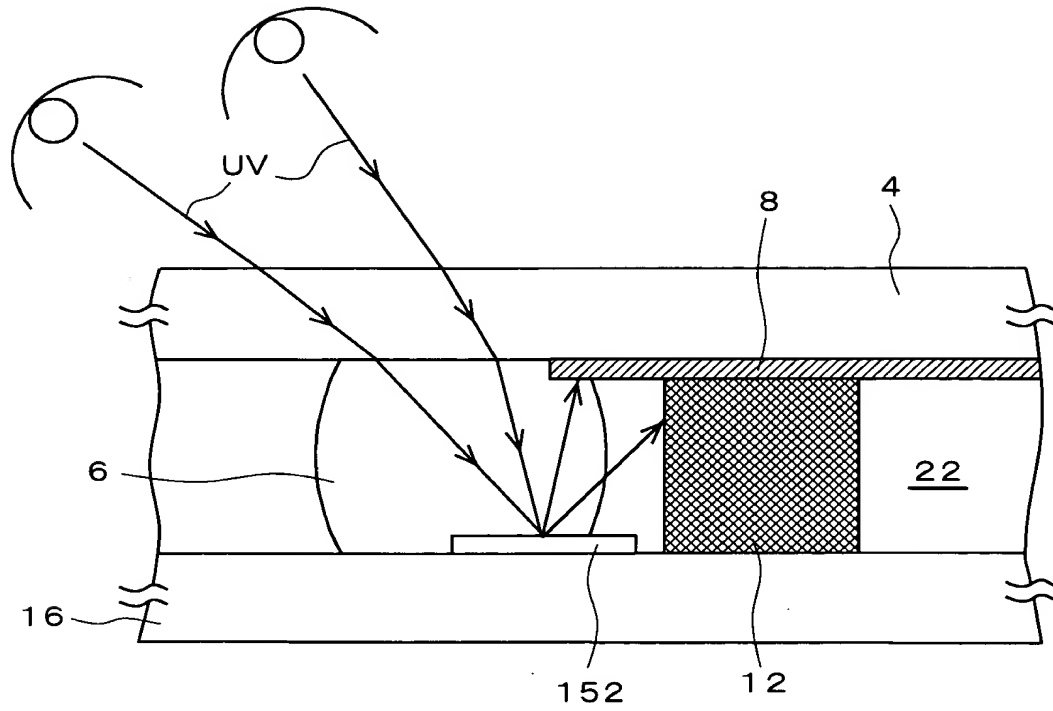


FIG. 20a

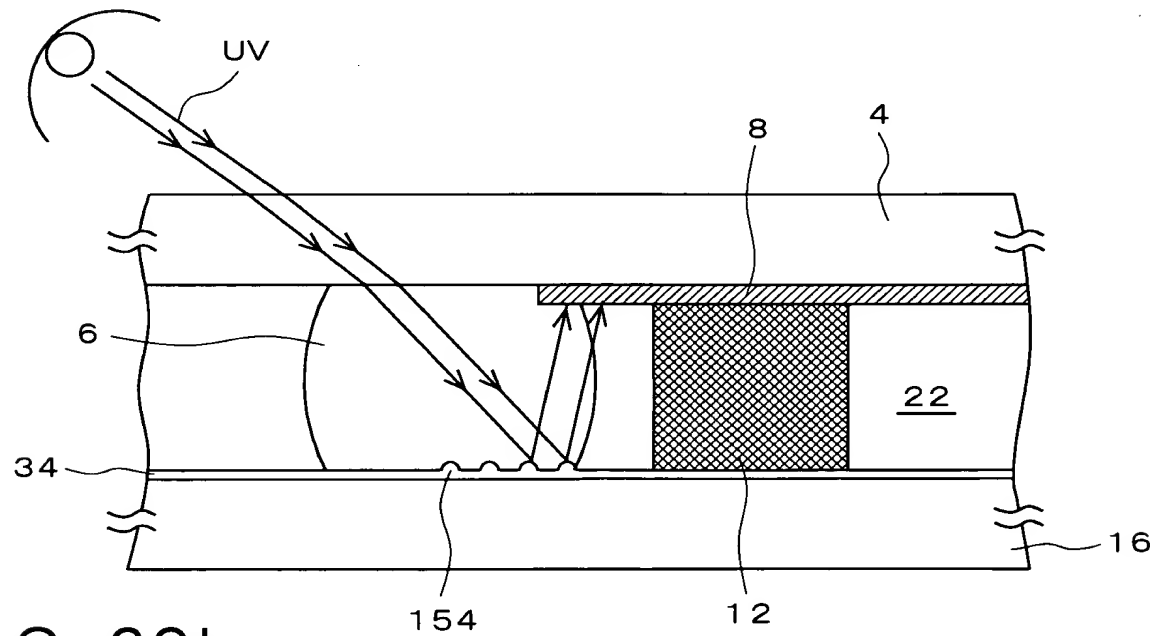


FIG. 20b

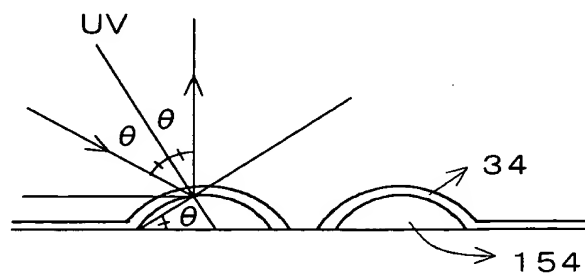


FIG. 21a

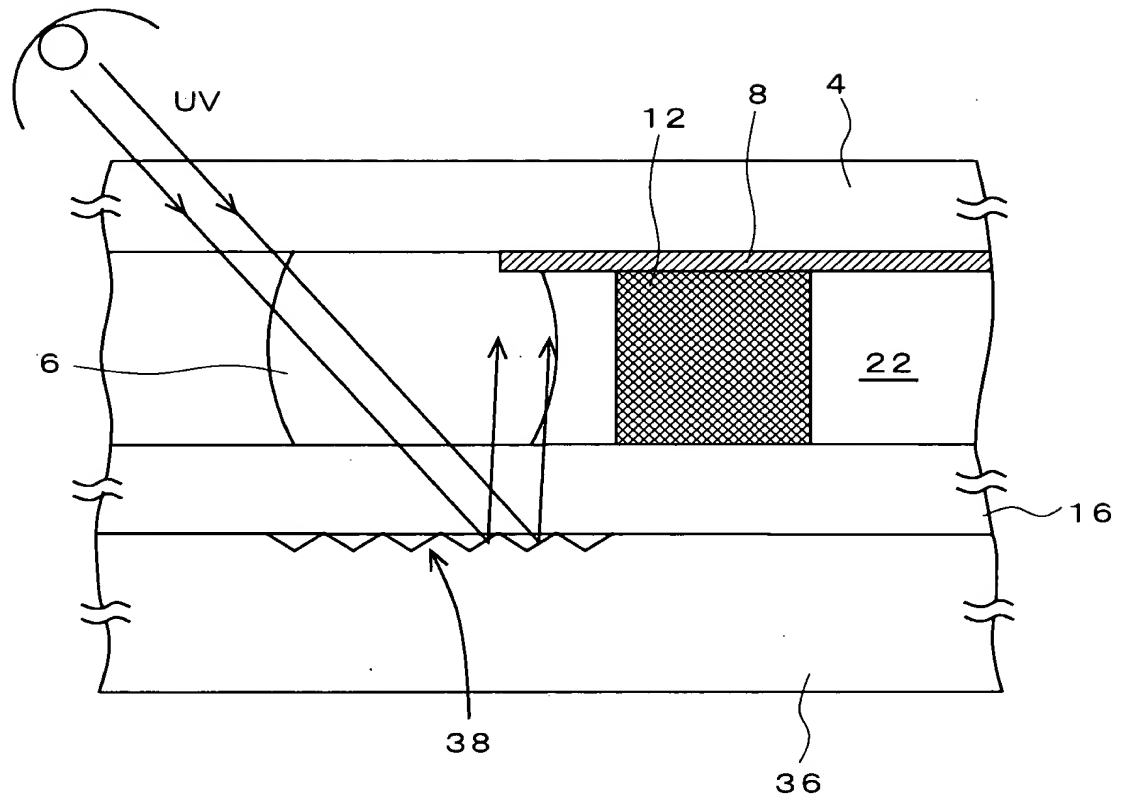


FIG. 21b

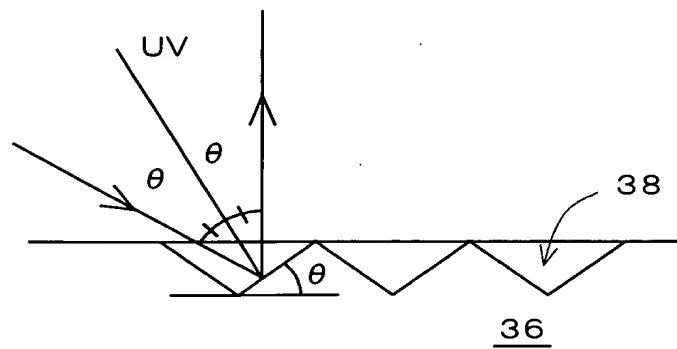


FIG. 22a

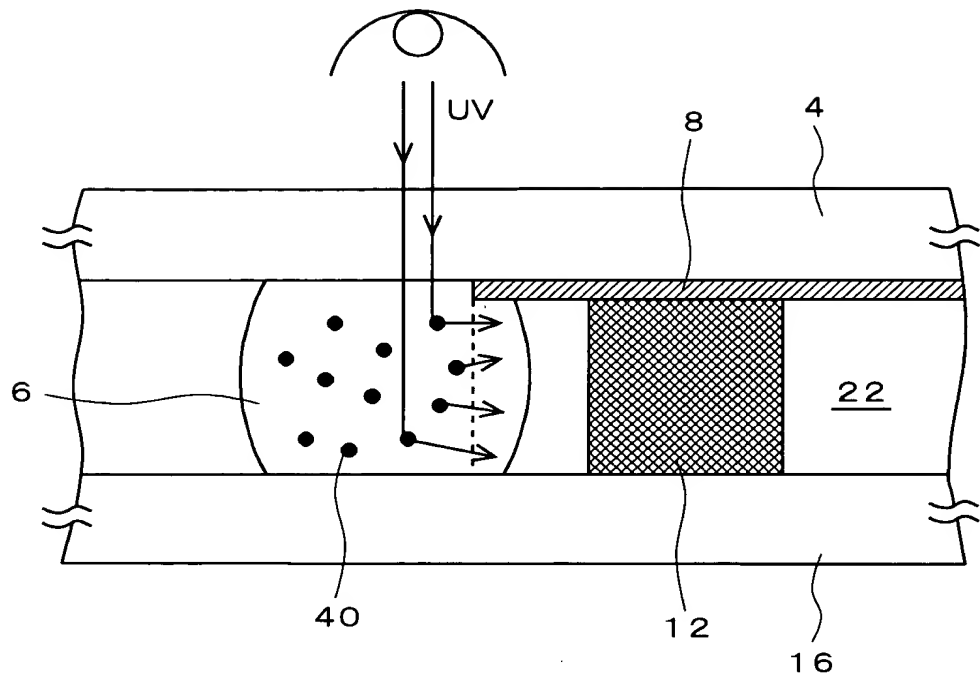


FIG. 22b

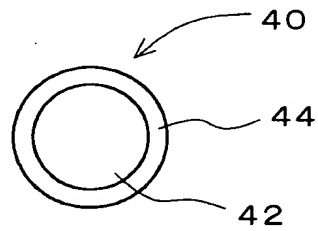


FIG. 23

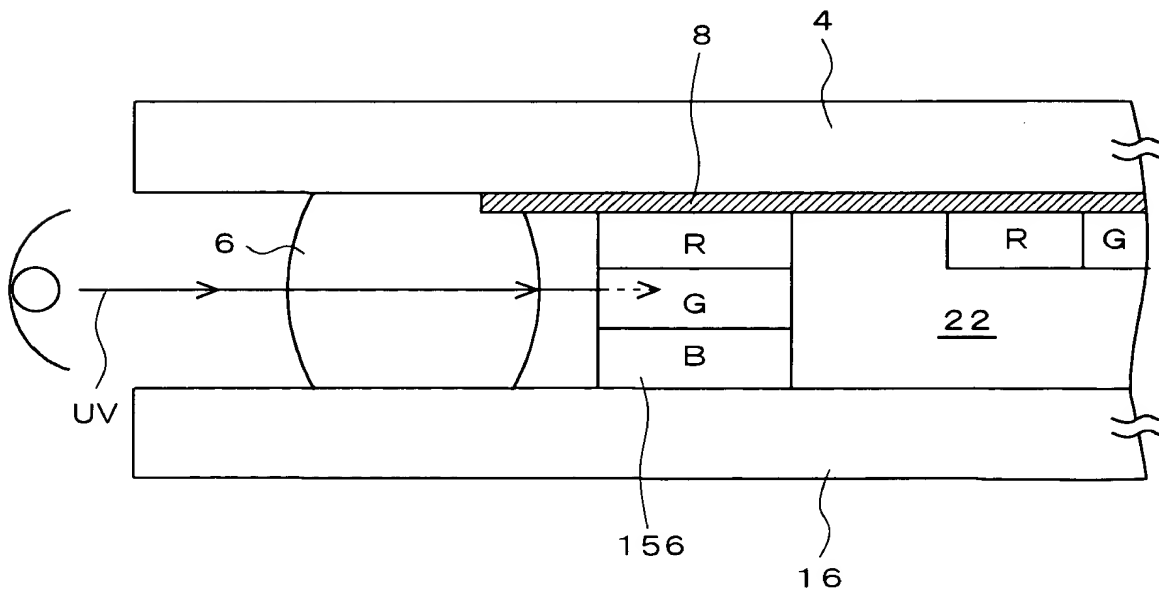


FIG. 24

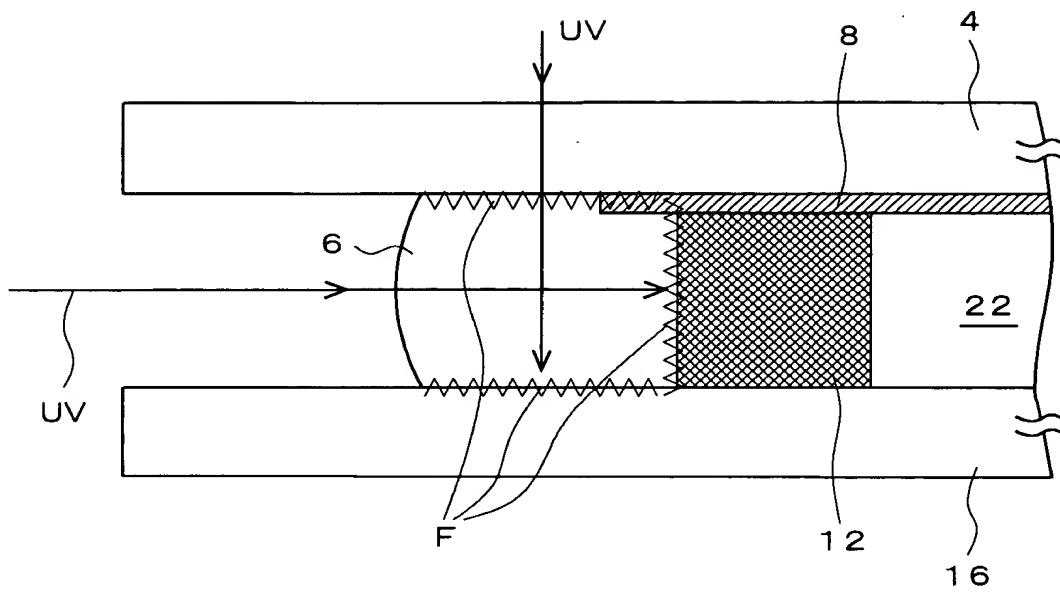


FIG. 25a

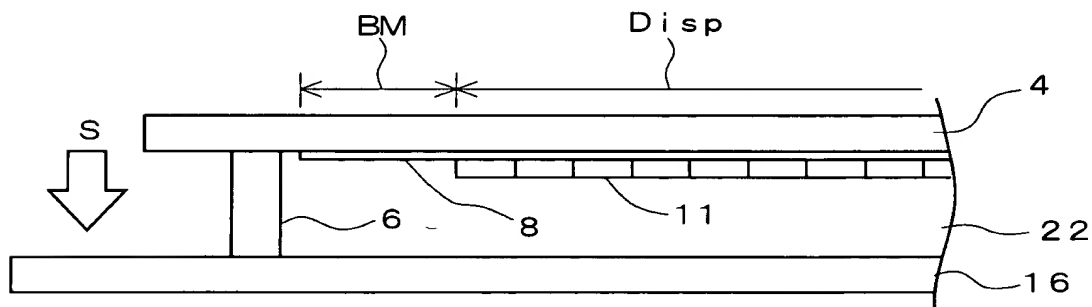


FIG. 25b

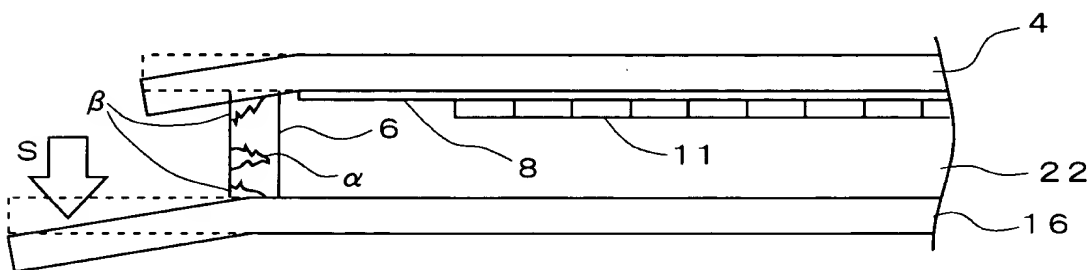


FIG. 26

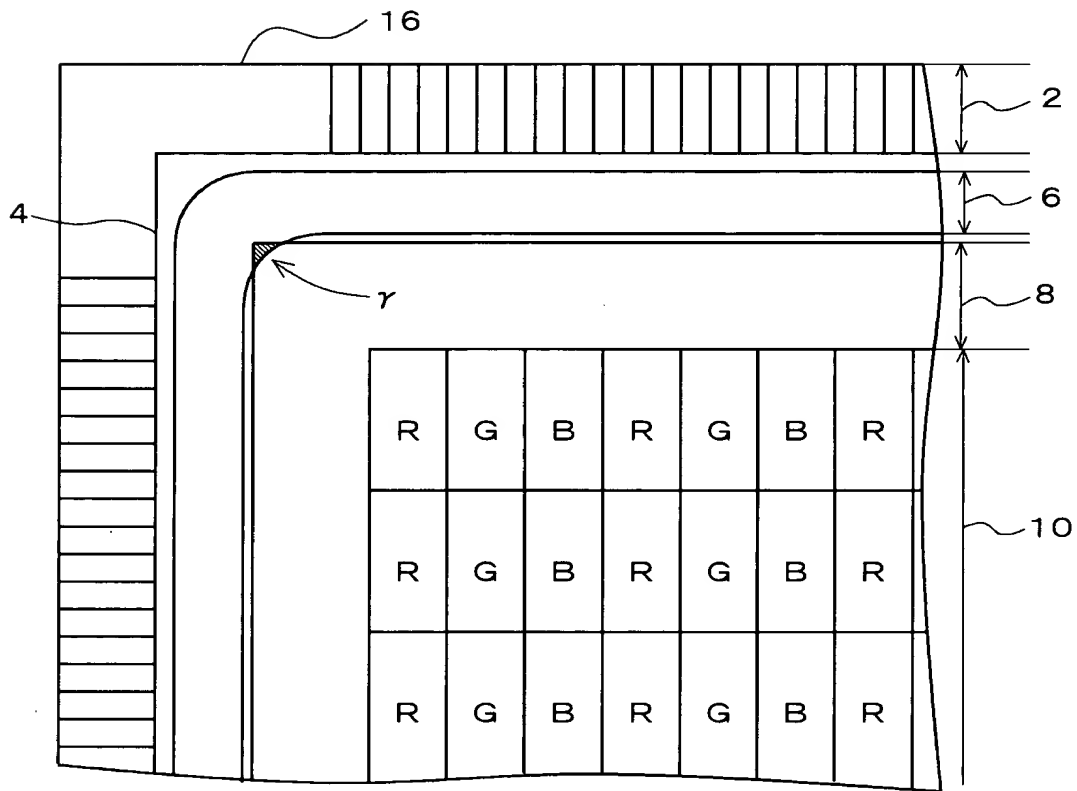


FIG. 27a

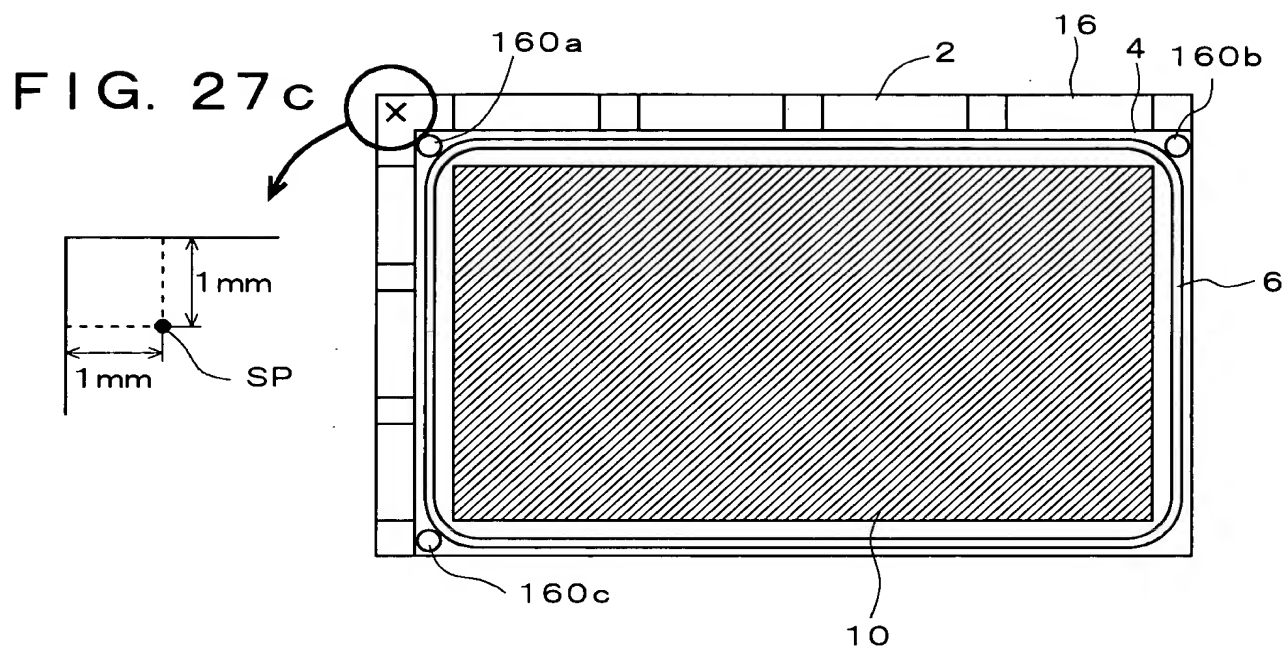


FIG. 27b

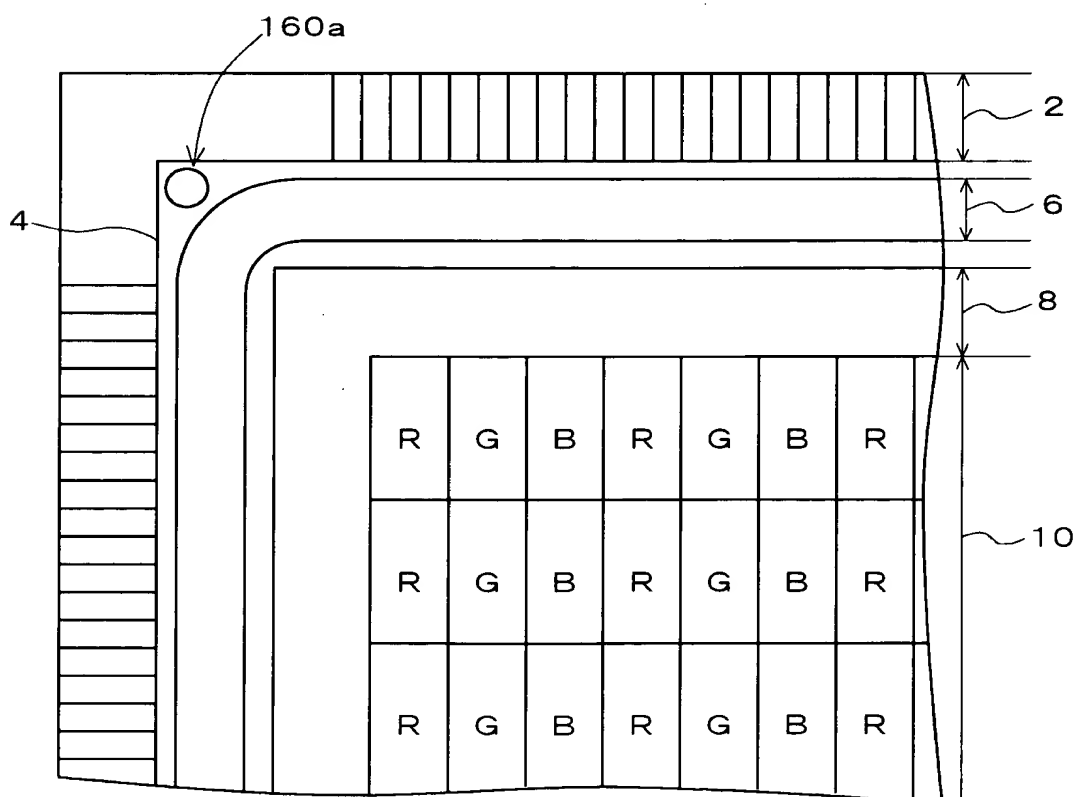


FIG. 28

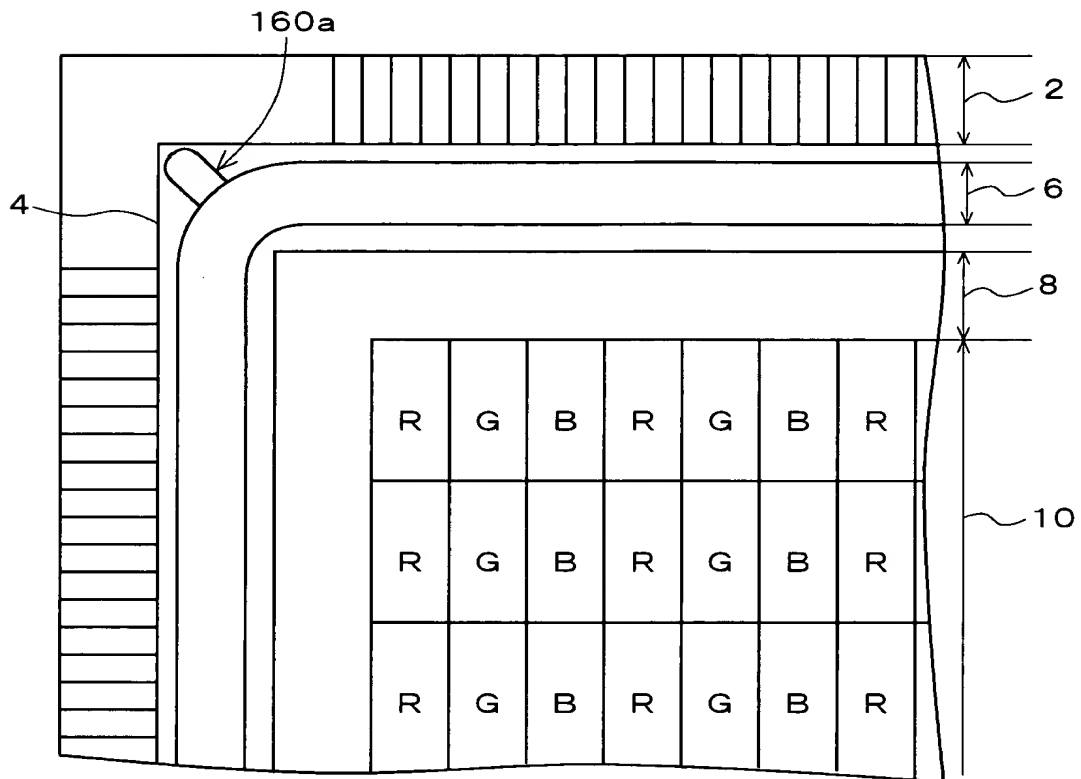


FIG. 29a

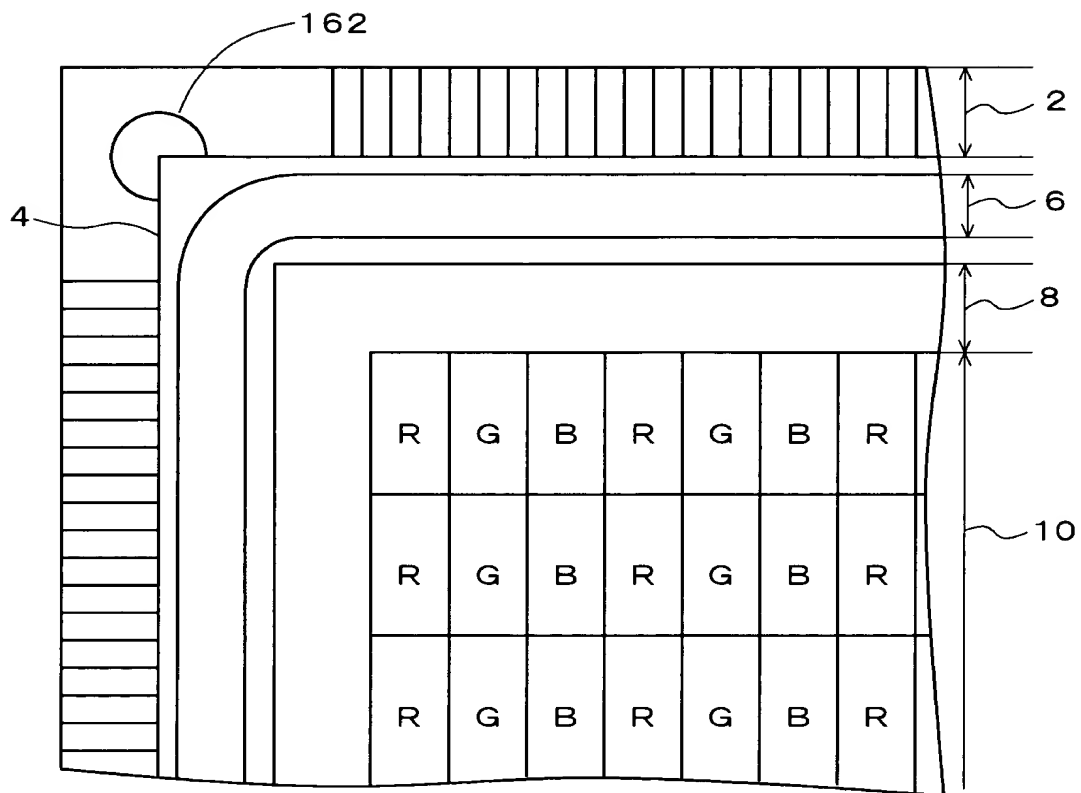


FIG. 29b

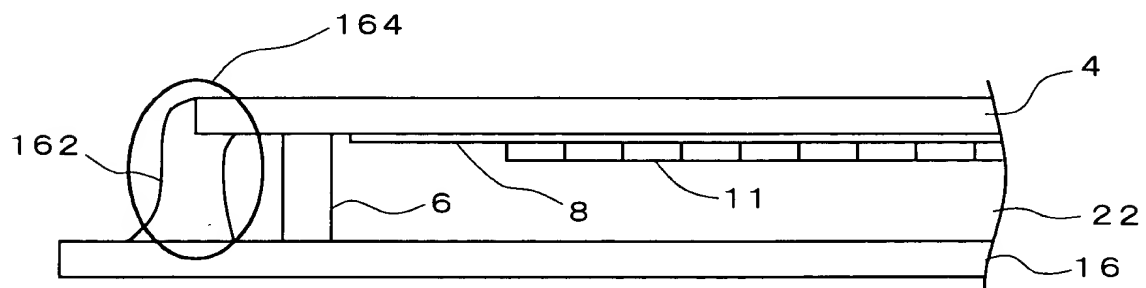


FIG. 30a

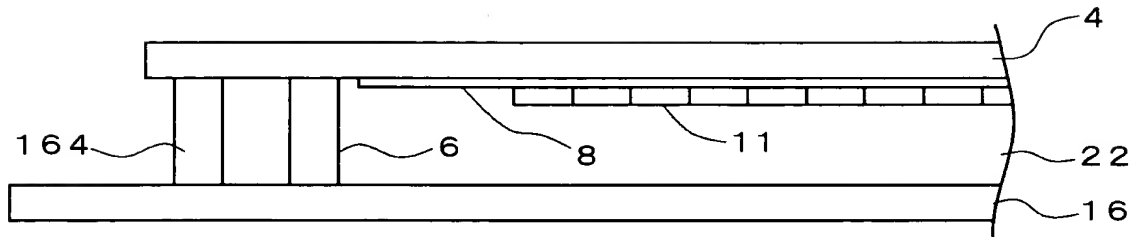


FIG. 30b

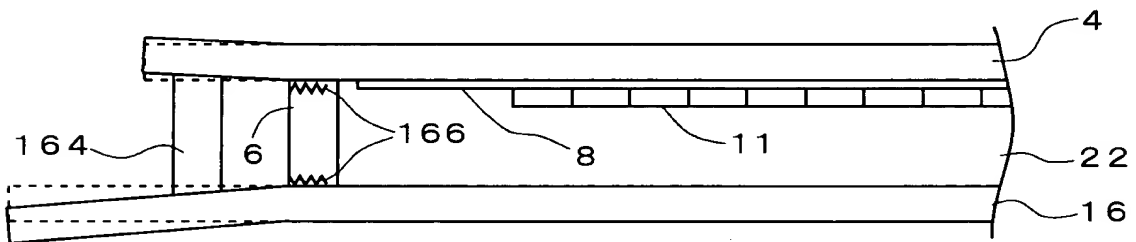


FIG. 31a

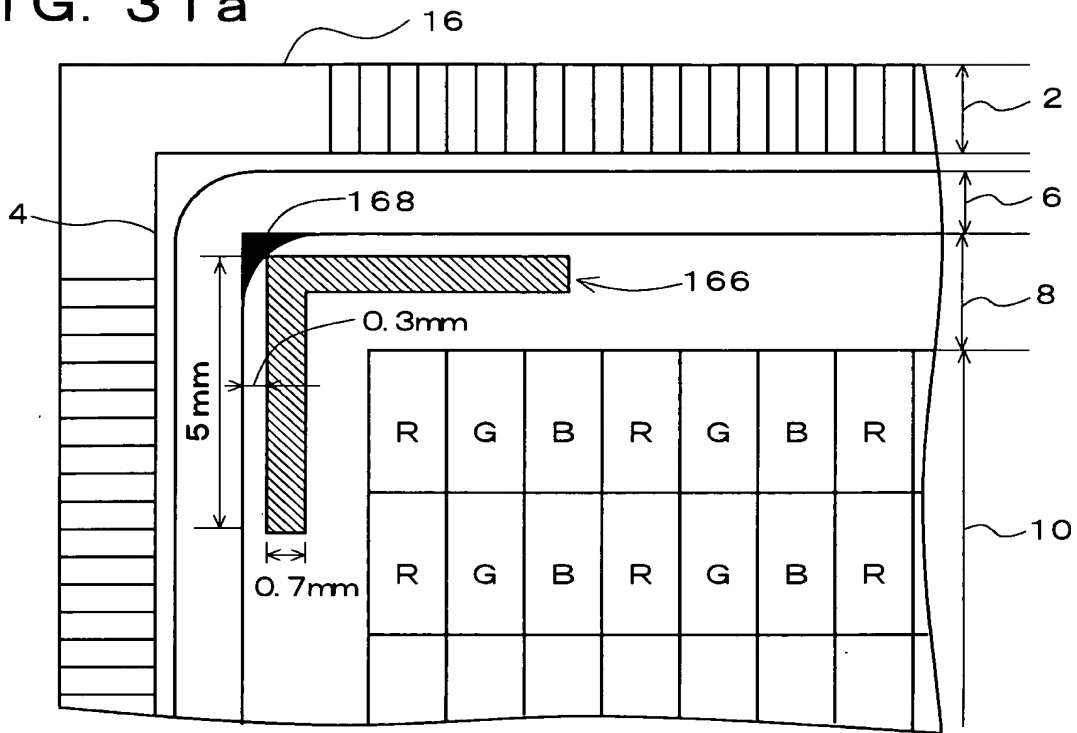


FIG. 31b

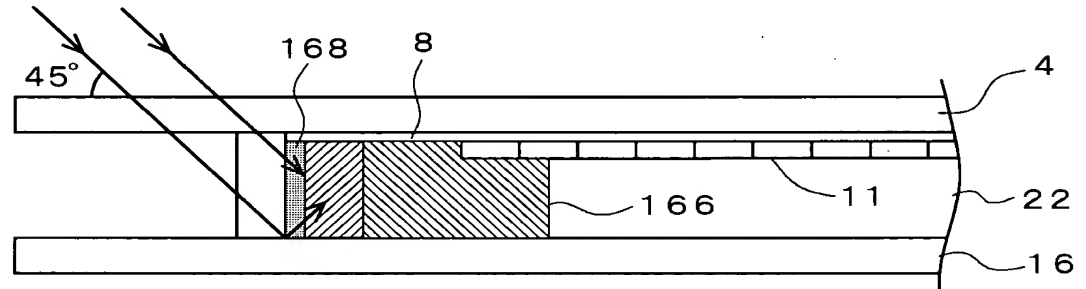
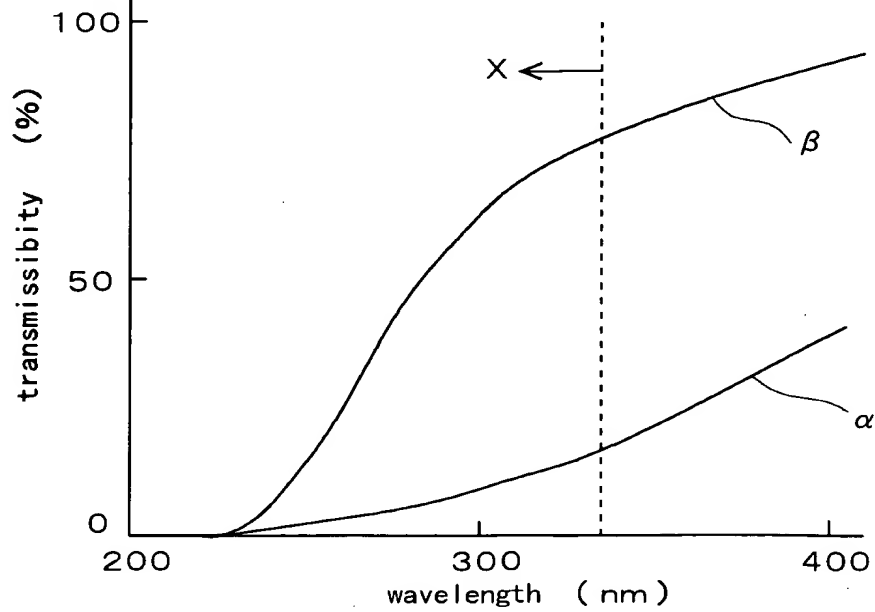


FIG. 31c



[illegible]

A cross-sectional view of a display device 1. The device consists of a top substrate 4, a middle layer 8, and a bottom substrate 16. A central region 170 is defined between the middle layer 8 and the bottom substrate 16. A vertical structure 6 is located on the left side of the central region 170. A horizontal structure 174 is located on the left side of the bottom substrate 16. A curved line 22 is shown on the right side of the central region 170. A label G1 is located on the right side of the bottom substrate 16. The device is shown in a cross-sectional view with break lines on the left and right sides.

FIG. 33a

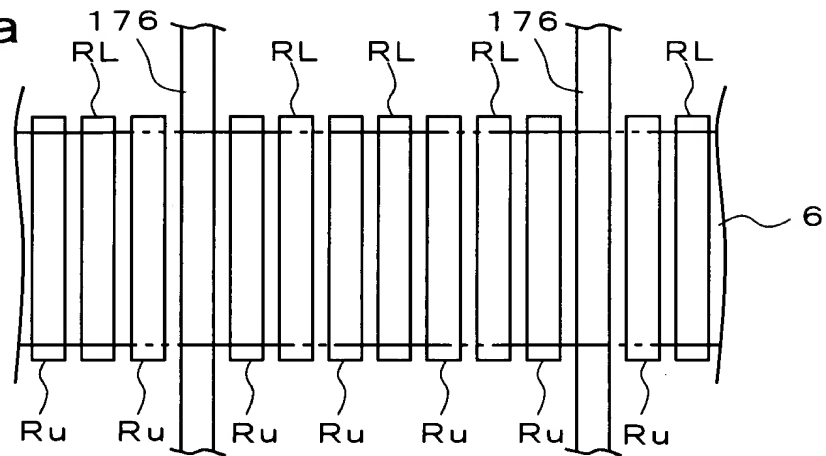


FIG. 33b

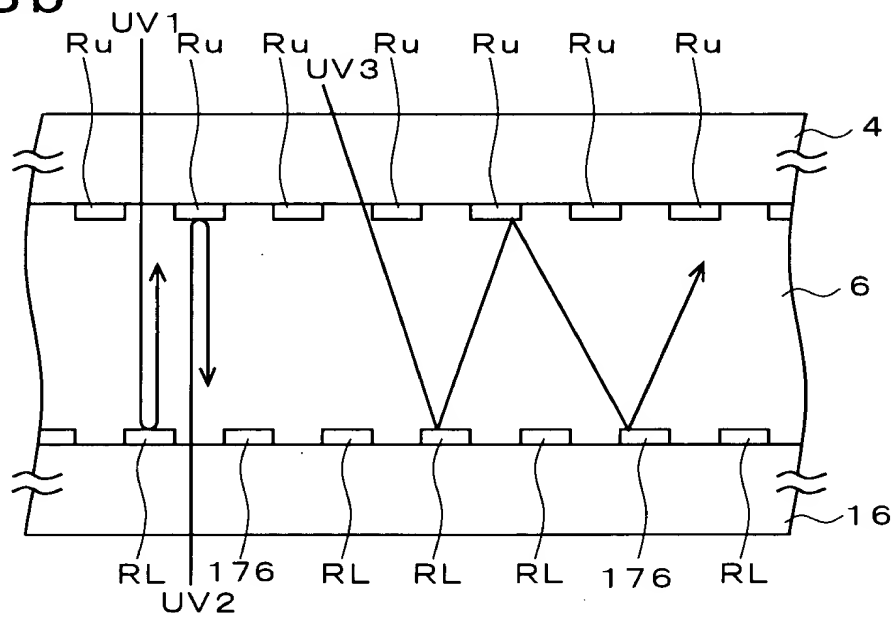


FIG. 33c

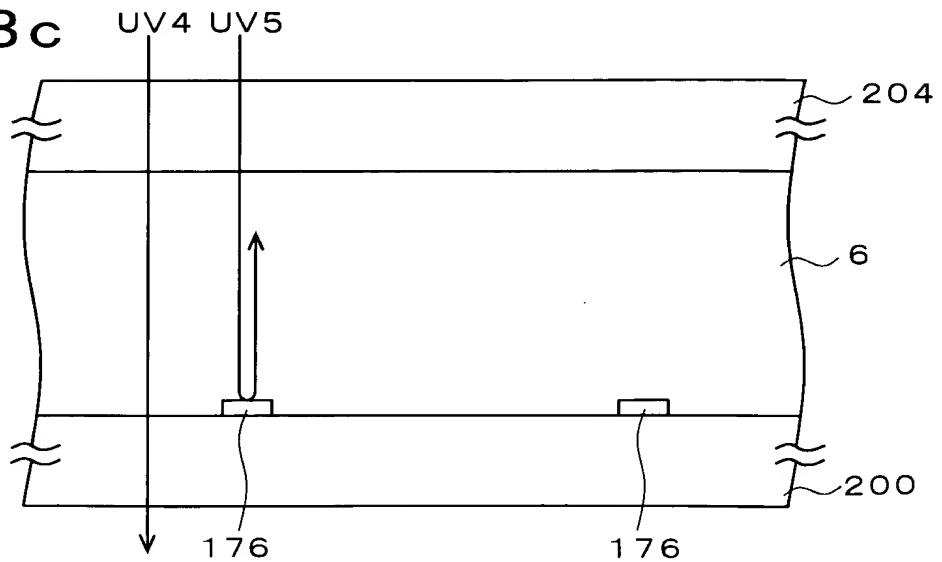


FIG. 34

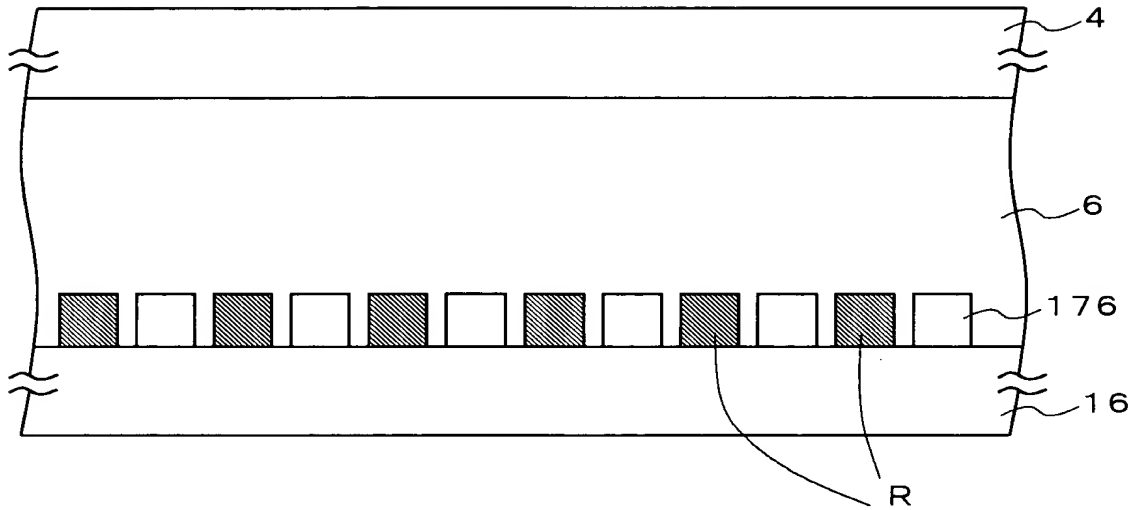


FIG. 35

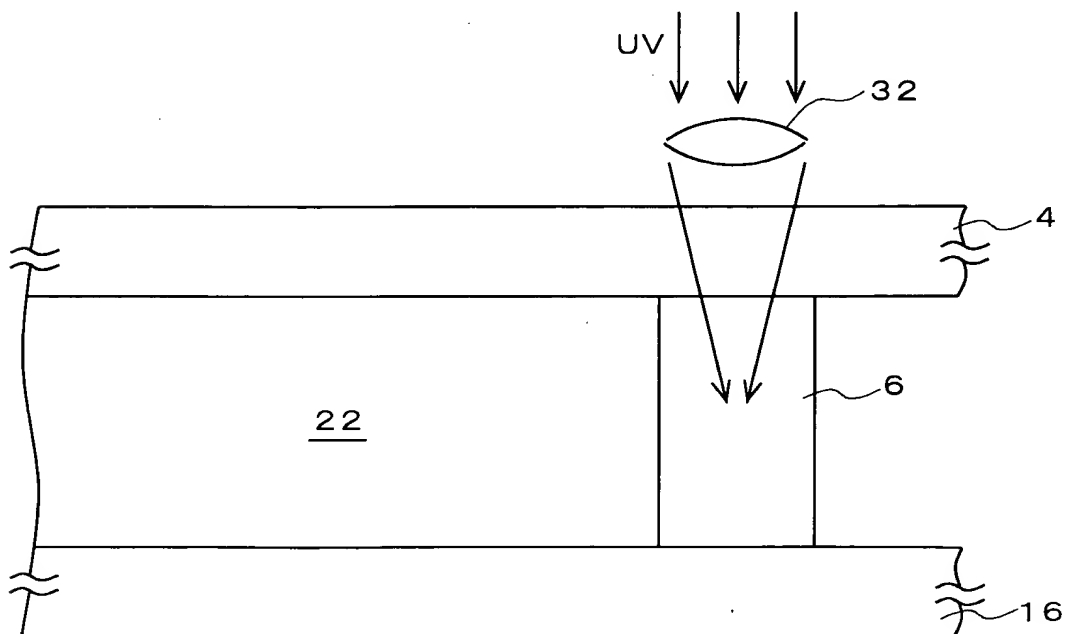


FIG. 36

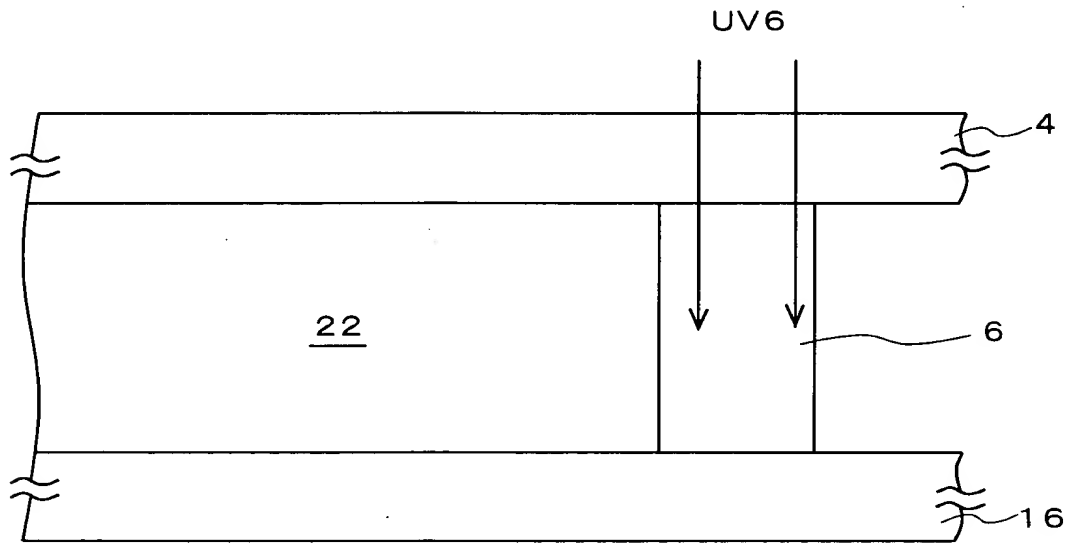


FIG. 37

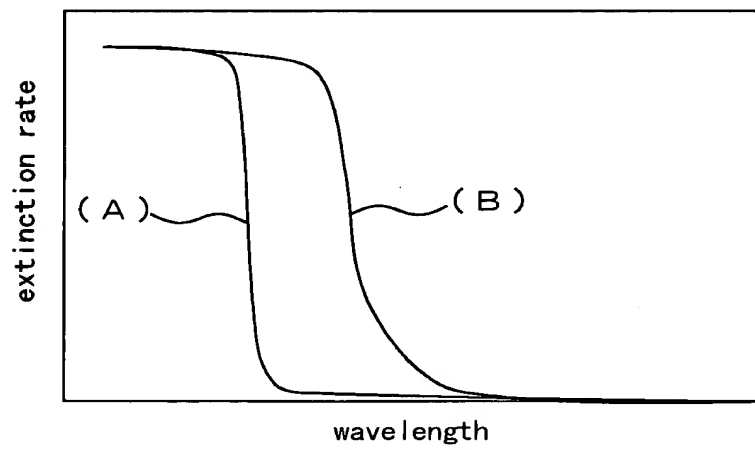


FIG. 38a

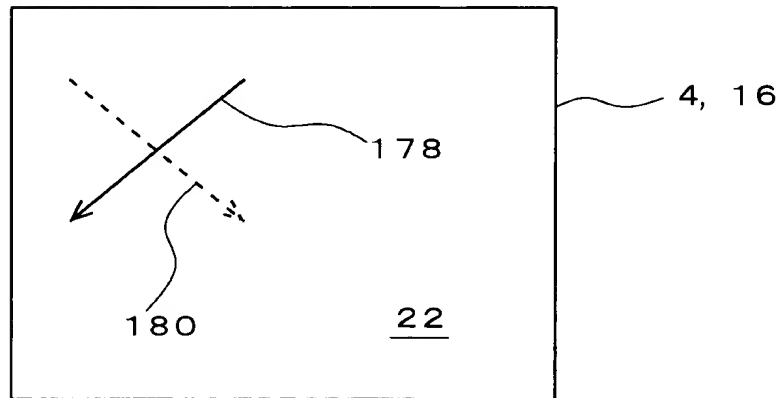


FIG. 38b

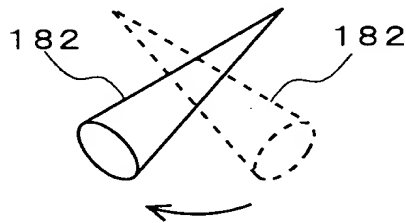


FIG. 38c

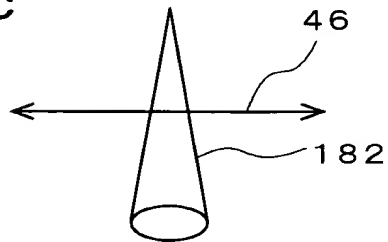


FIG. 39a

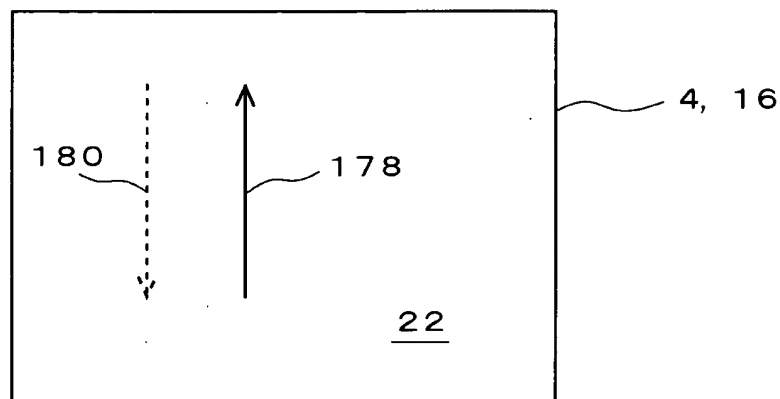


FIG. 39b

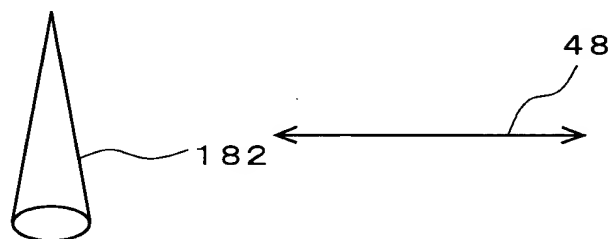


FIG. 40

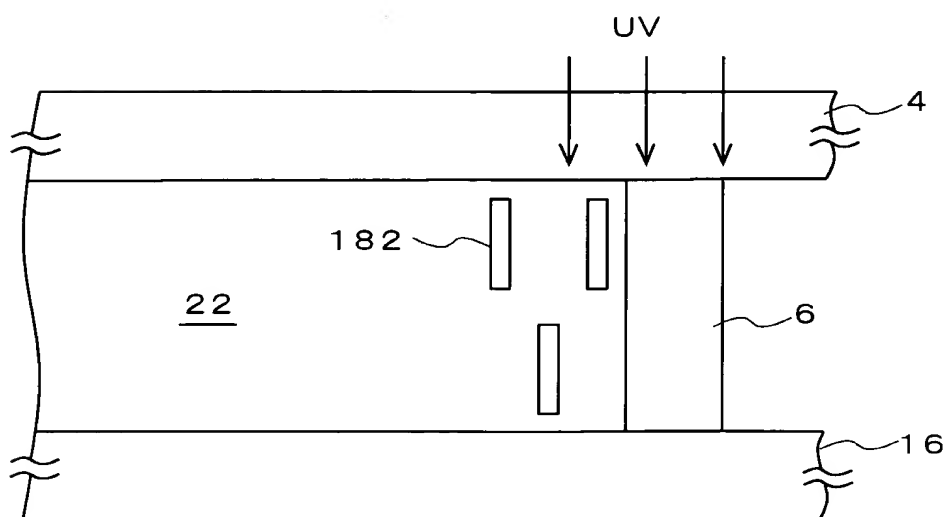


FIG. 41a

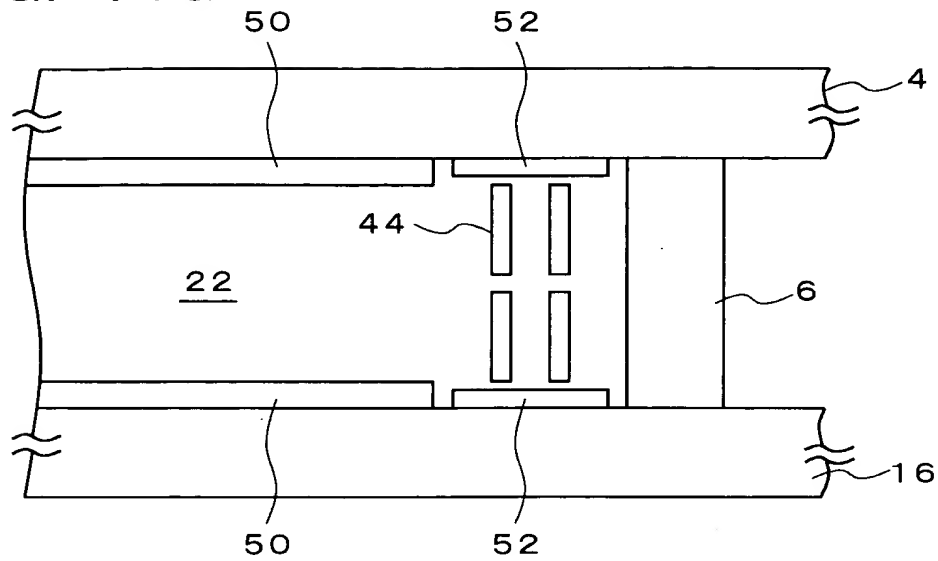


FIG. 41b

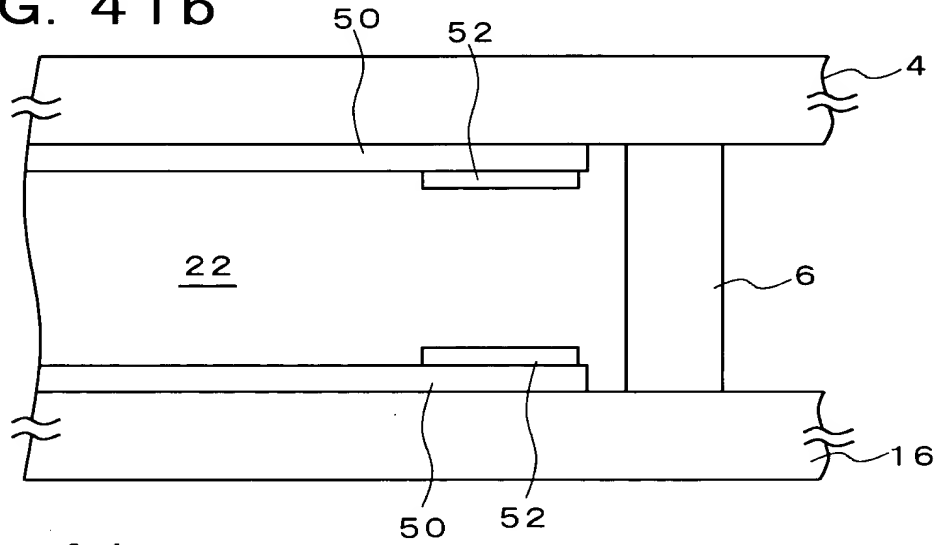


FIG. 41c

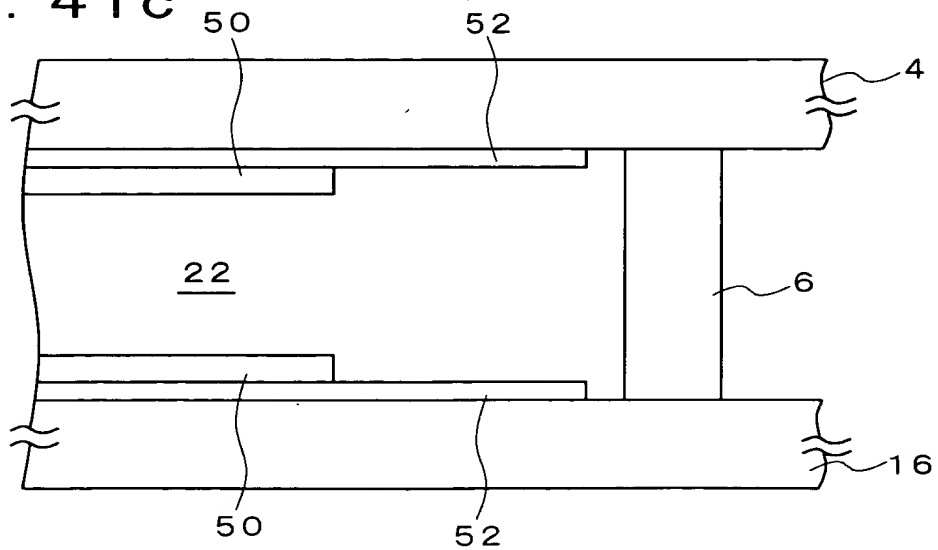


FIG. 42a

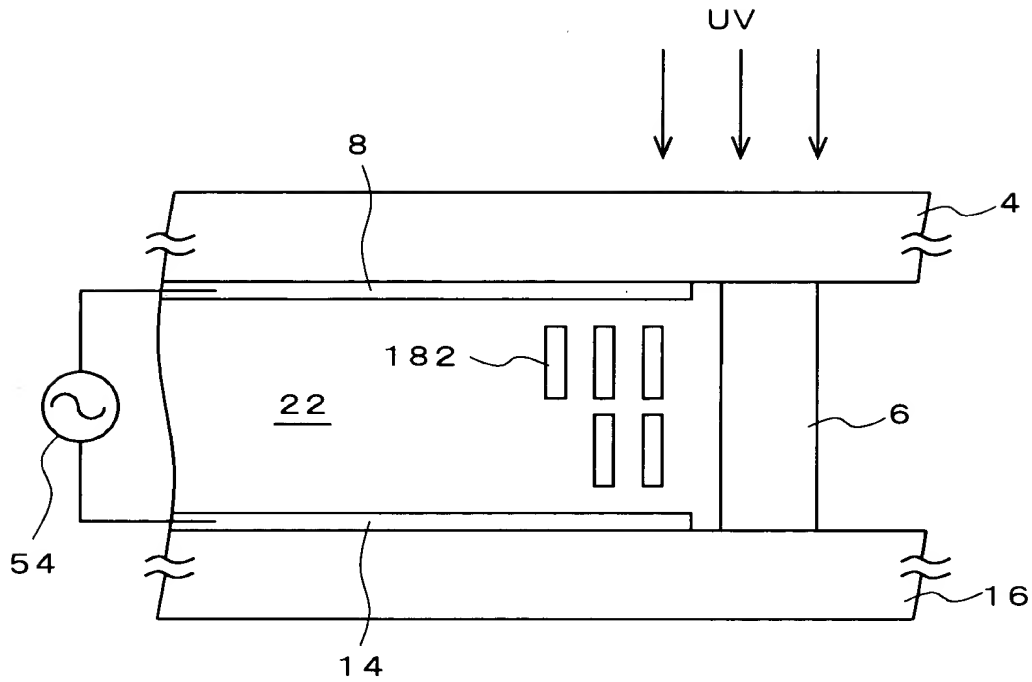


FIG. 42b

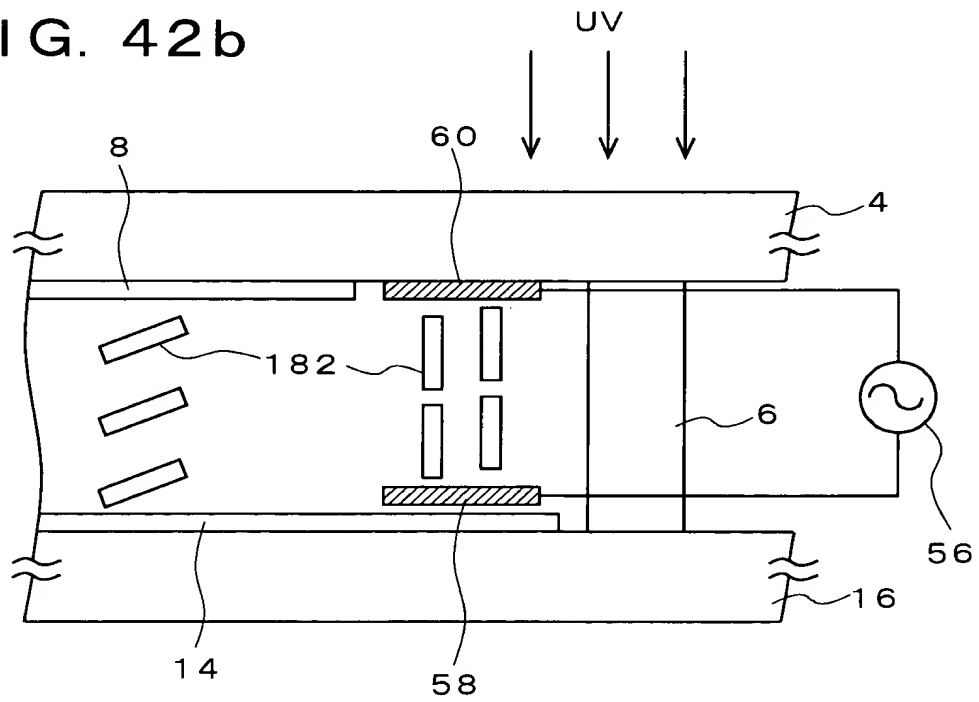


FIG. 43a

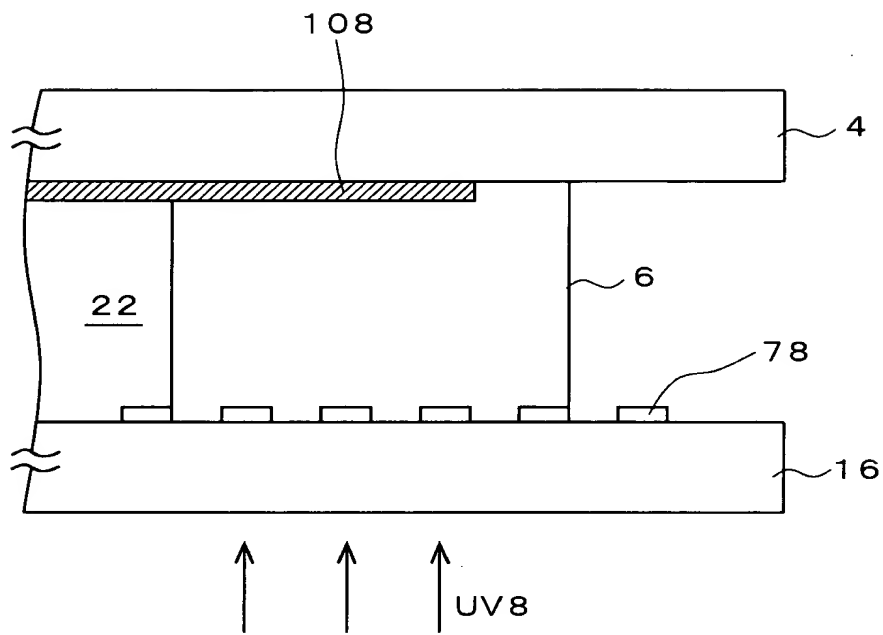


FIG. 43b

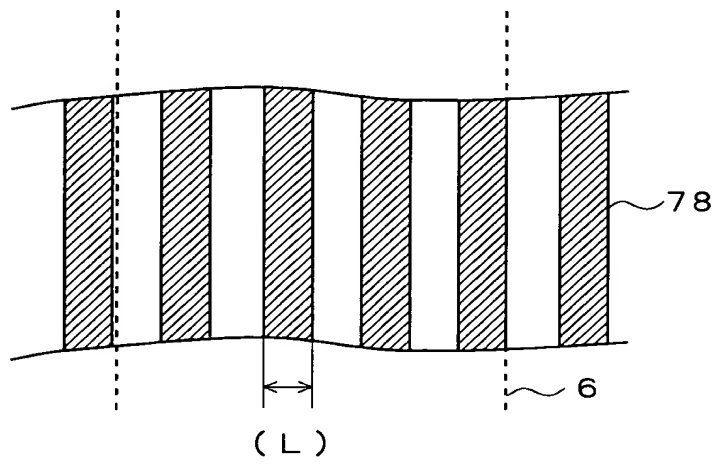


FIG. 44

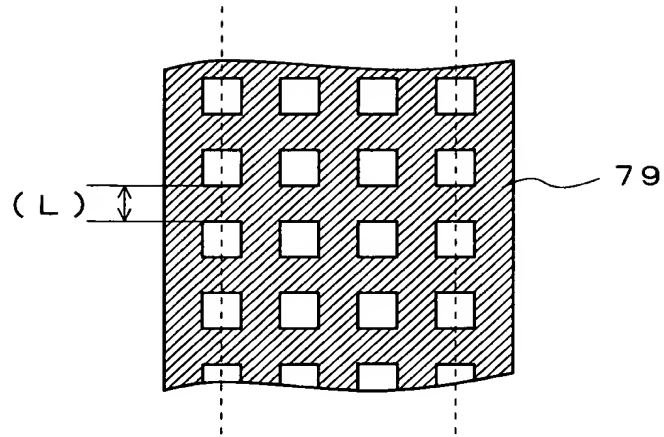


FIG. 45

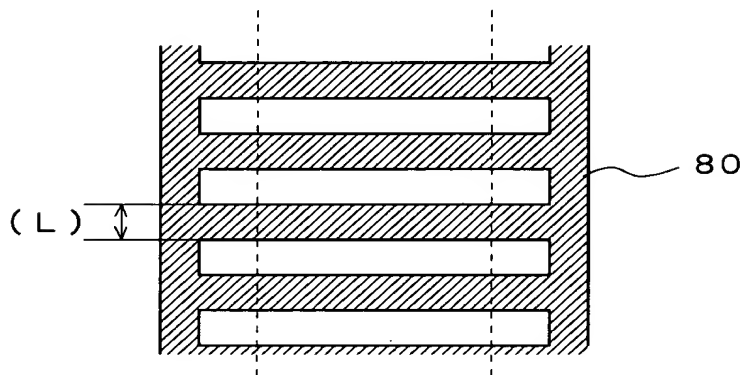


FIG. 46

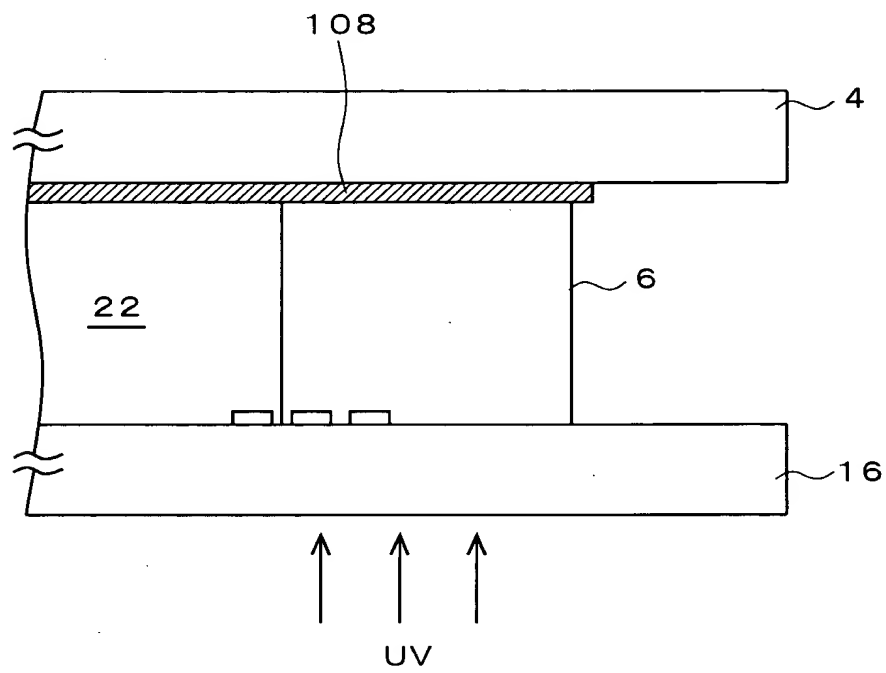


FIG. 47a

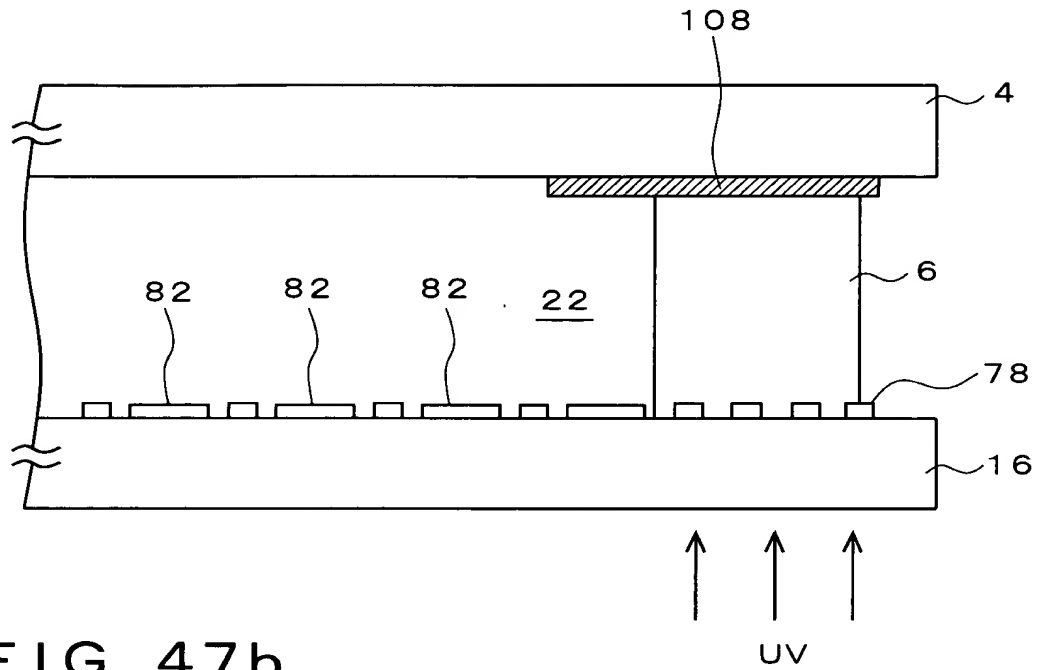


FIG. 47b

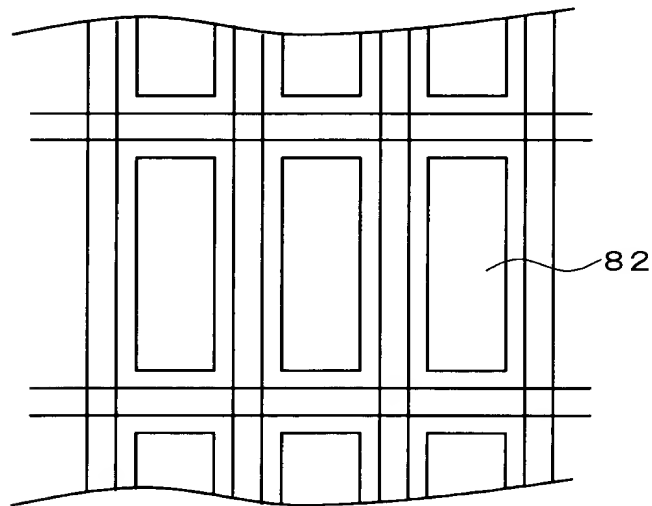


FIG. 48

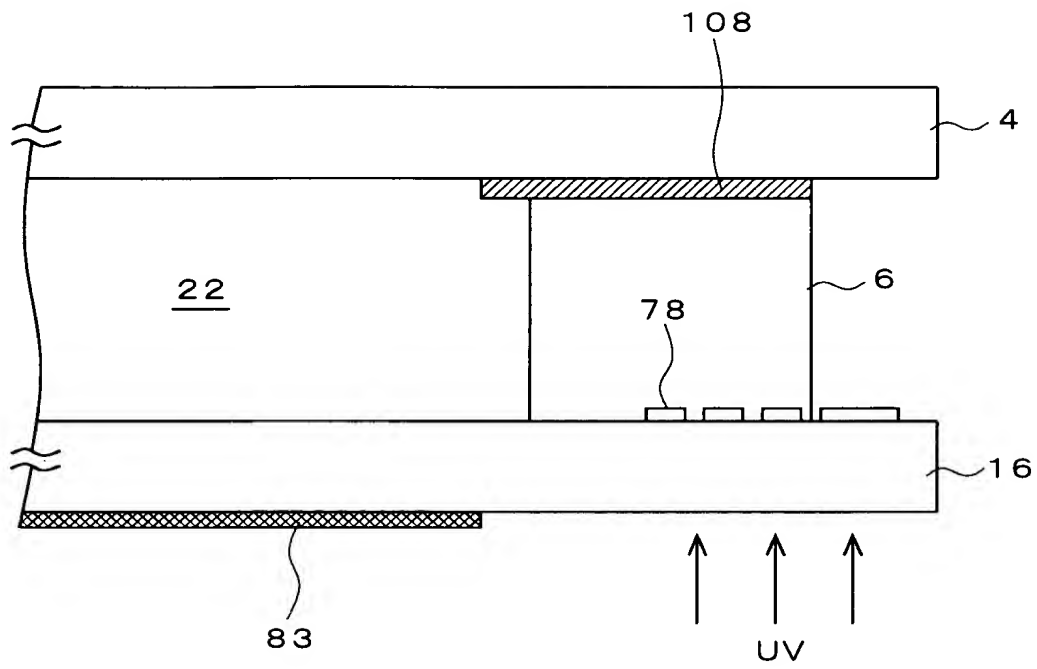


FIG. 49

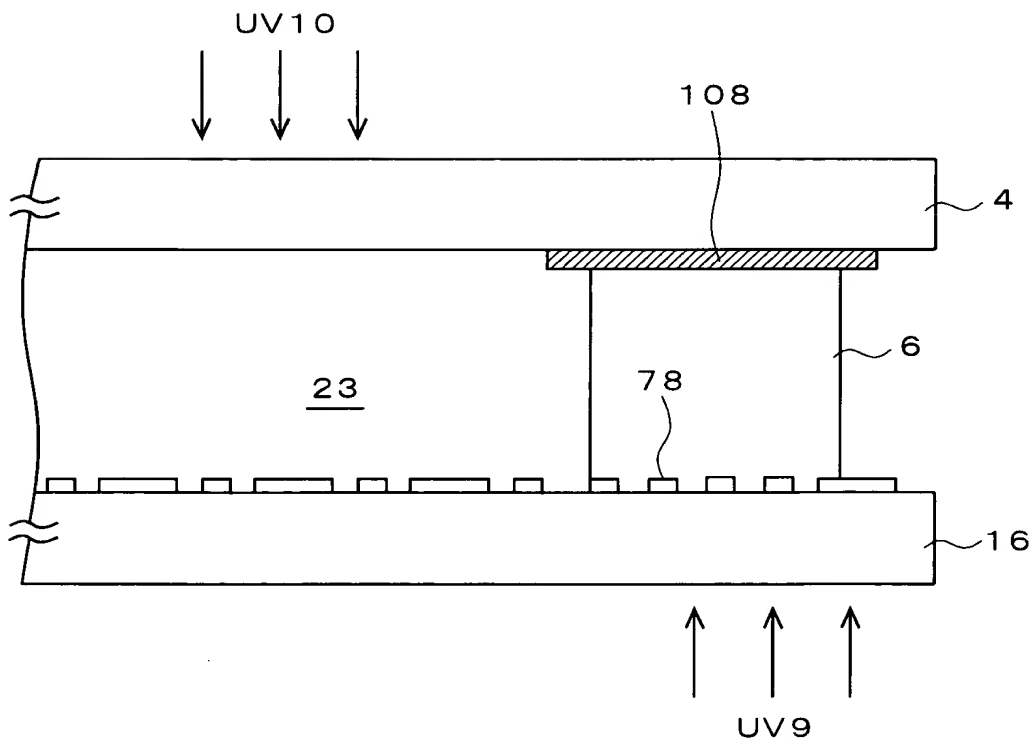


FIG. 50

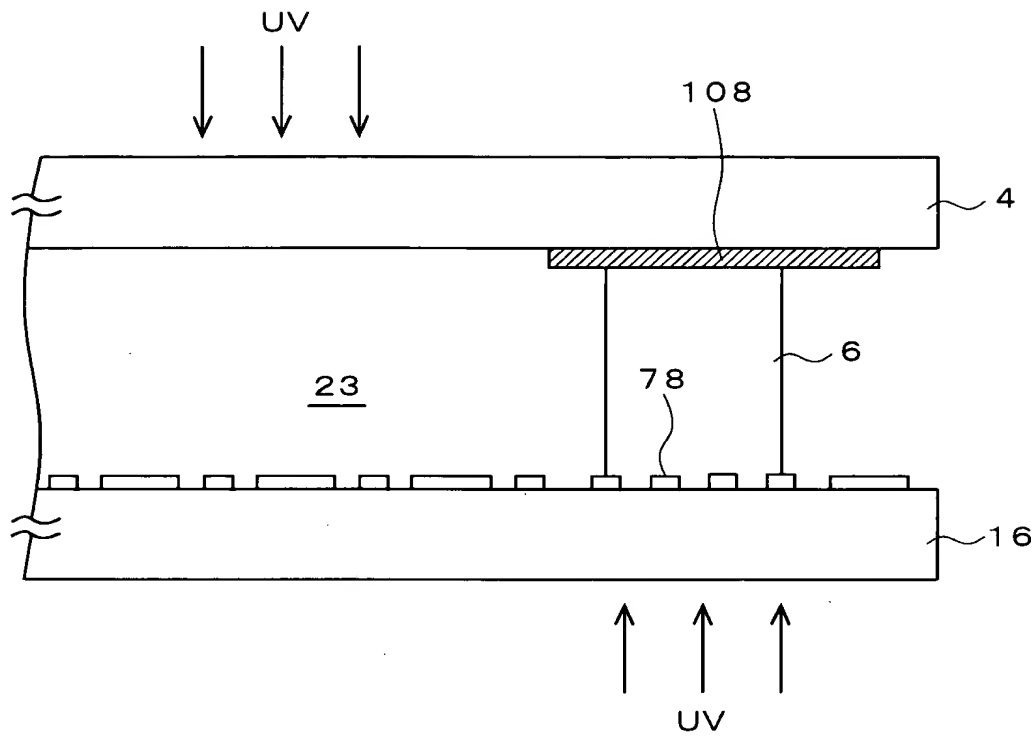


FIG. 51

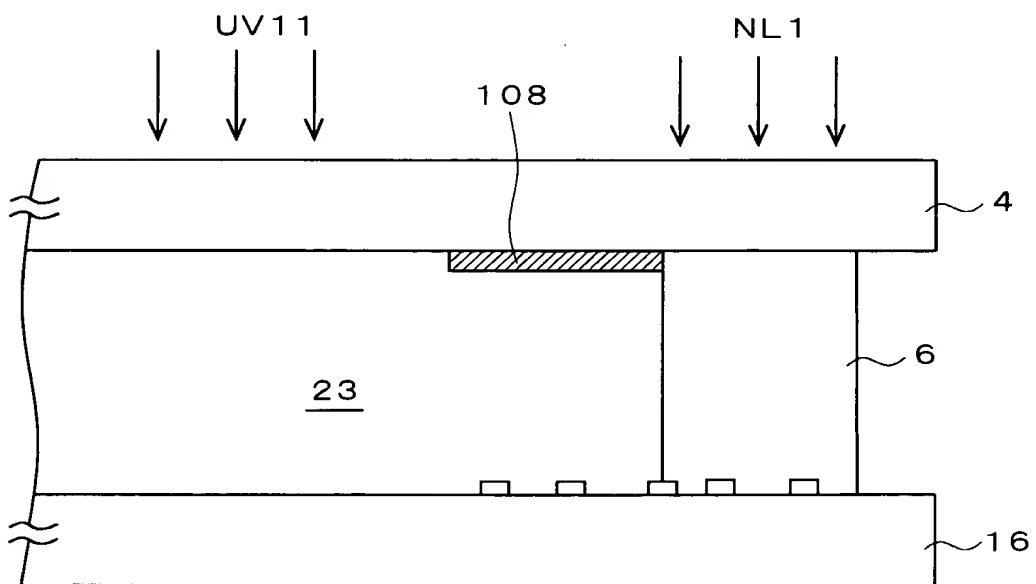


FIG. 52

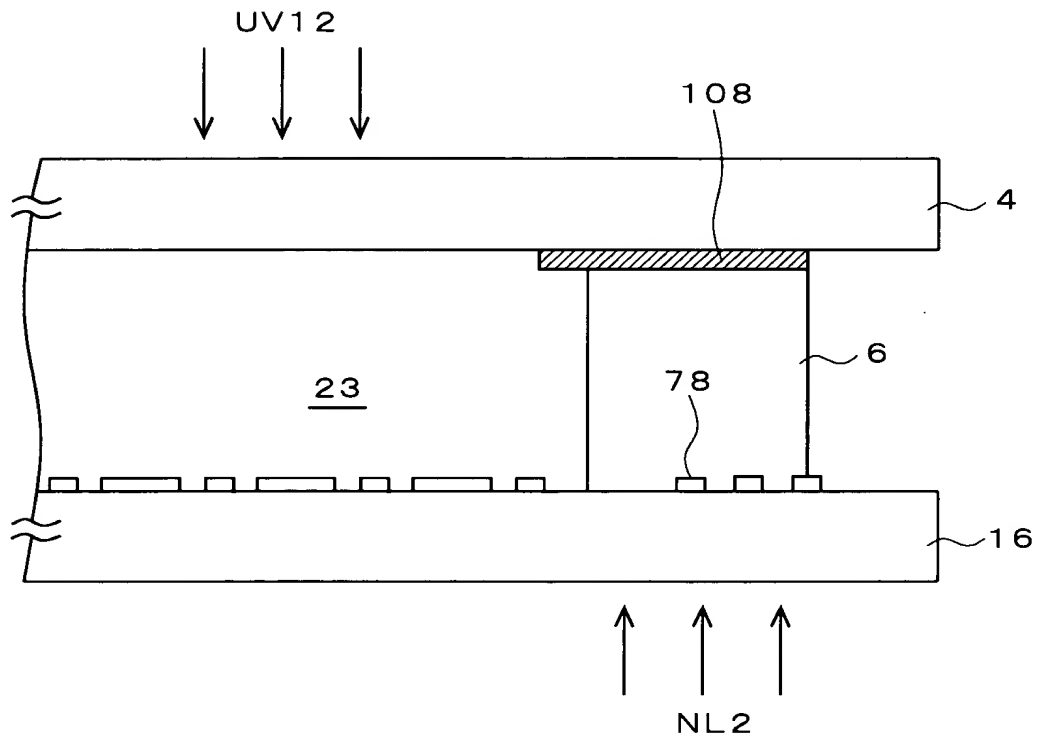


FIG. 53

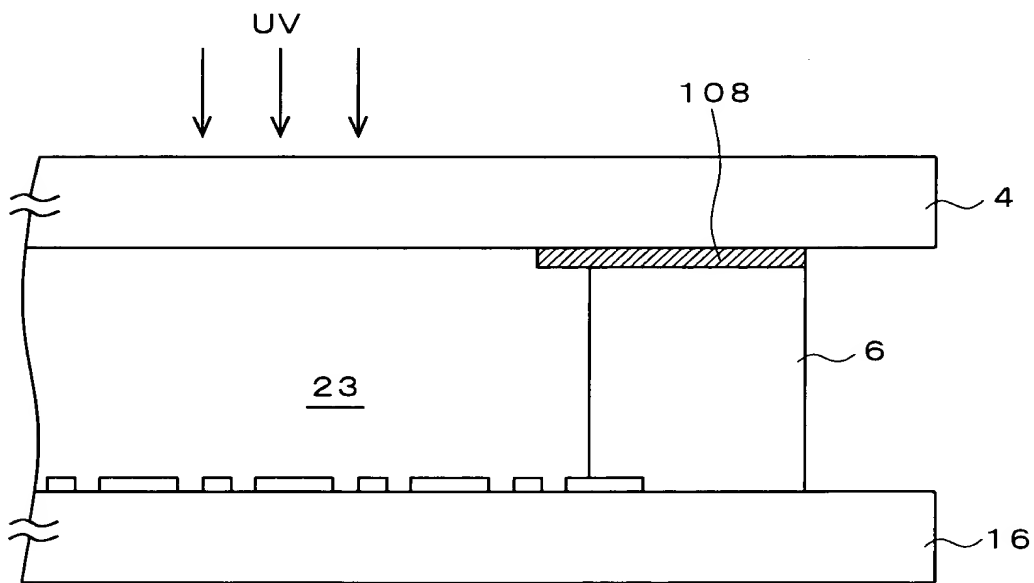


FIG. 54

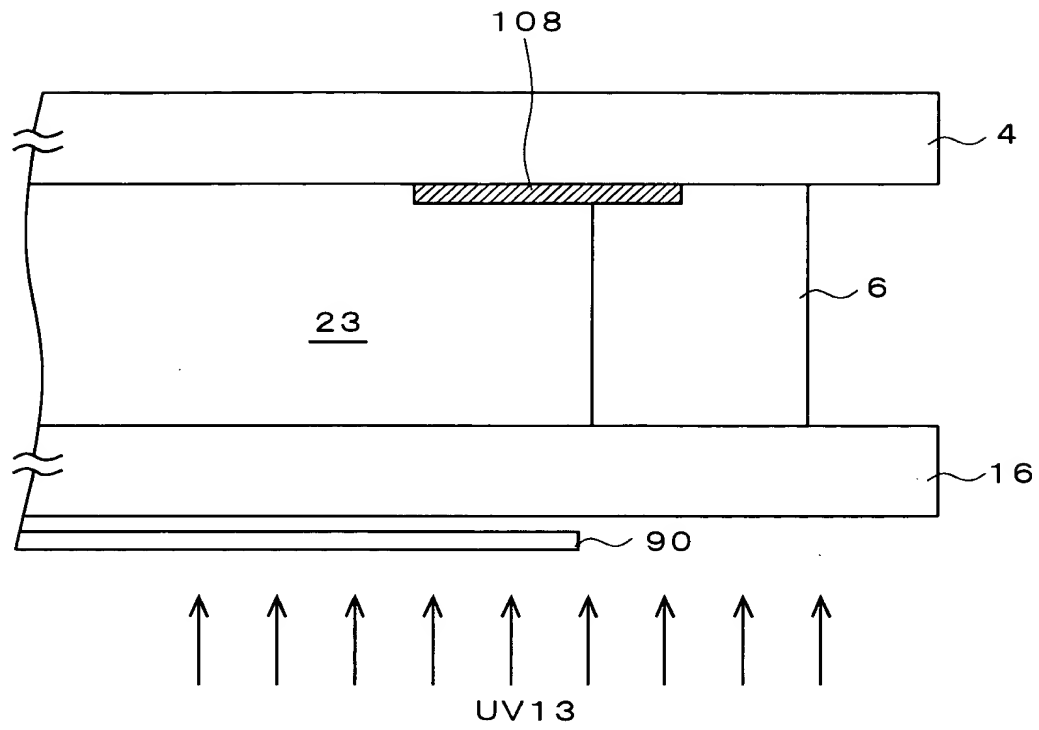


FIG. 55

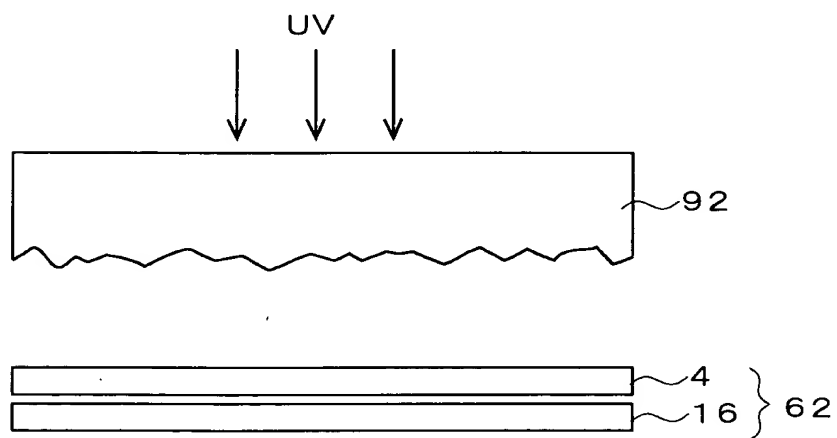


FIG. 56a

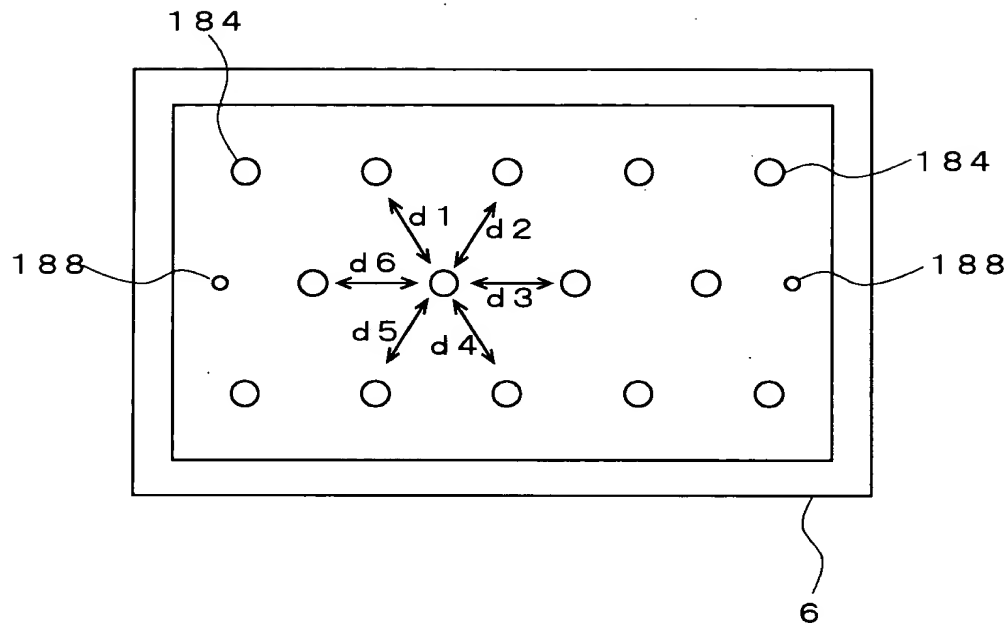


FIG. 56b

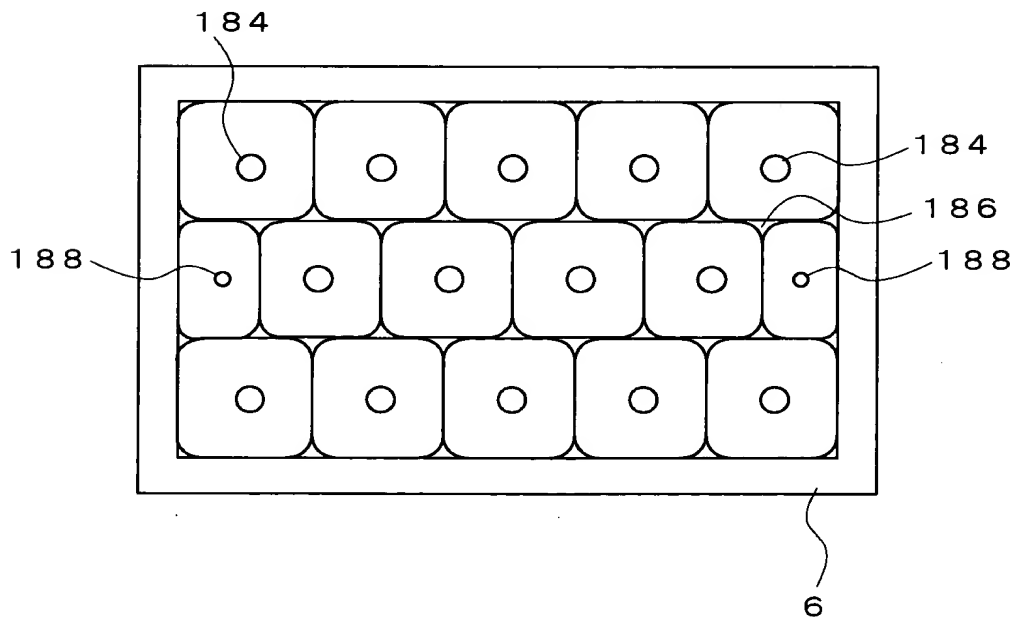


FIG. 57

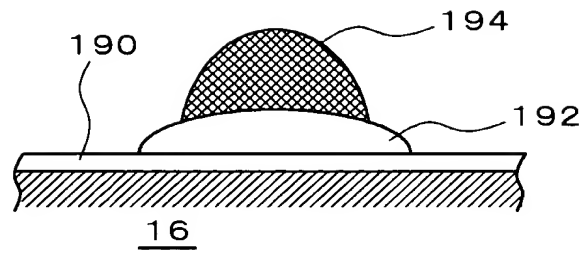


FIG. 58

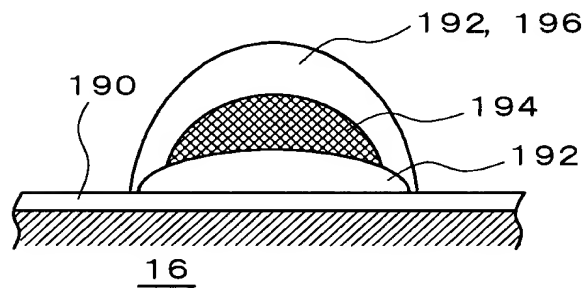


FIG. 59

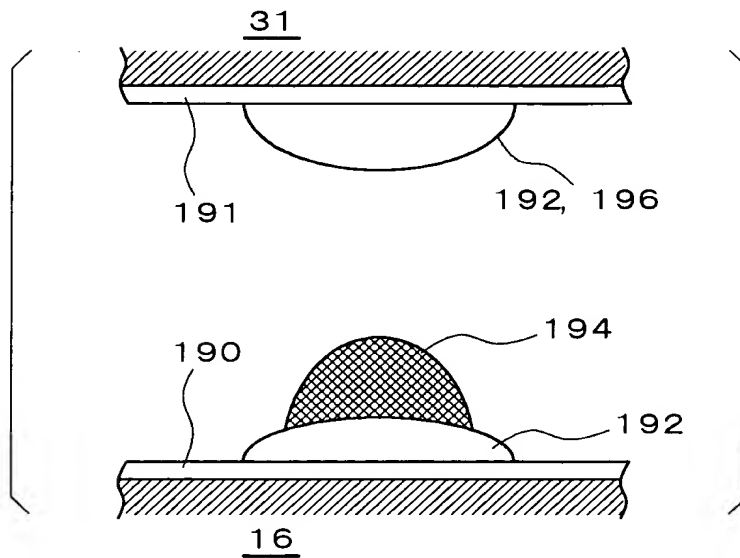


FIG. 60

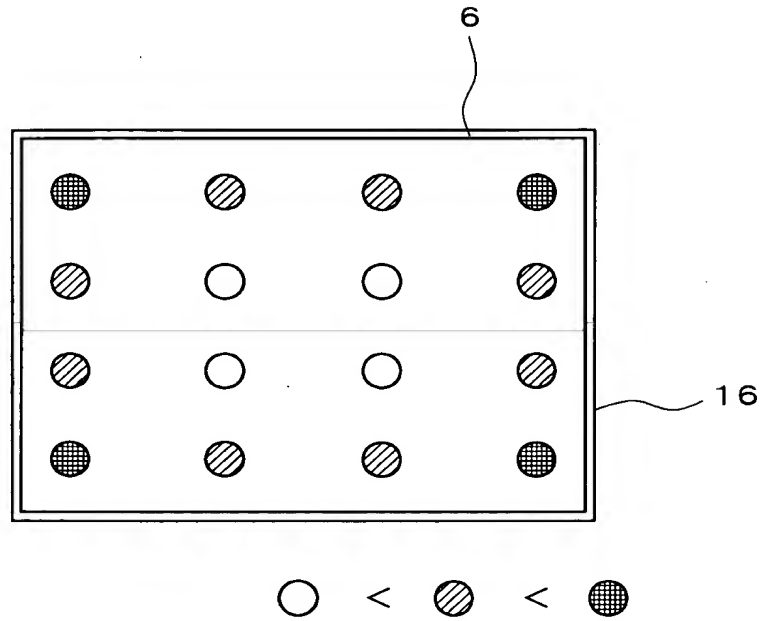


FIG. 61

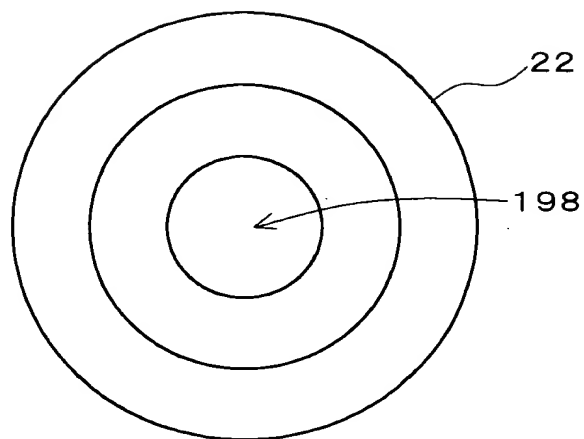


FIG. 62

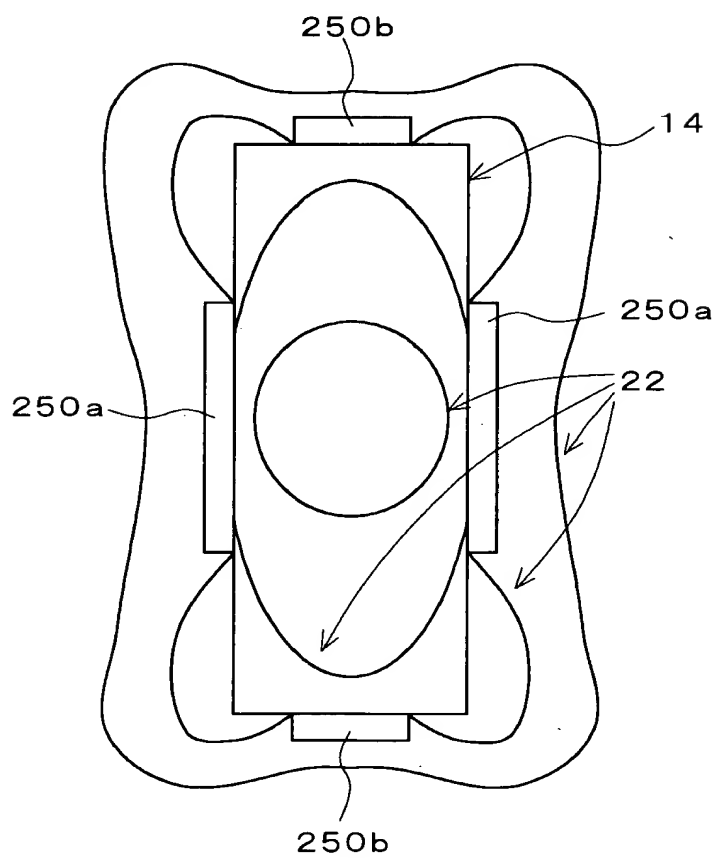


FIG. 63

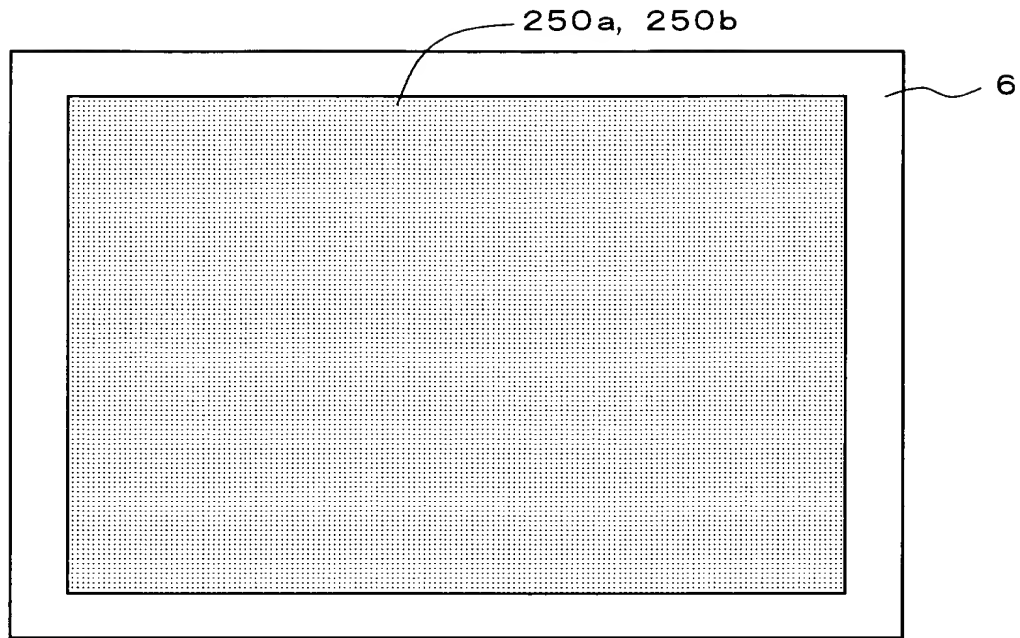


FIG. 64

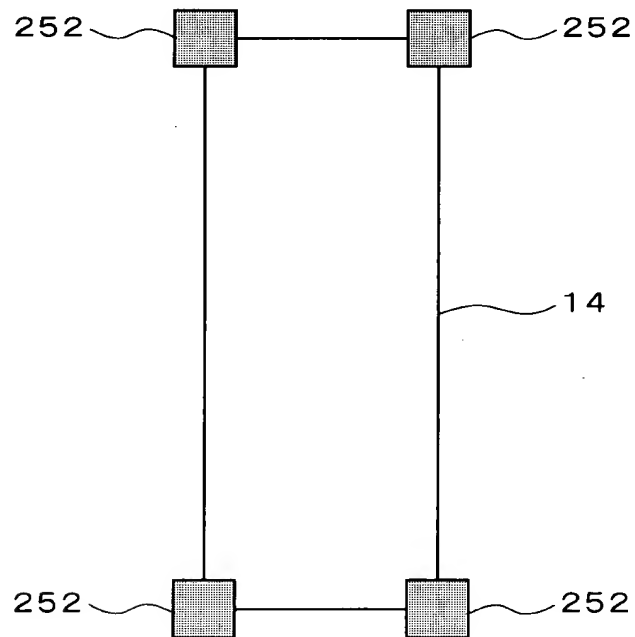


FIG. 65

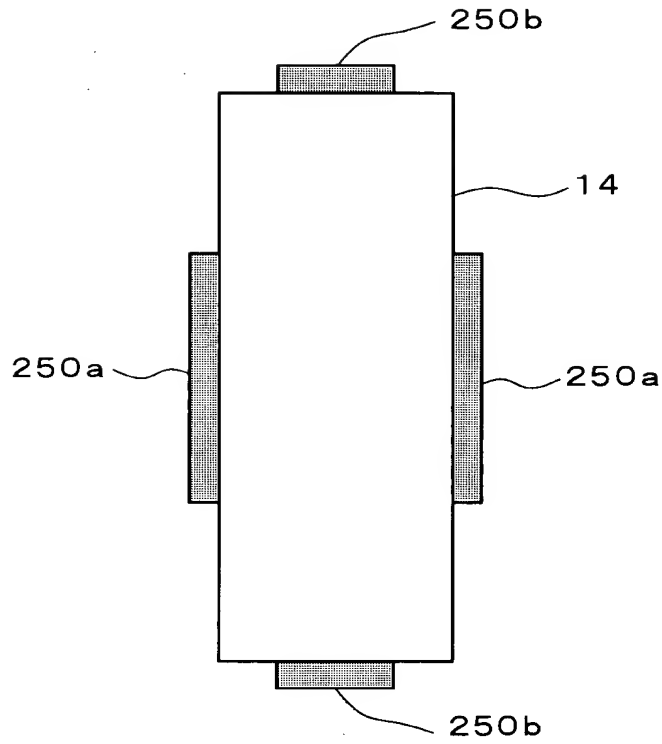


FIG. 66

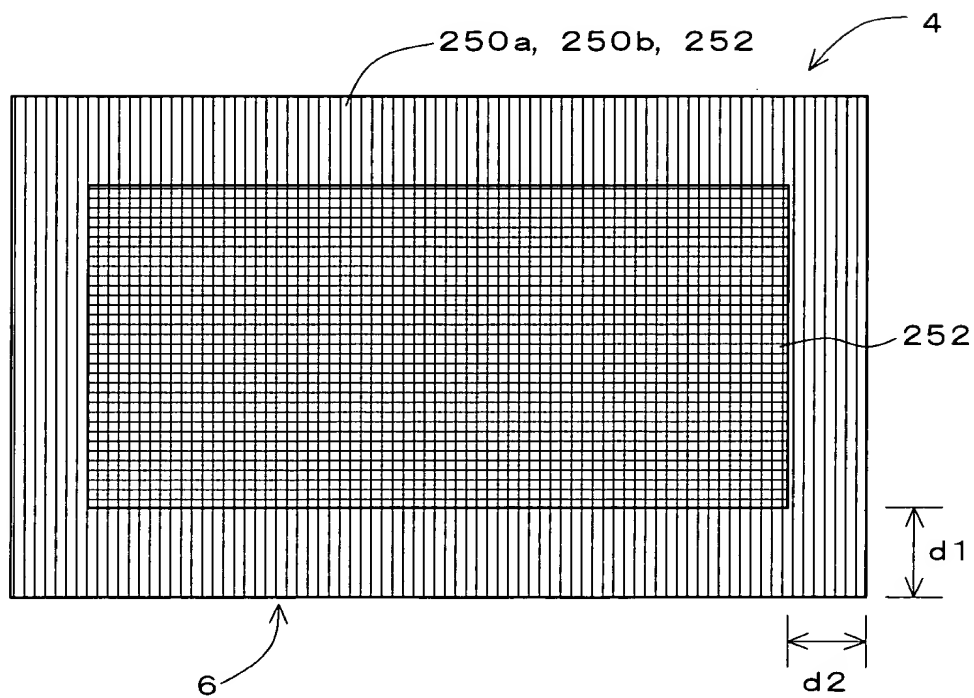


FIG. 67a

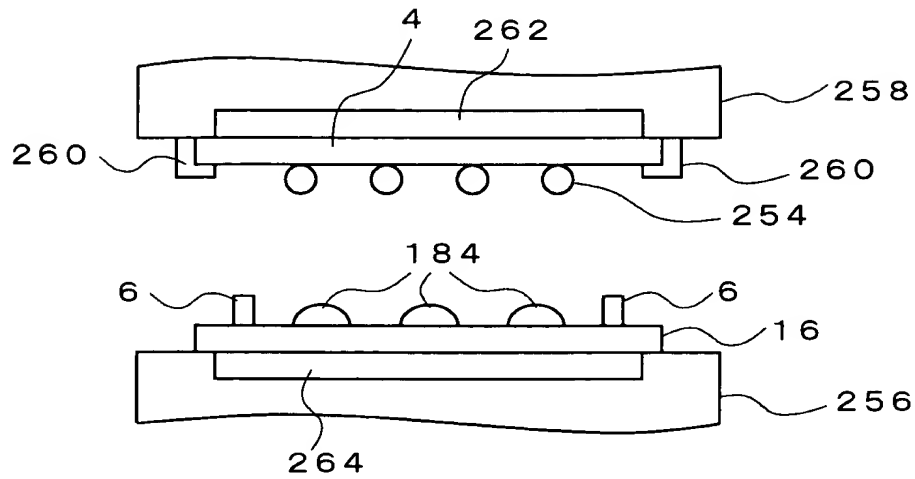


FIG. 67b

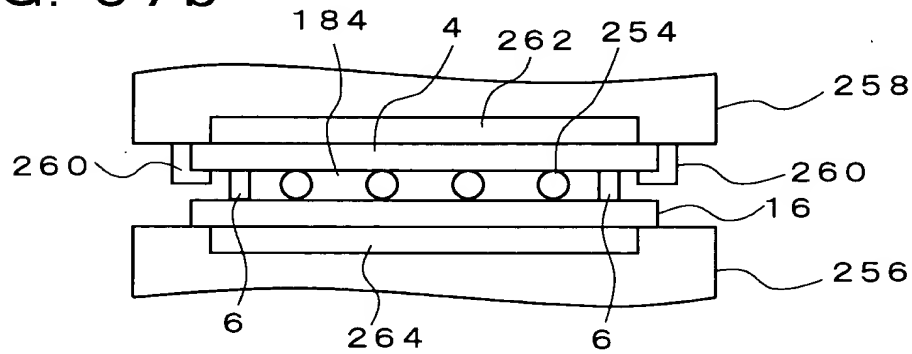


FIG. 67c

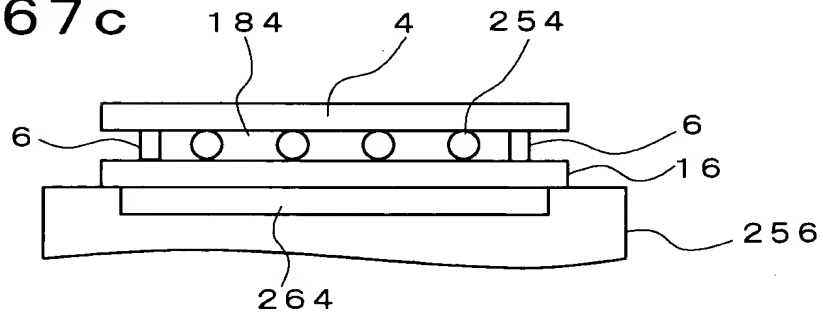


FIG. 67d

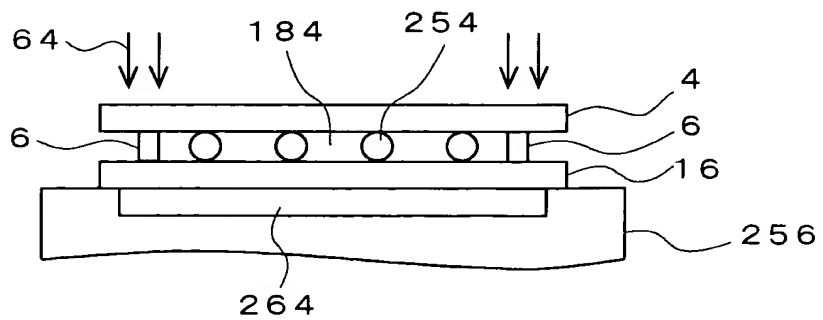


FIG. 68a

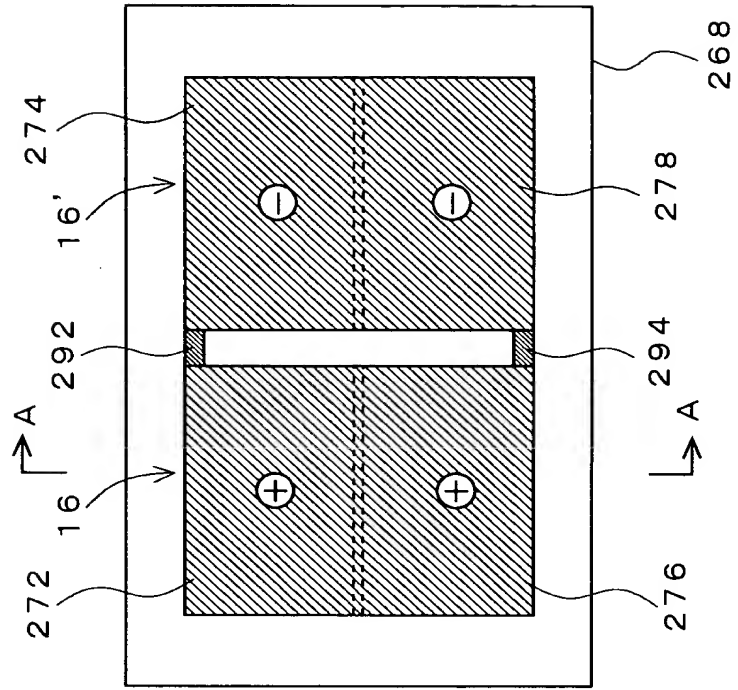


FIG. 69a

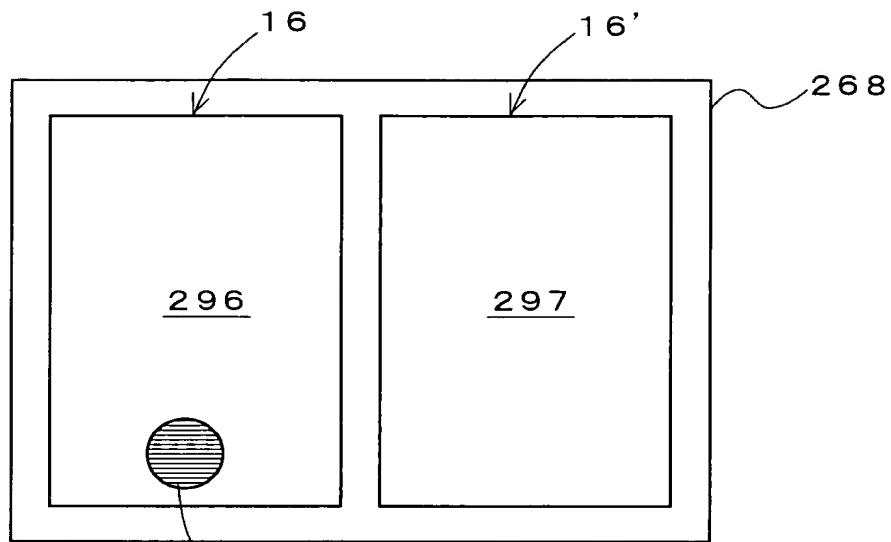


FIG. 69b

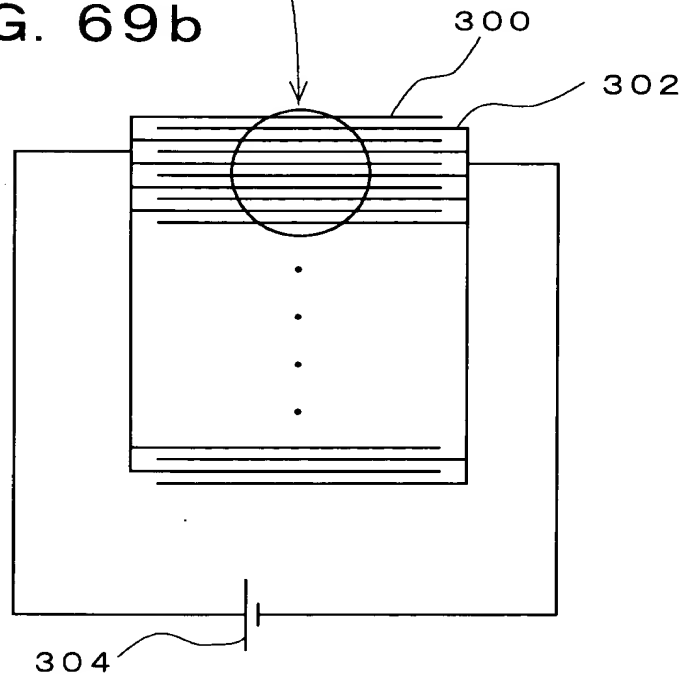


FIG. 70a

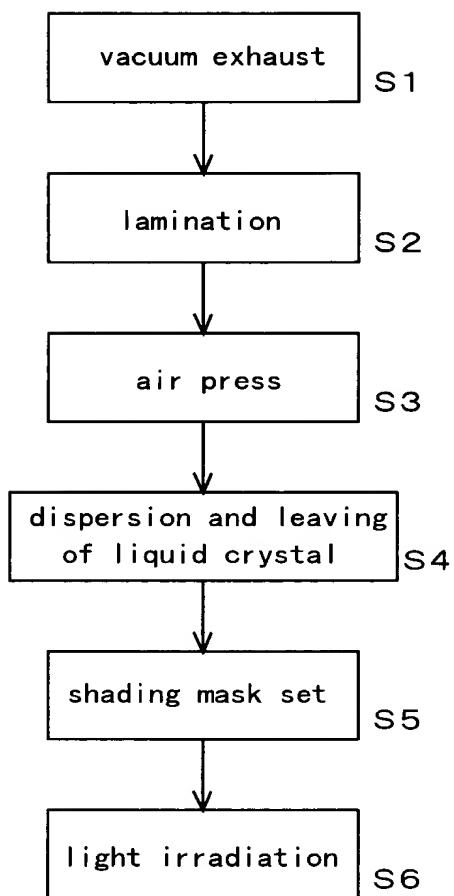


FIG. 70b

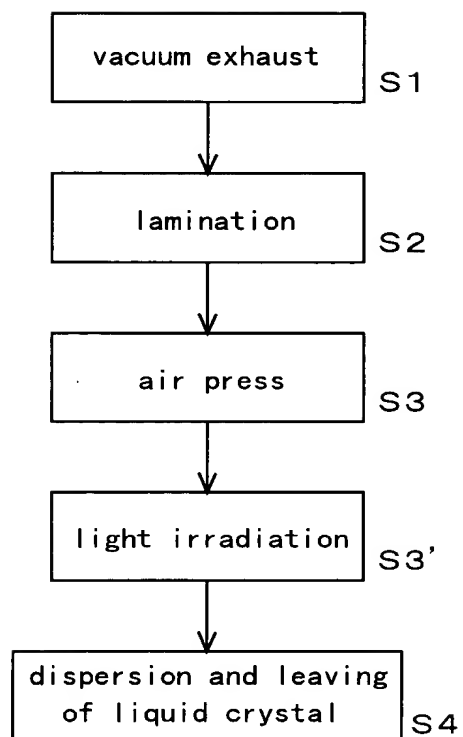


FIG. 71

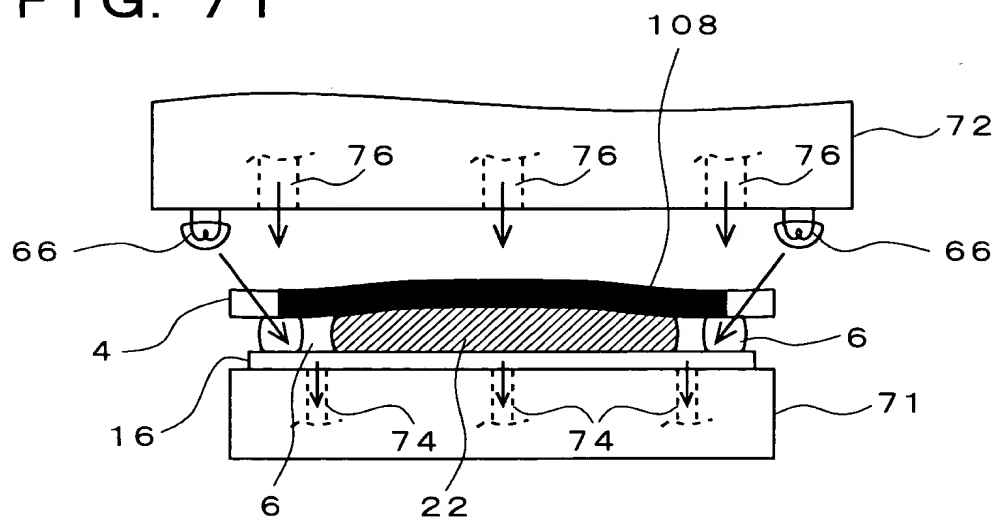


FIG. 72

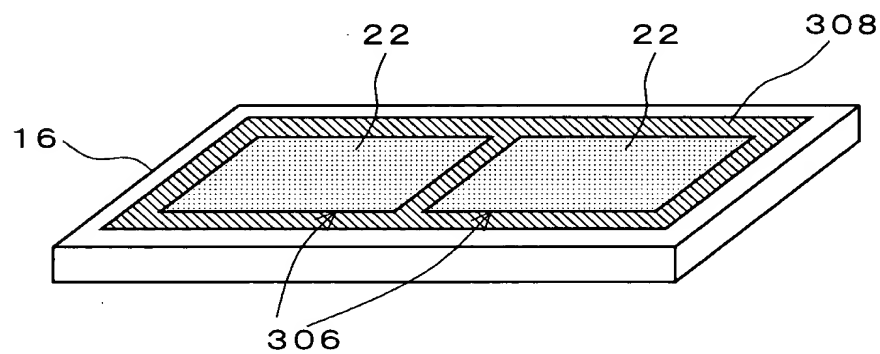


FIG. 73

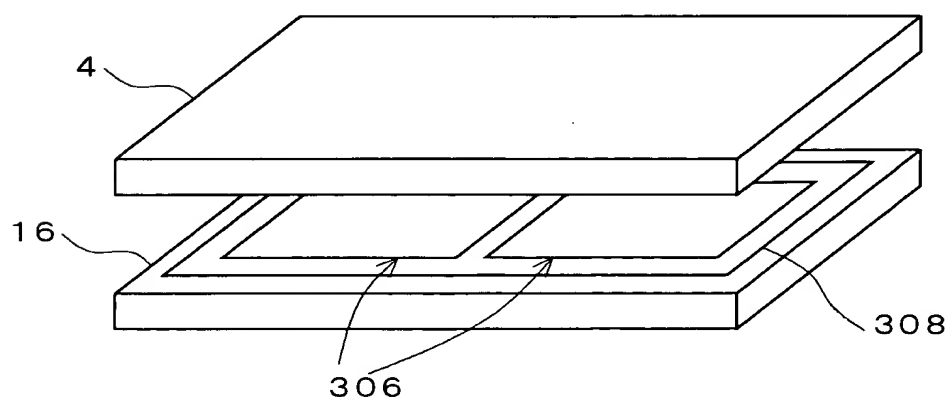


FIG. 74

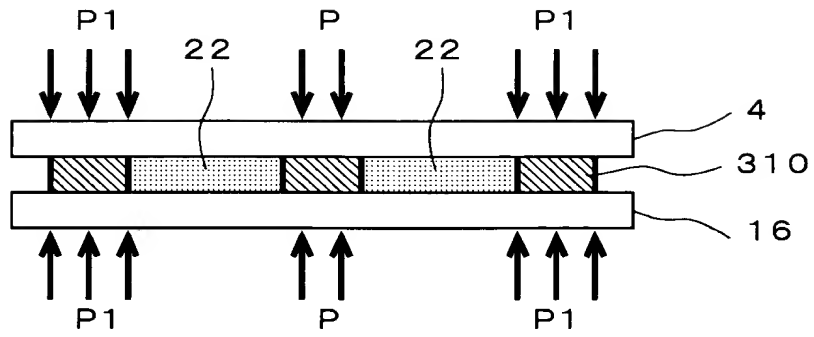


FIG. 75

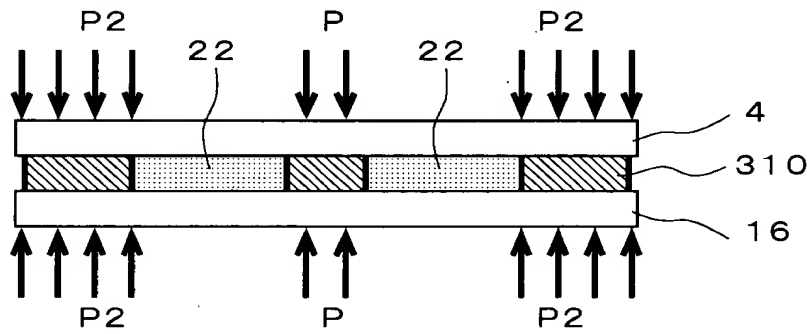


FIG. 76

cell gap (seal side - panel center)
(μm)

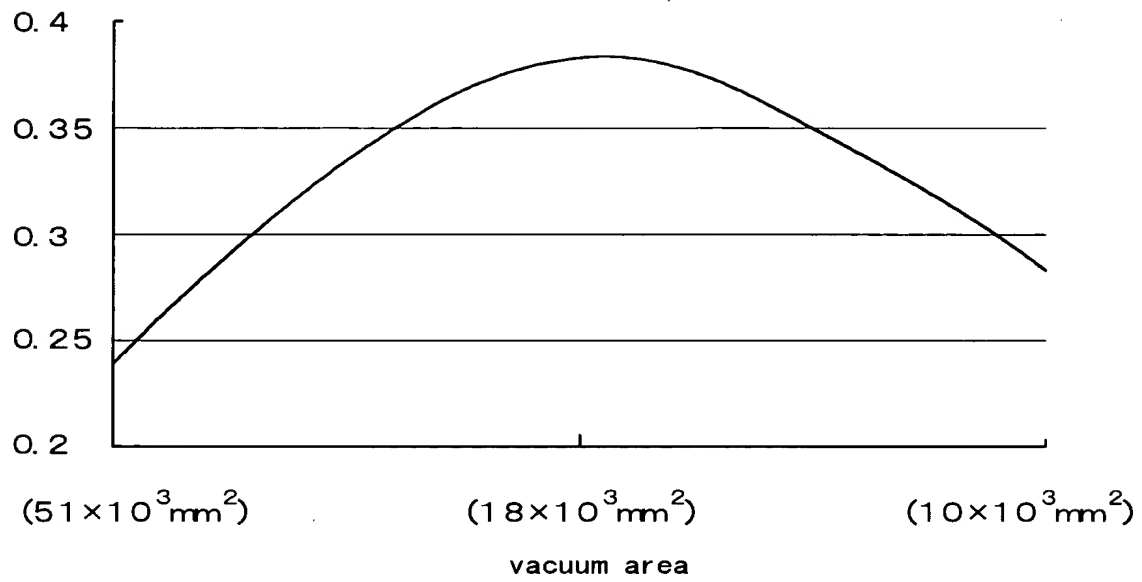


FIG. 77a

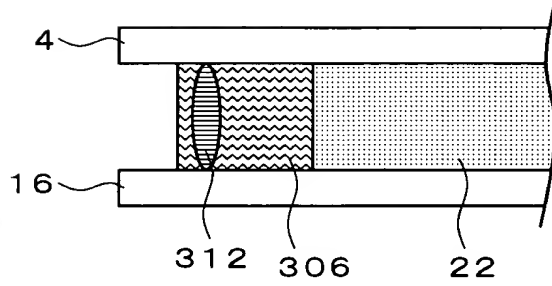


FIG. 77b

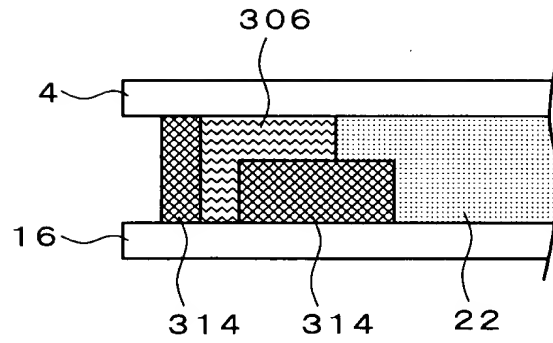


FIG. 78

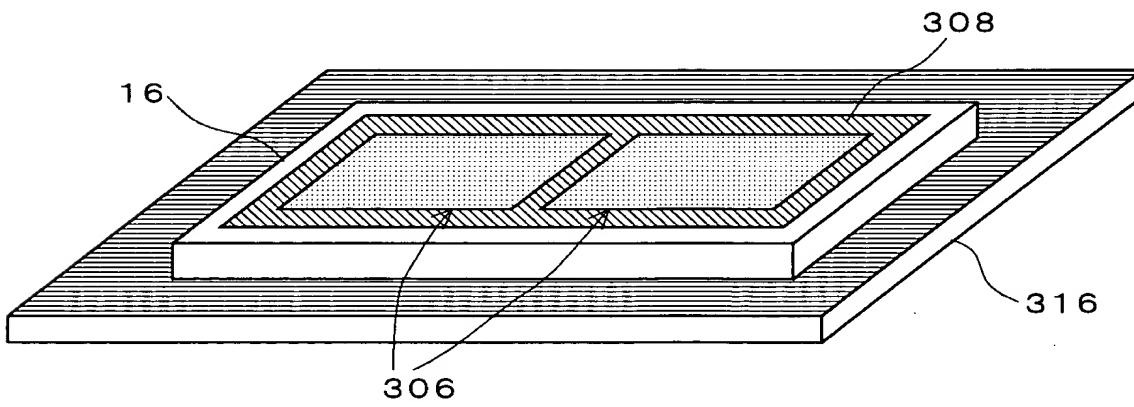


FIG. 79

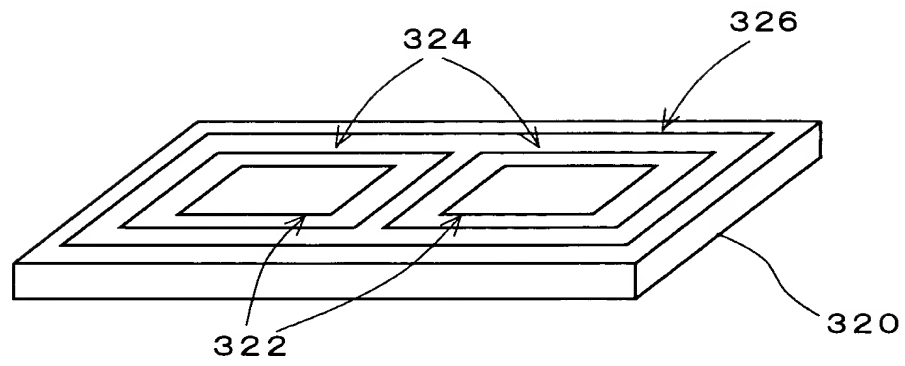


FIG. 80

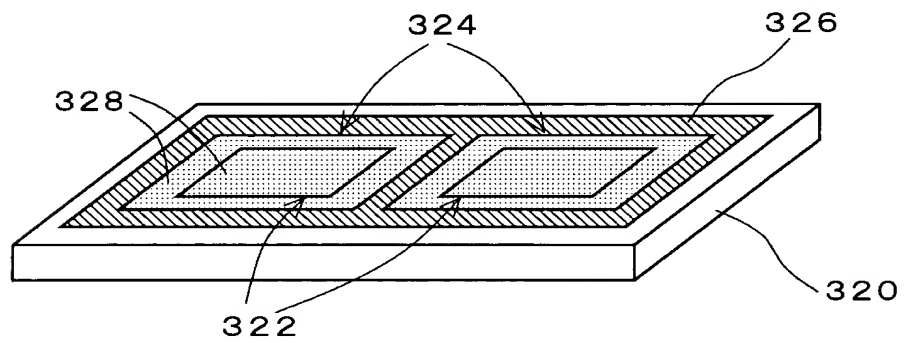


FIG. 81

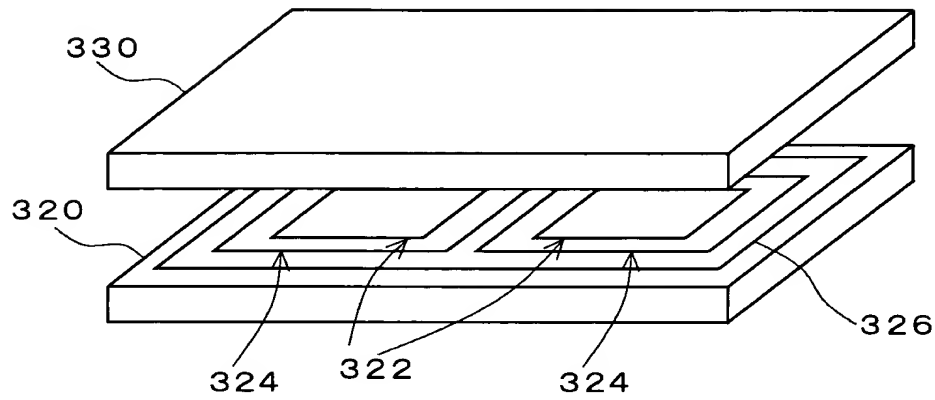


FIG. 82b

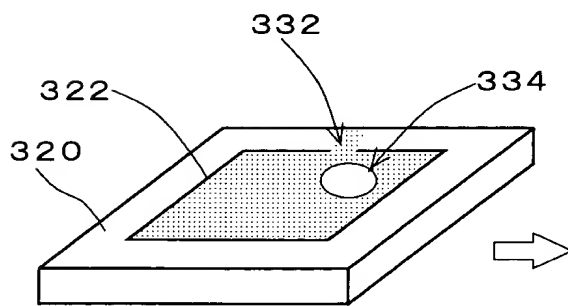


FIG. 82a

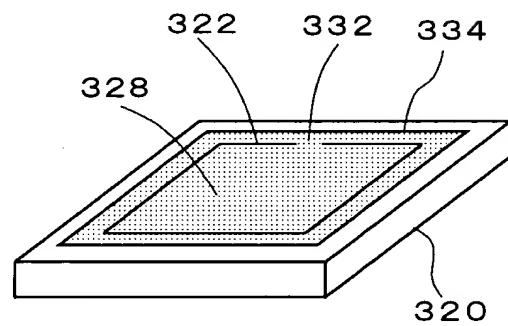


FIG. 83

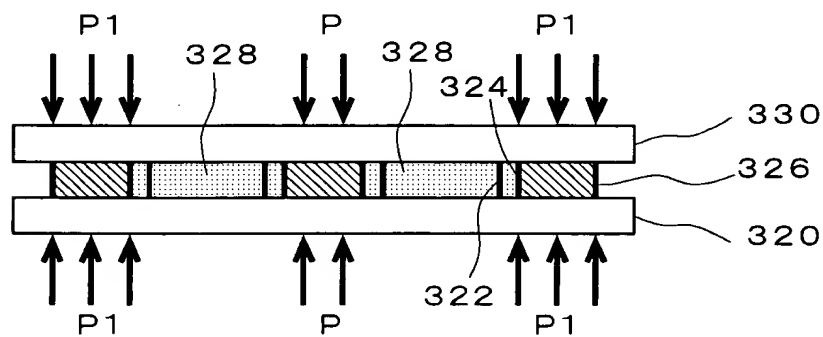


FIG. 86

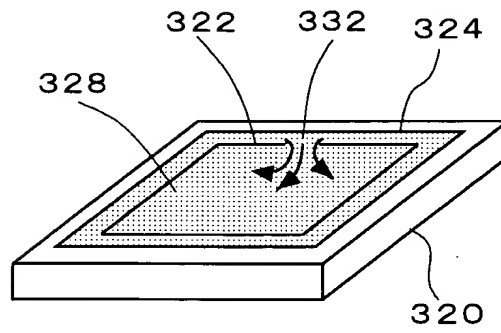


FIG. 87

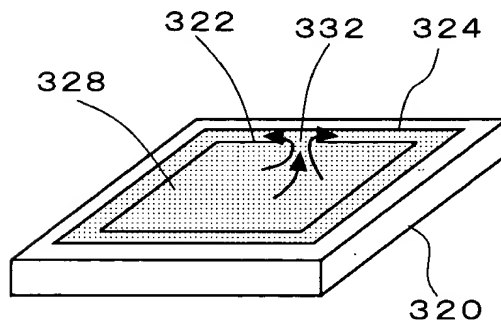


FIG. 88a

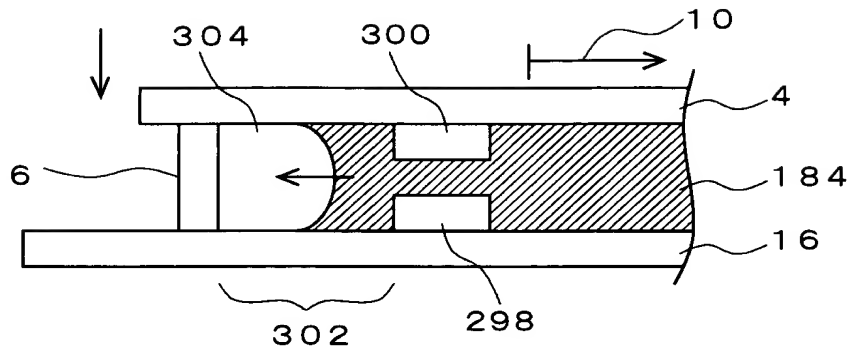


FIG. 88a

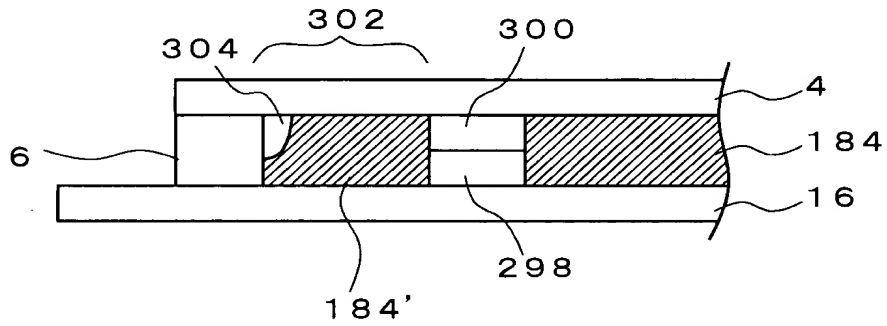


FIG. 89

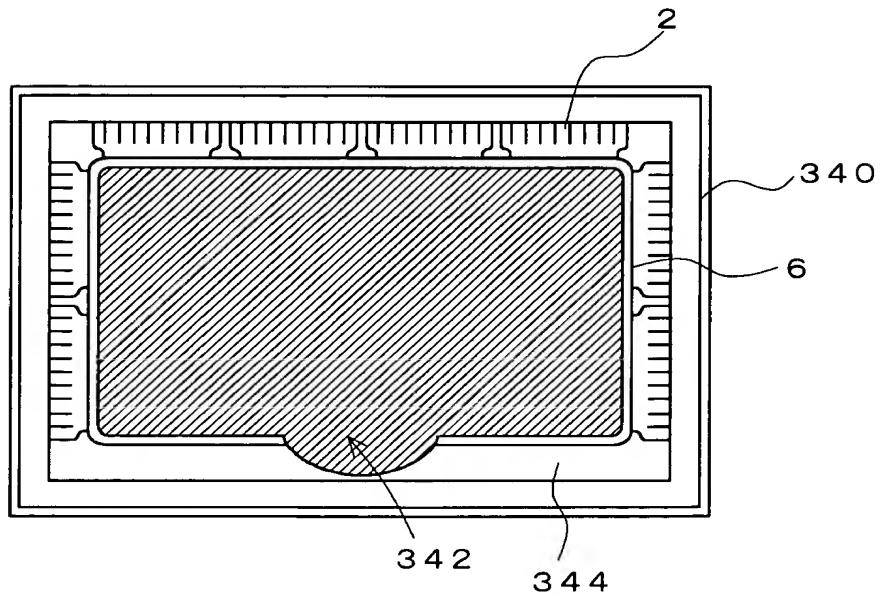


FIG. 90a

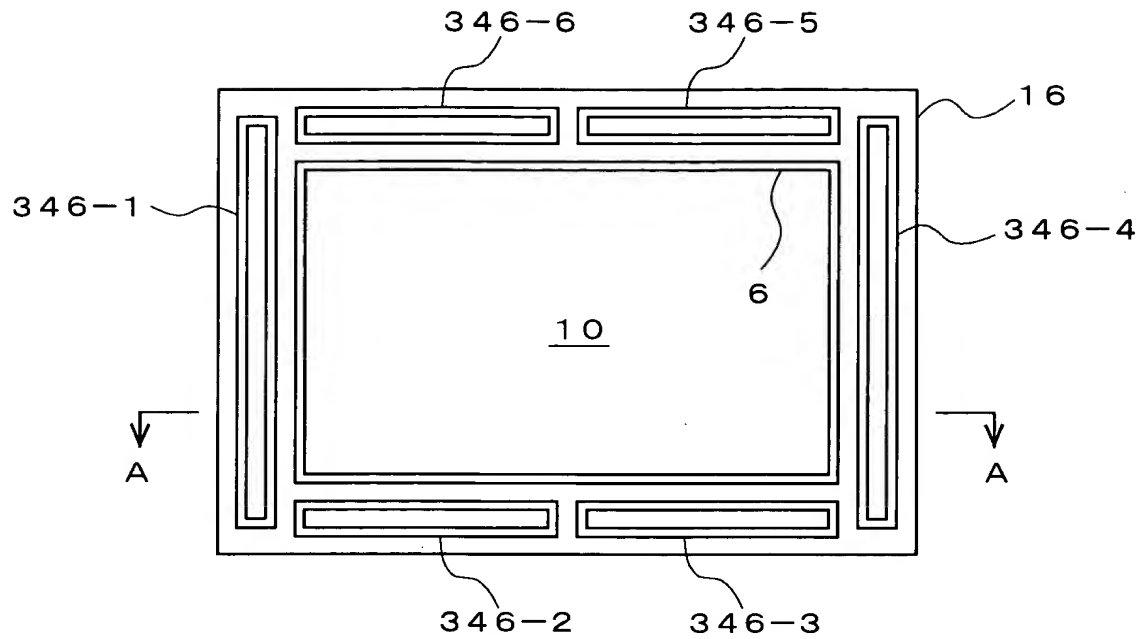


FIG. 90b

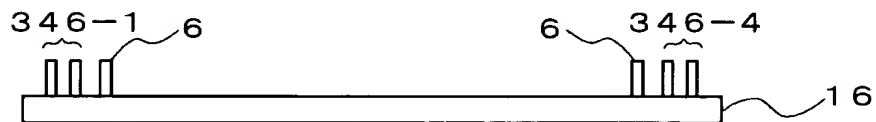


FIG. 90c

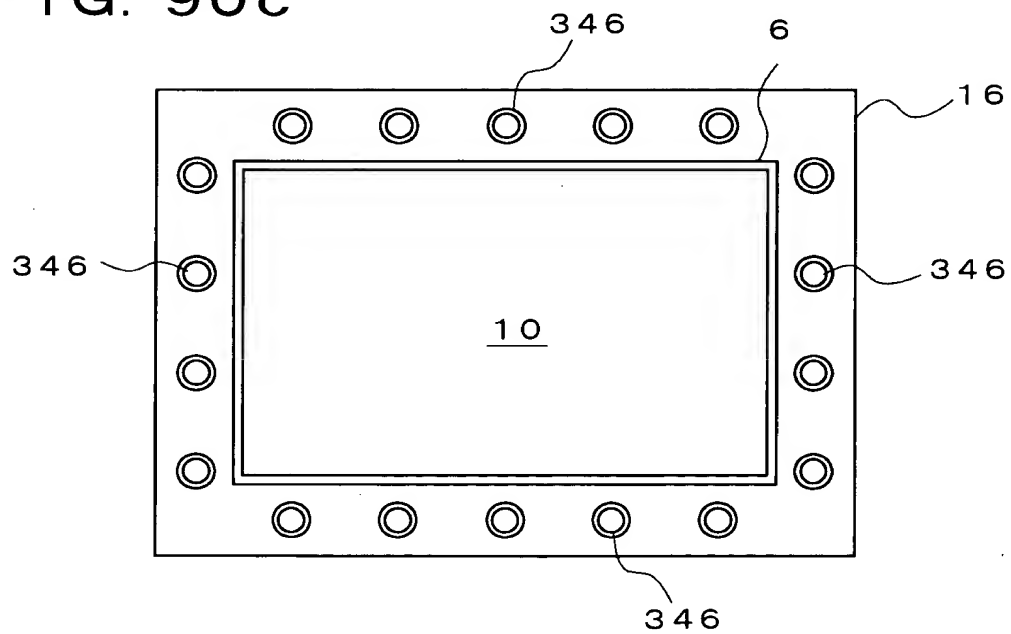


FIG. 91

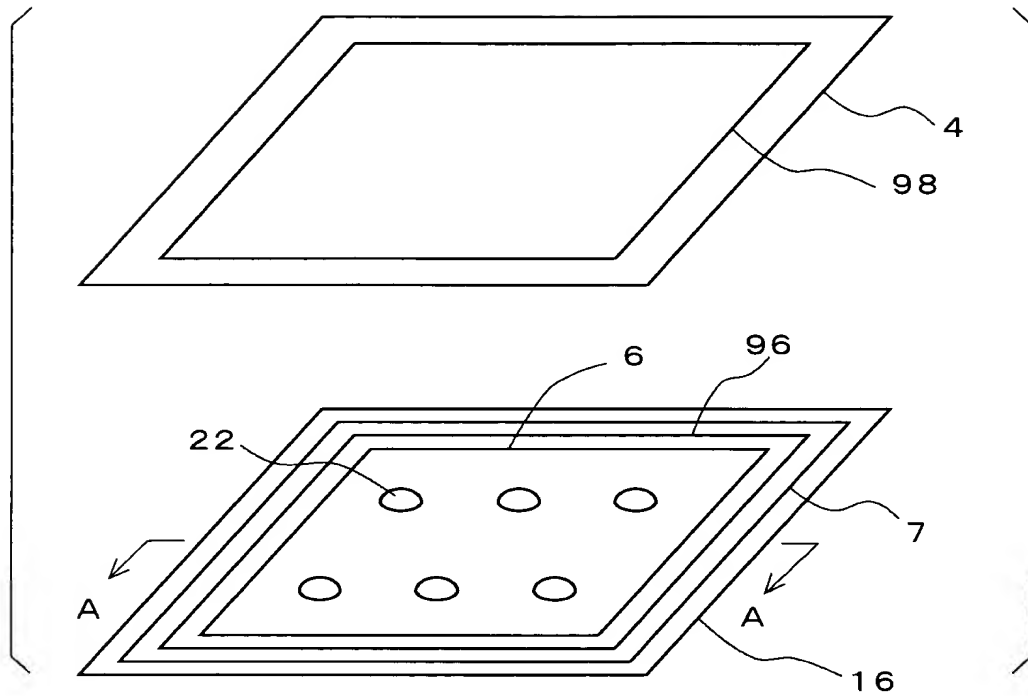


FIG. 92

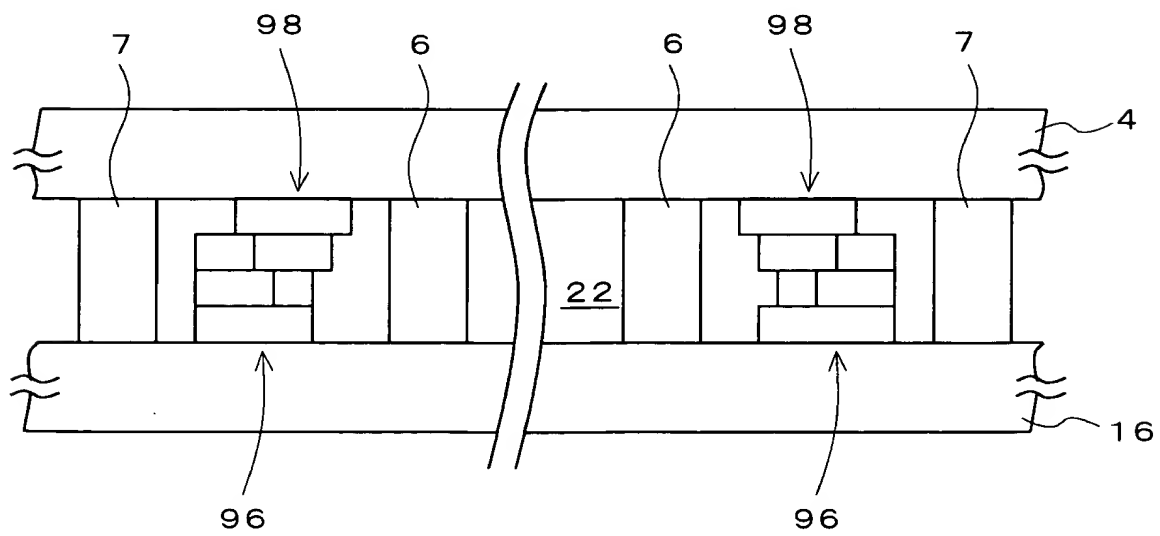


FIG. 93

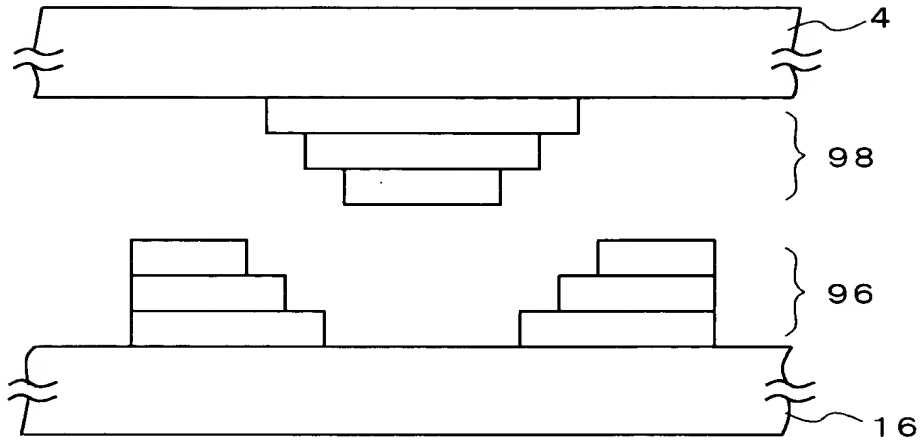


FIG. 94

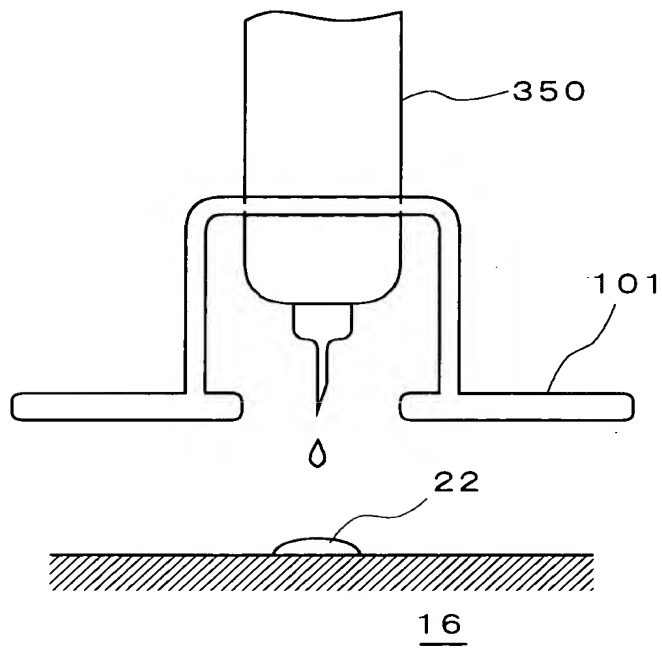


FIG. 95

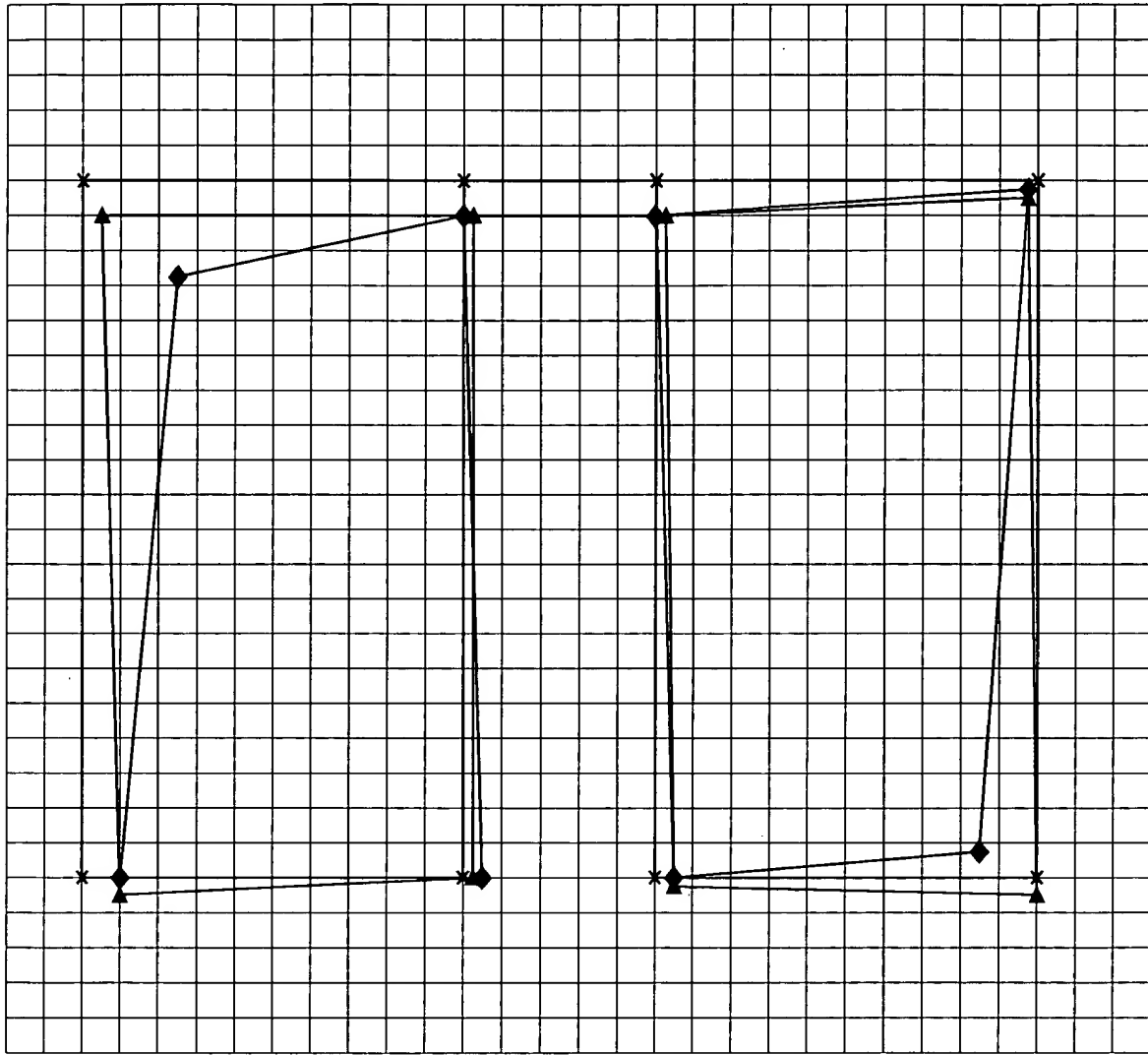
1 scale: 1 μm

FIG. 96

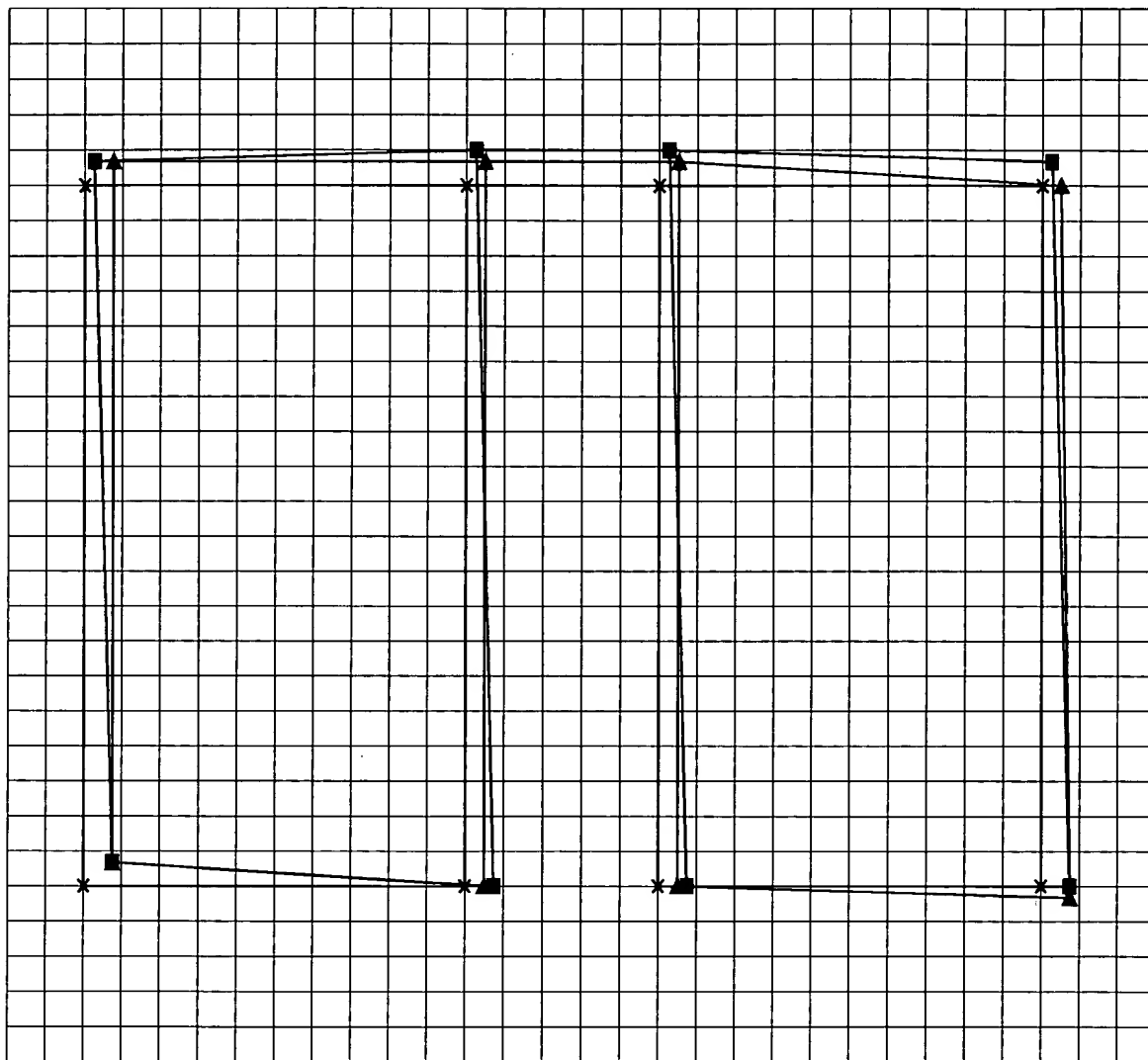
1 scale: 1 μm

FIG. 97

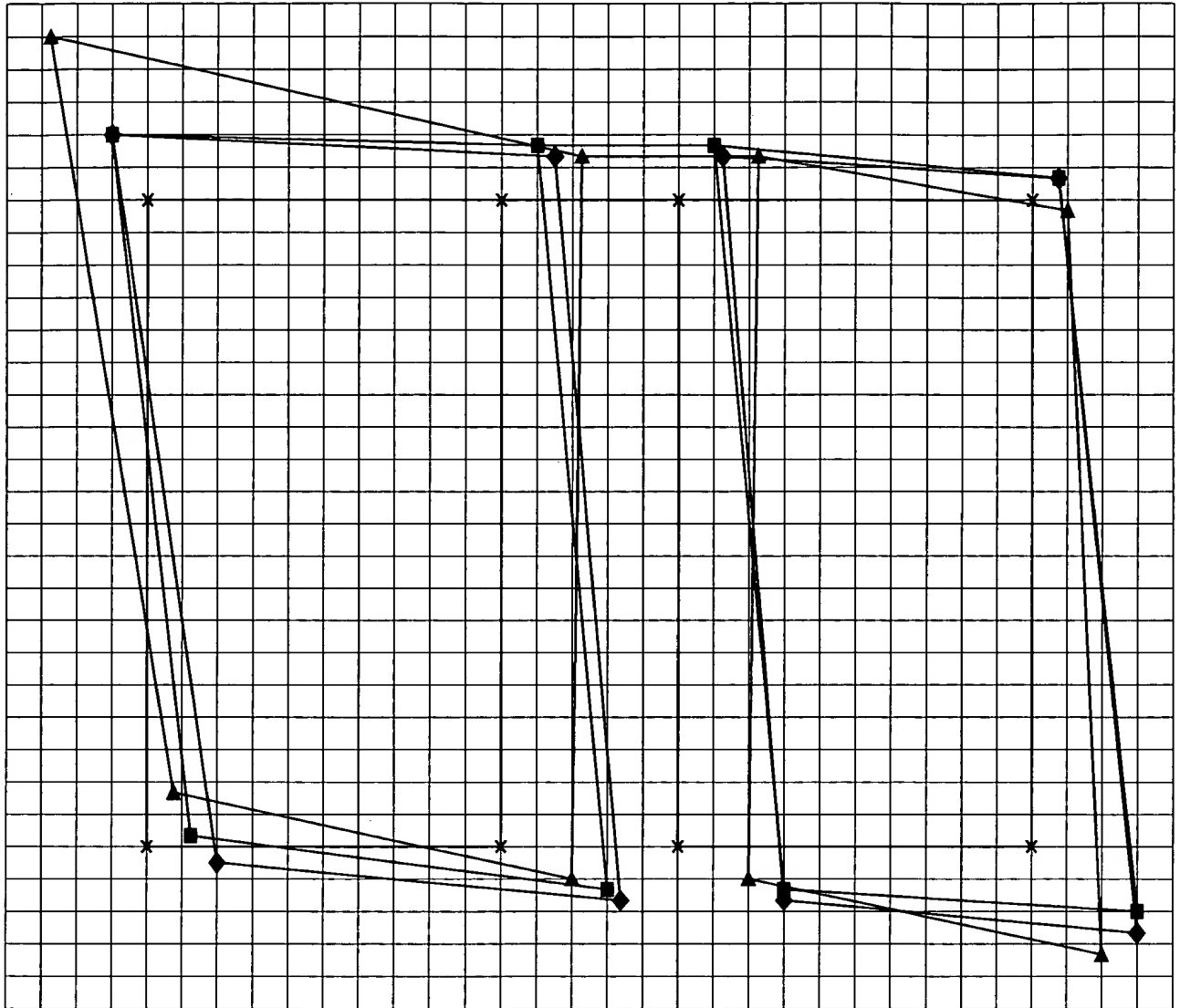
1 scale: 1 μm

FIG. 98

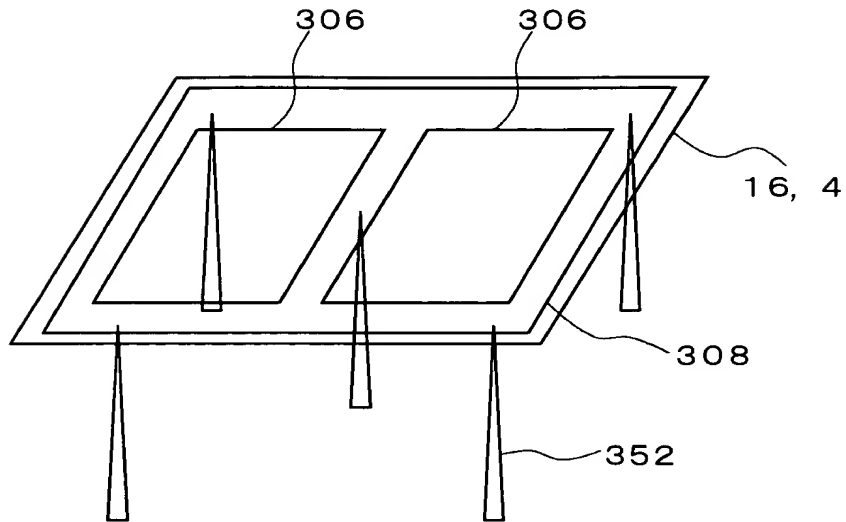


FIG. 99

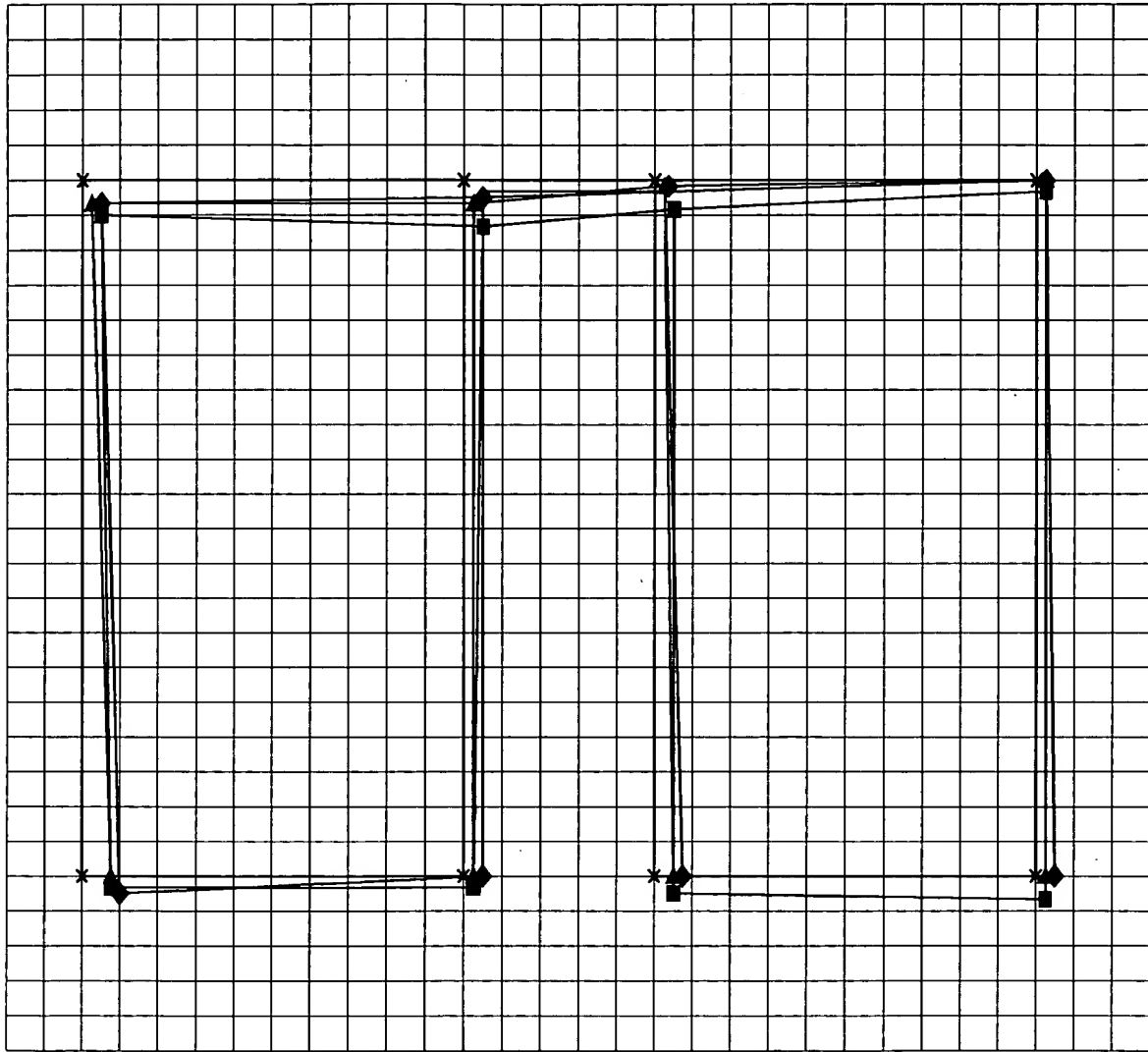
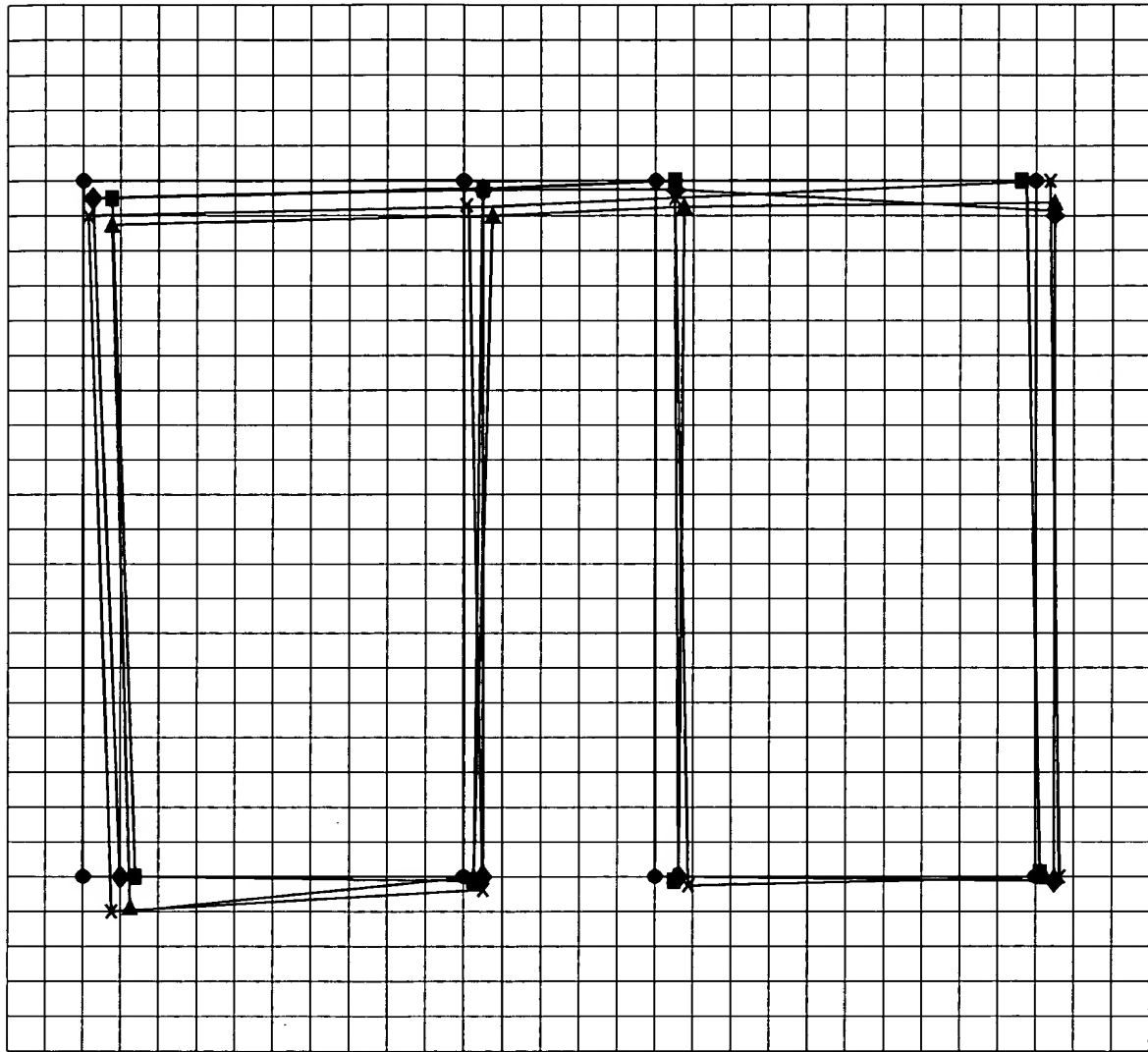
1 scale: 1 μm

FIG. 100

1 scale: 1 μm

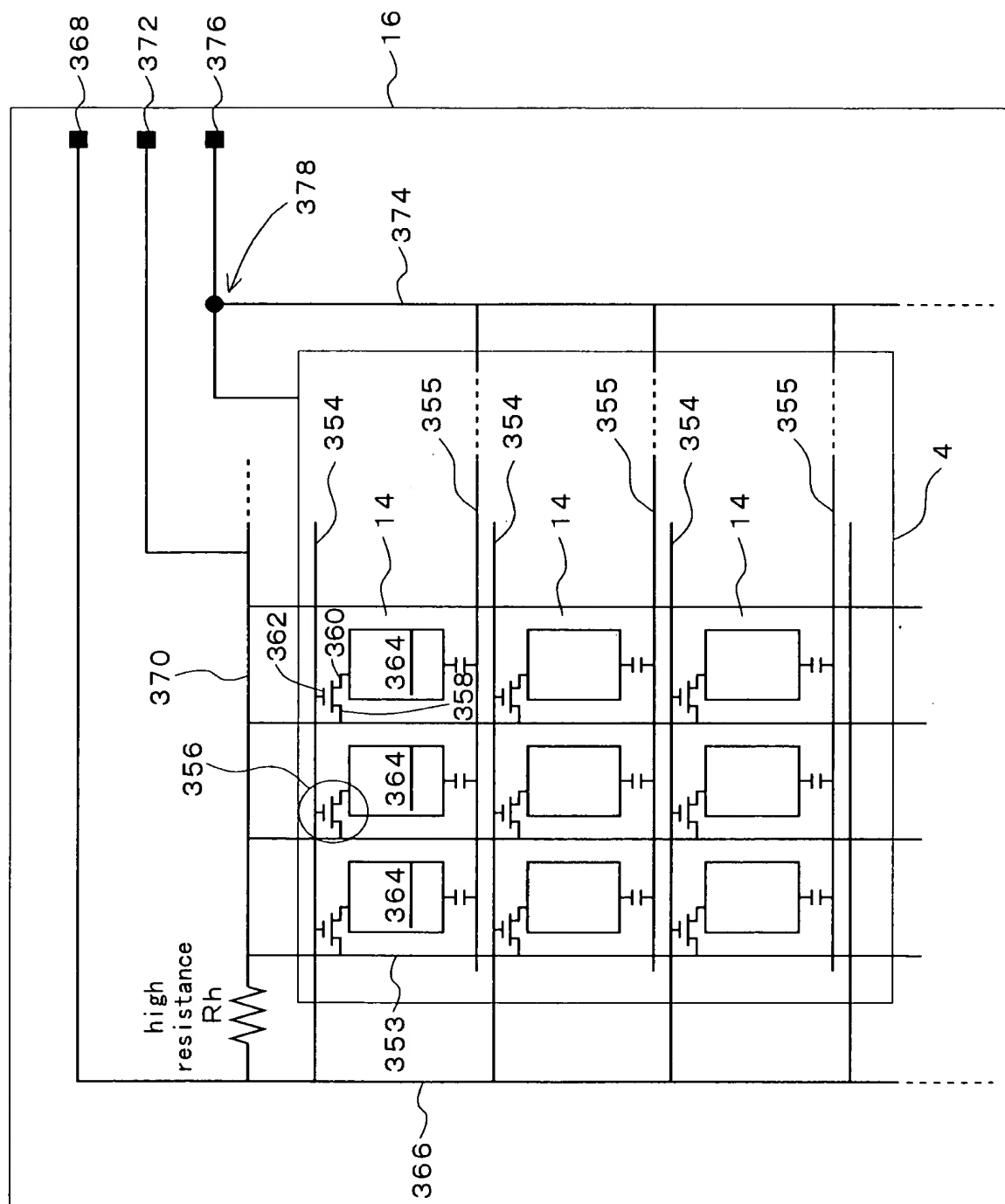
[illegible]

FIG. 102

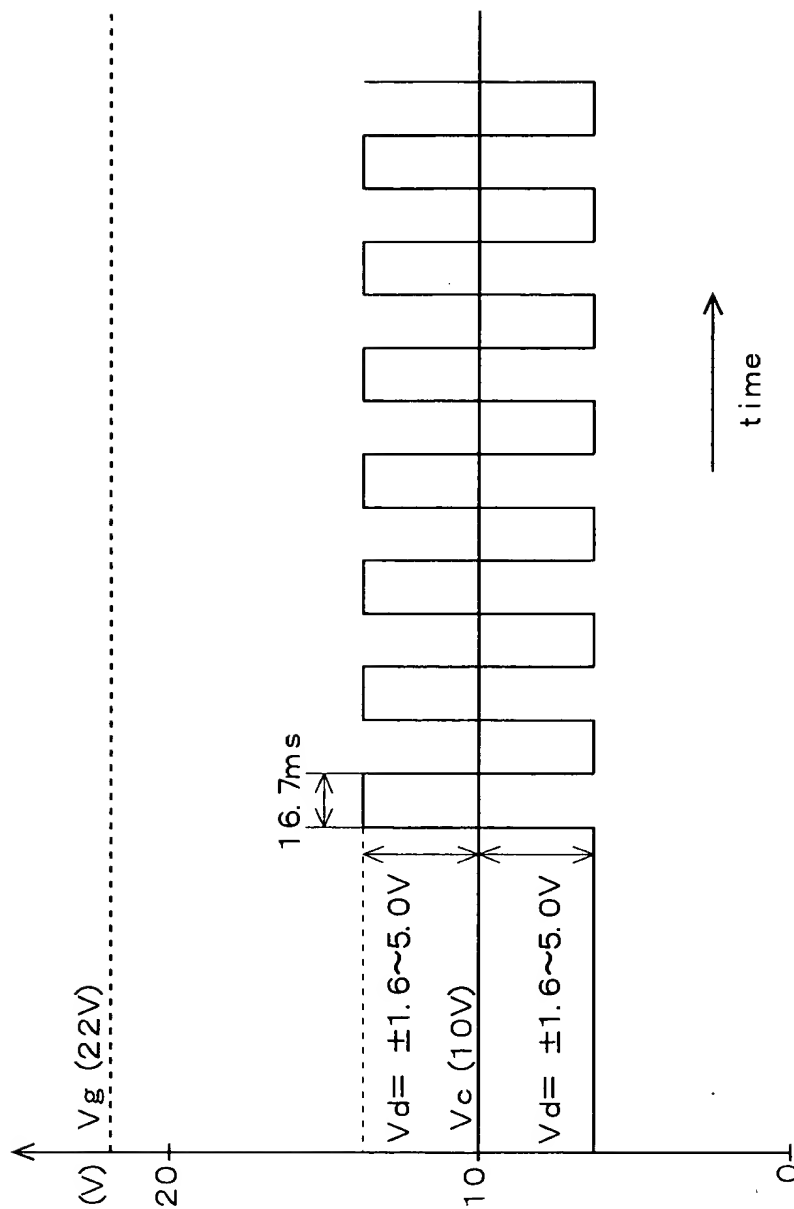


FIG. 103

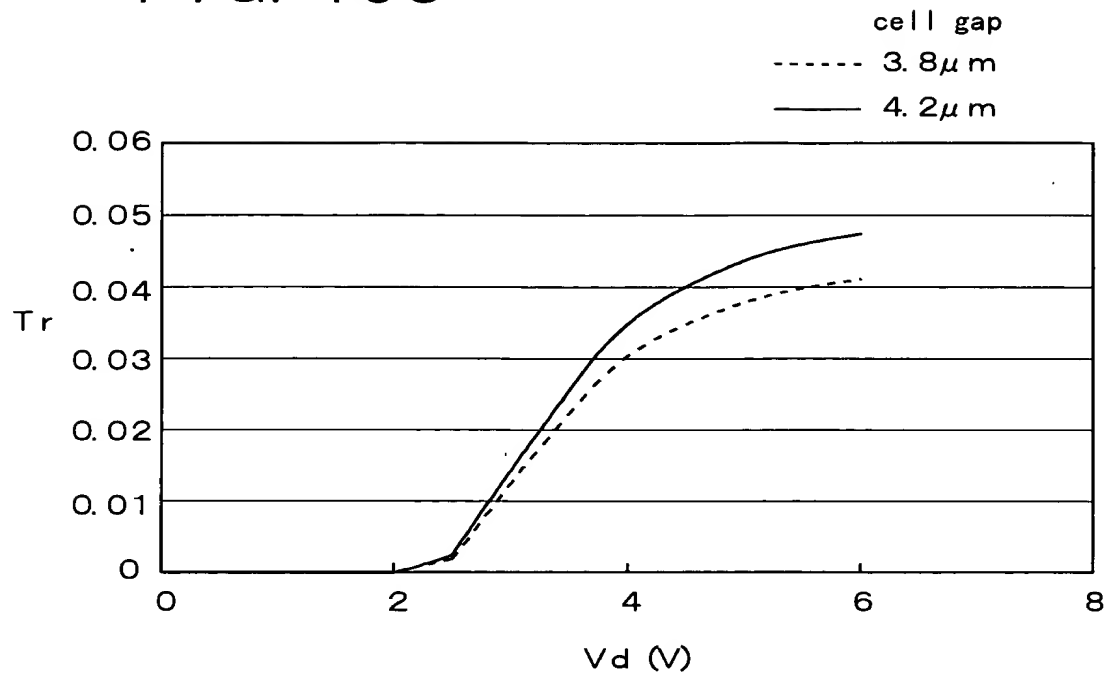


FIG. 104

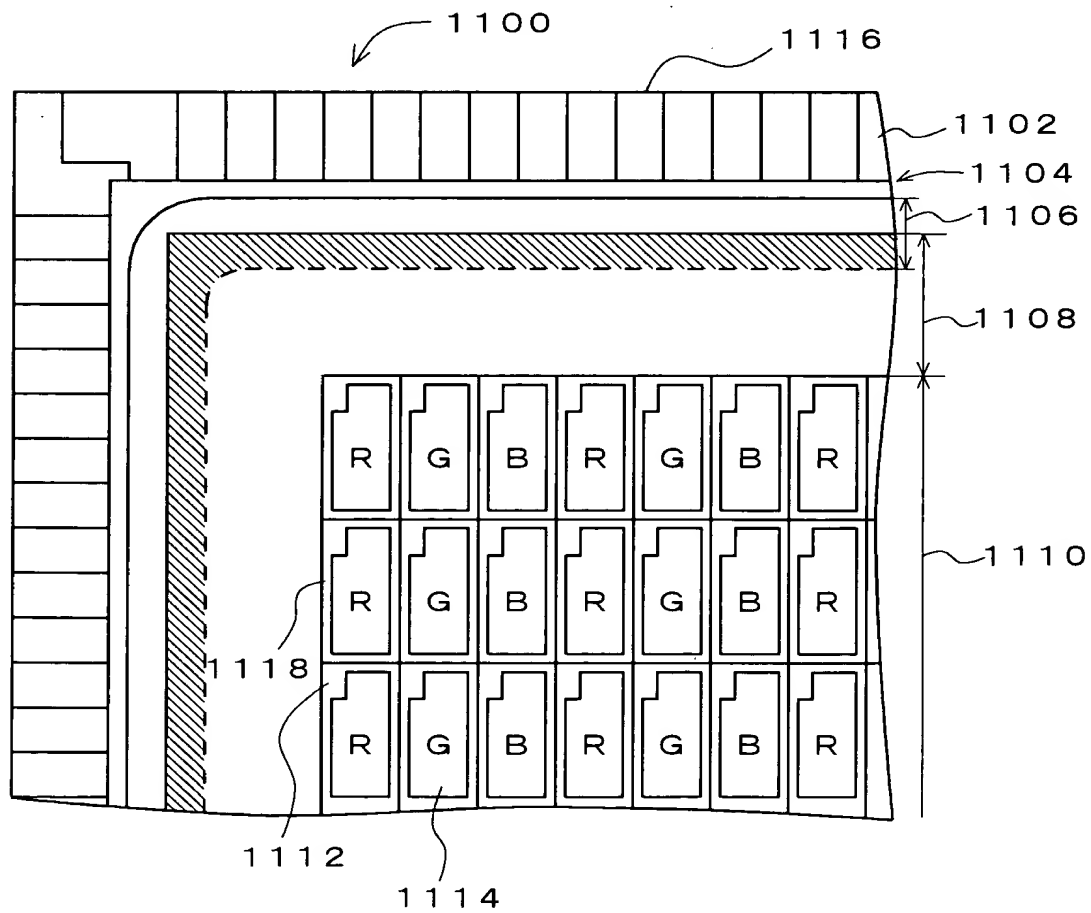


FIG. 105a

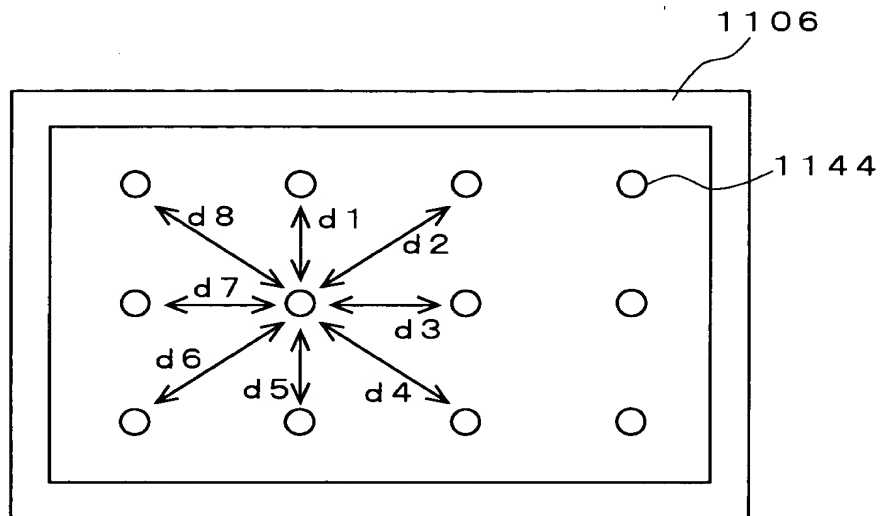


FIG. 105b

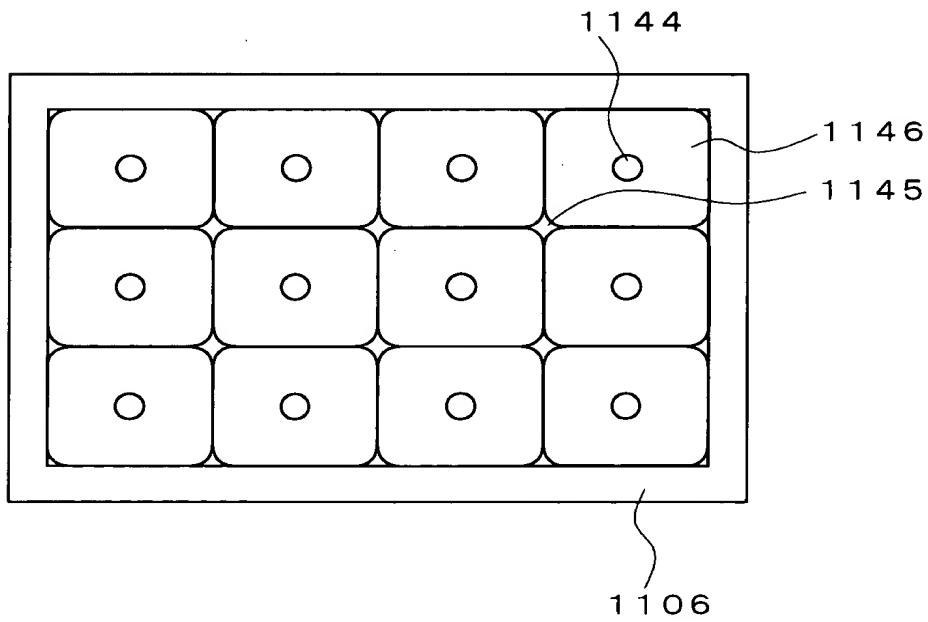


FIG. 106a

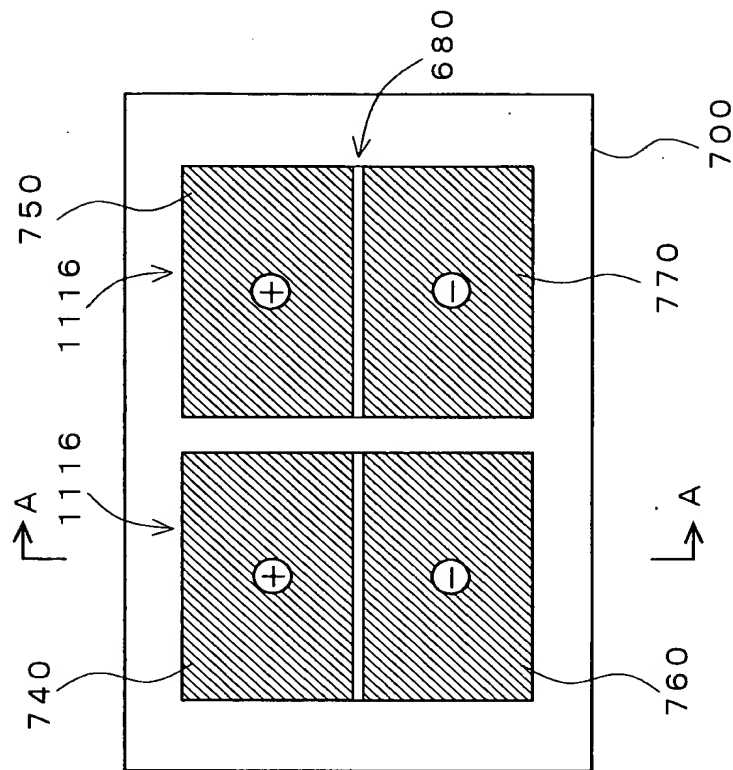


FIG. 106b

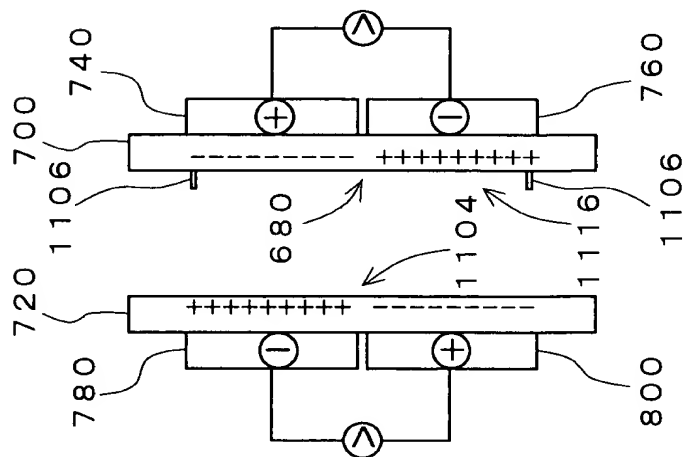


FIG. 107a

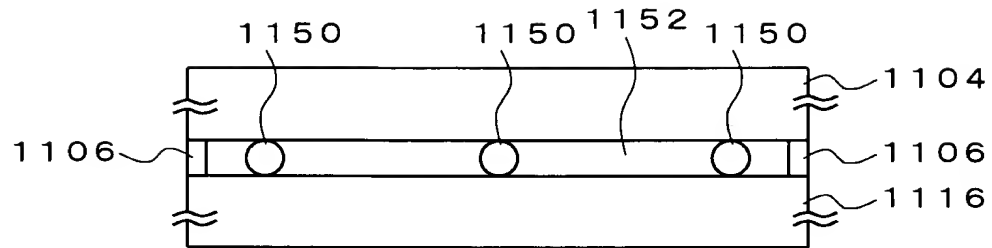


FIG. 107b

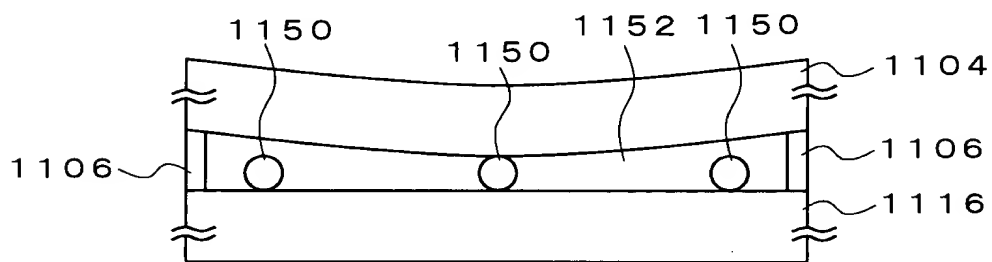


FIG. 107c

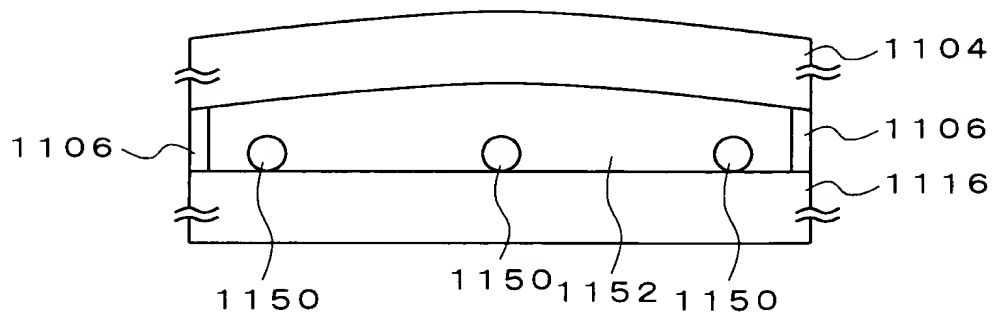


FIG. 108a

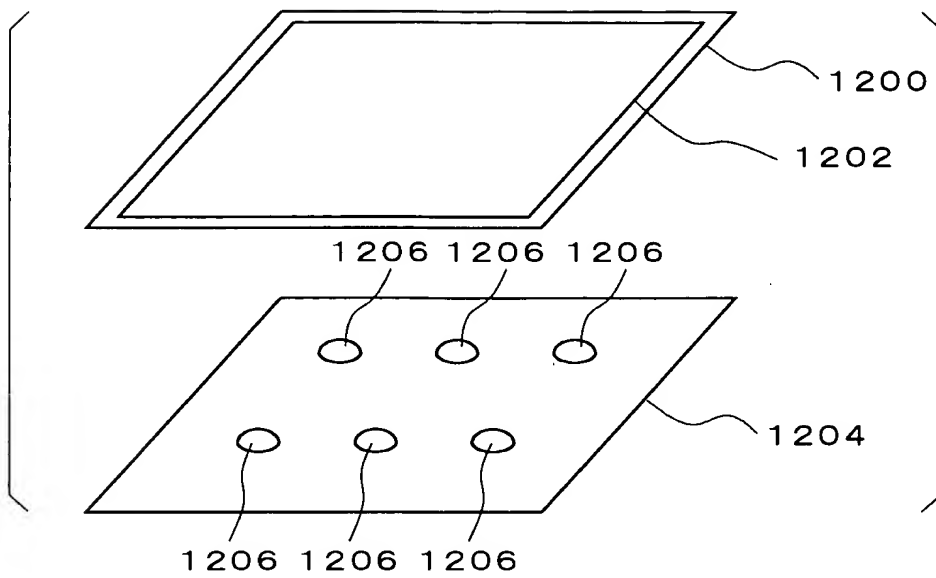


FIG. 108b

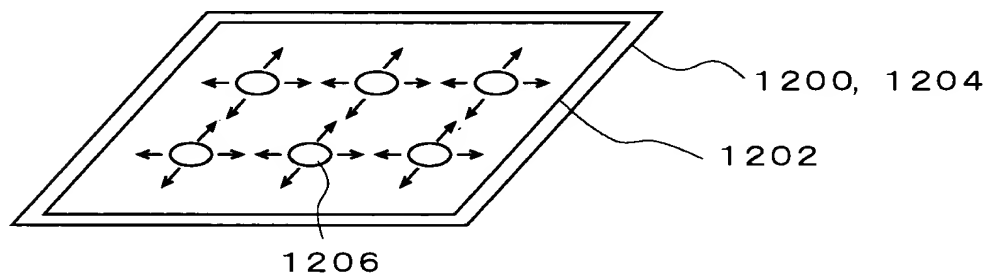


FIG. 108c

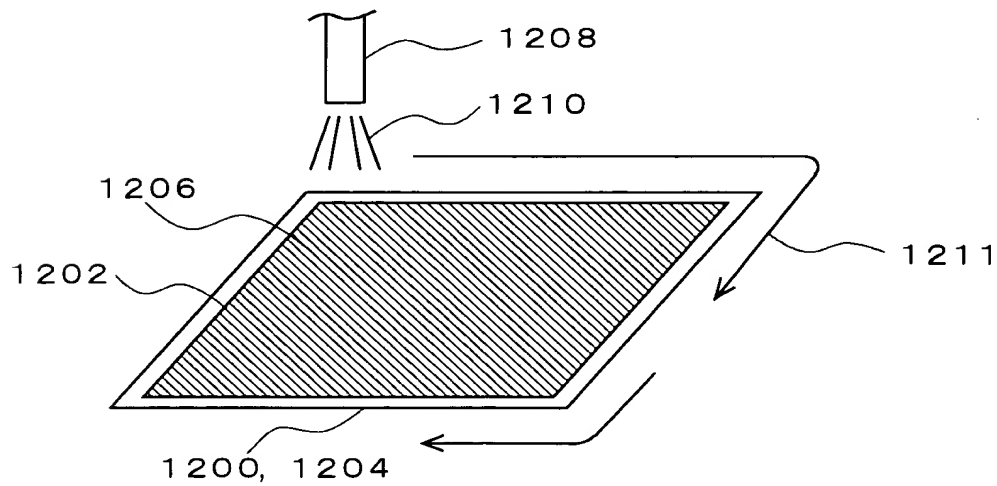


FIG. 109

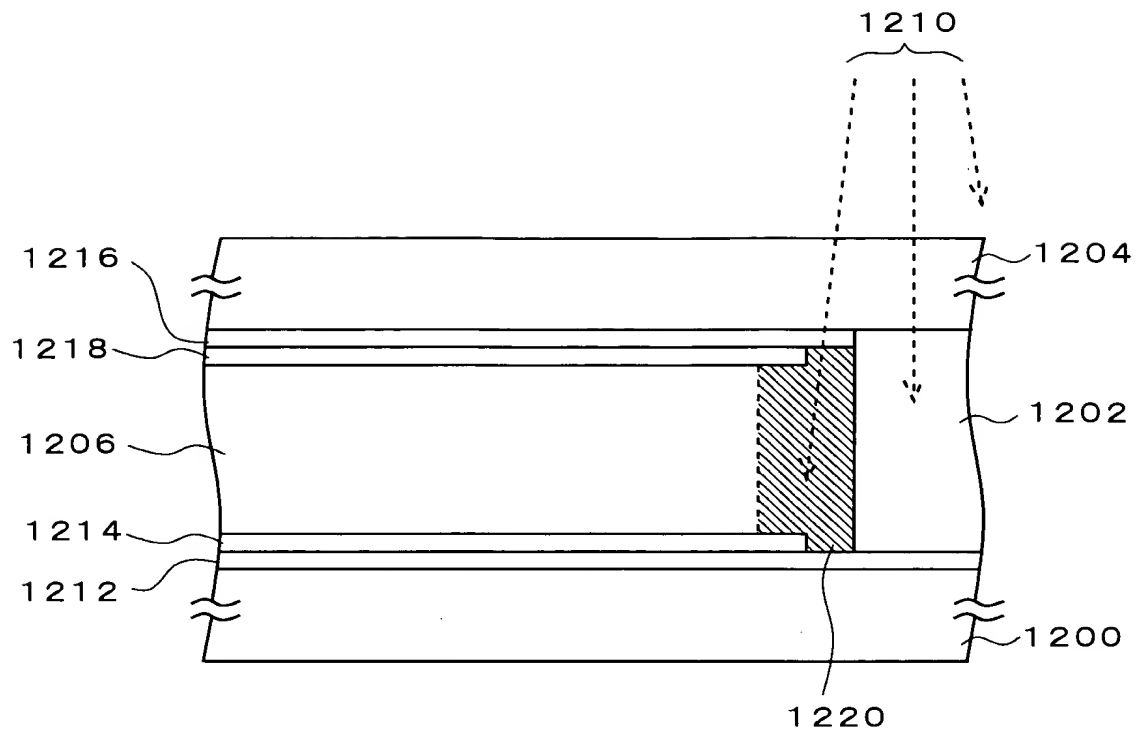


FIG. 110a

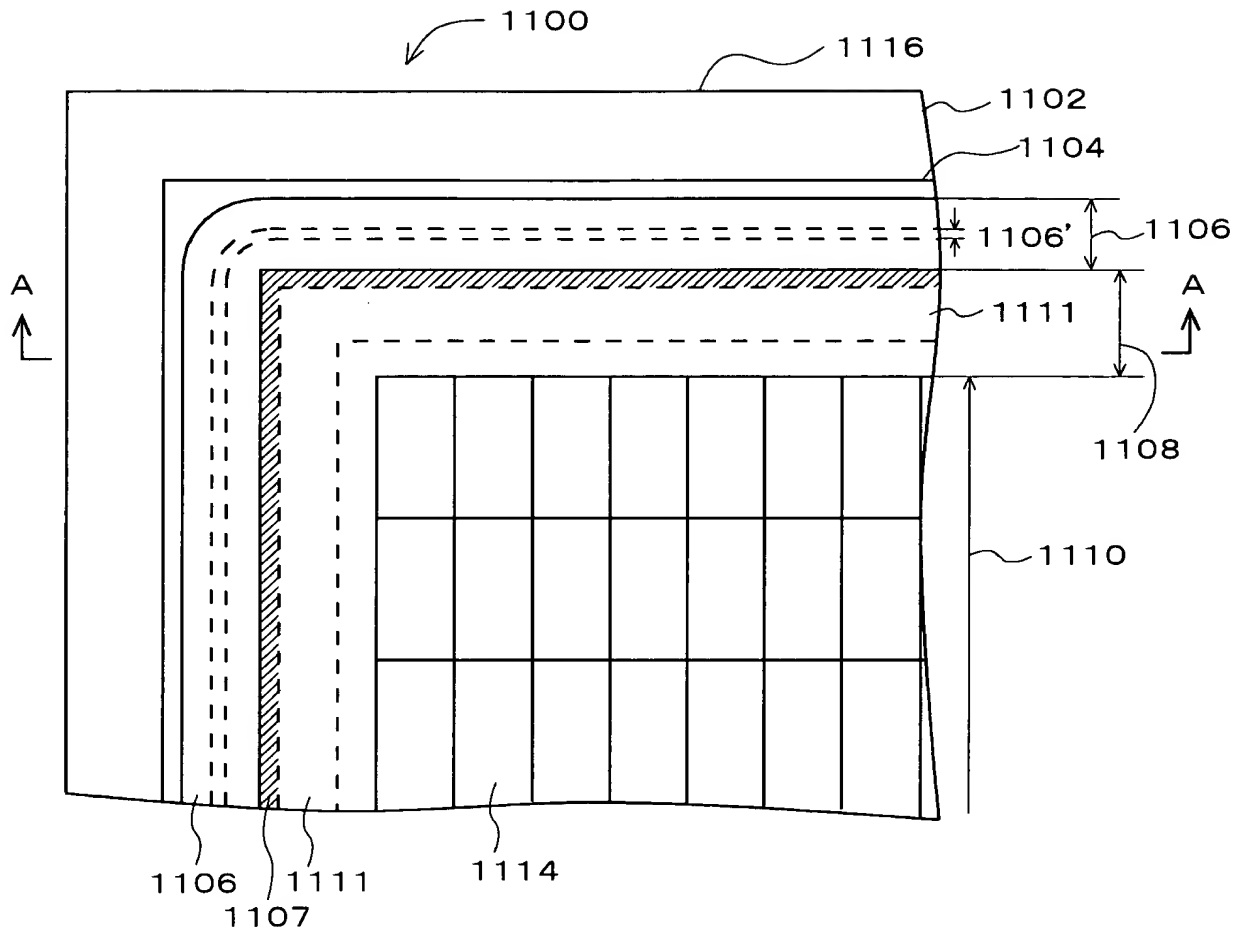


FIG. 110b

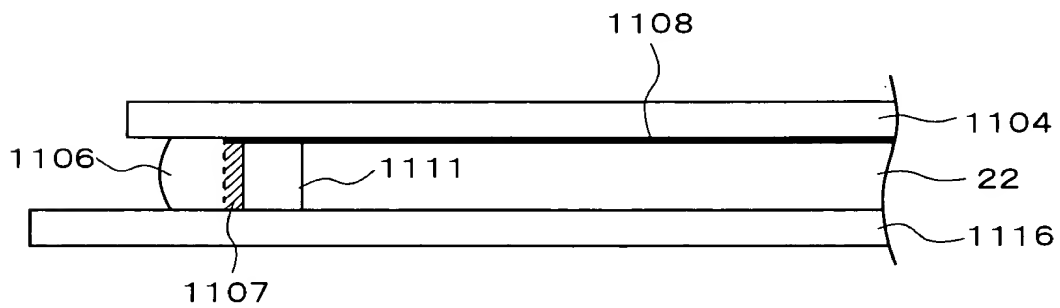


FIG. 111

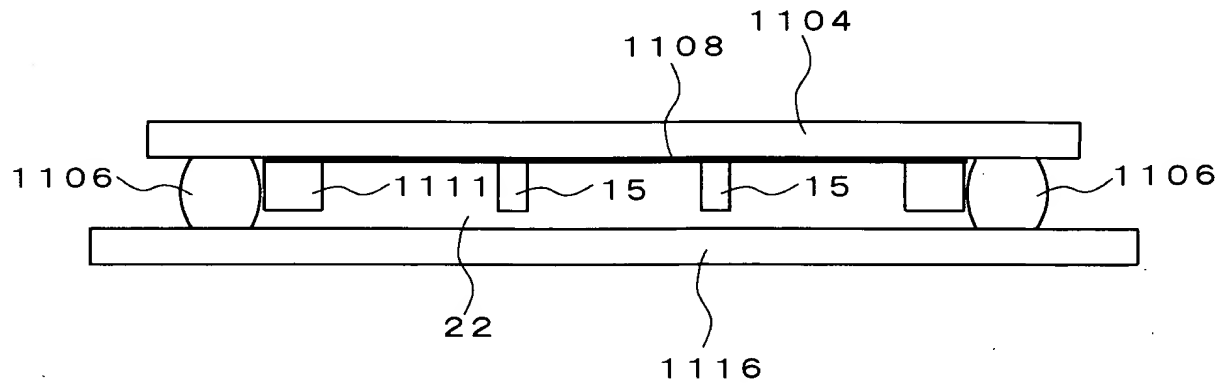


FIG. 112

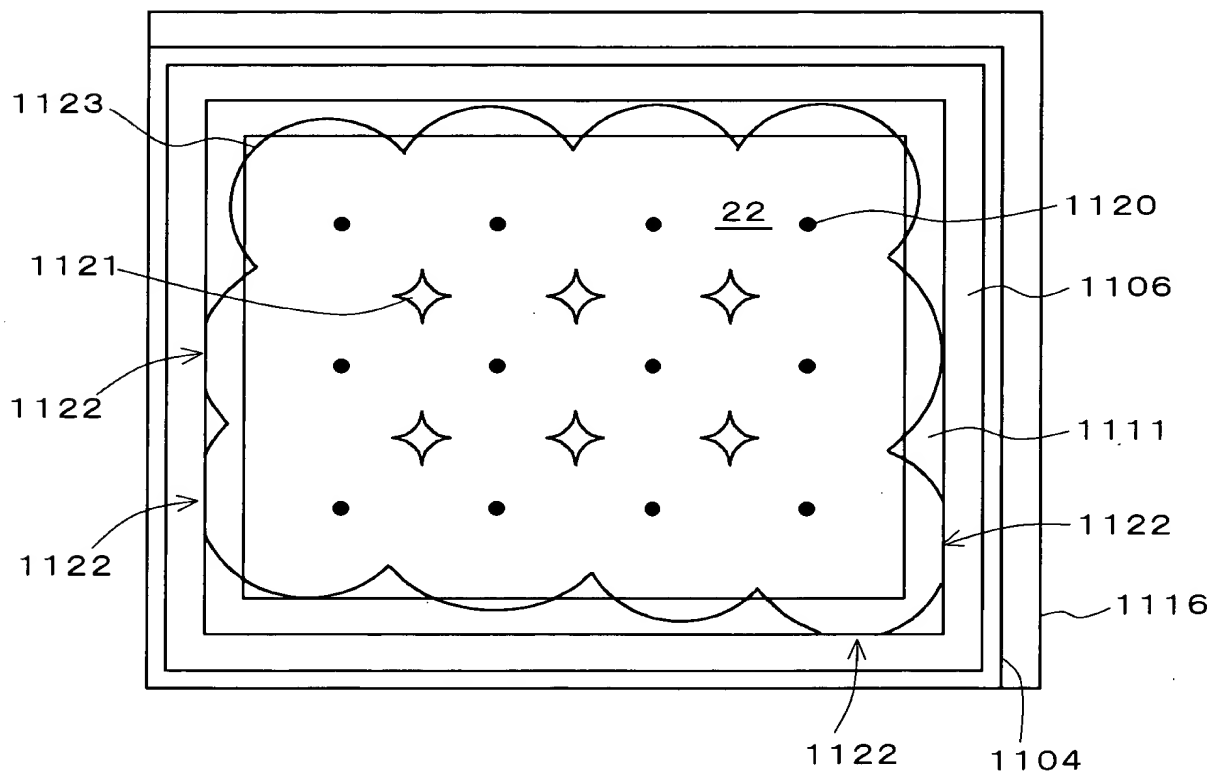


FIG. 113

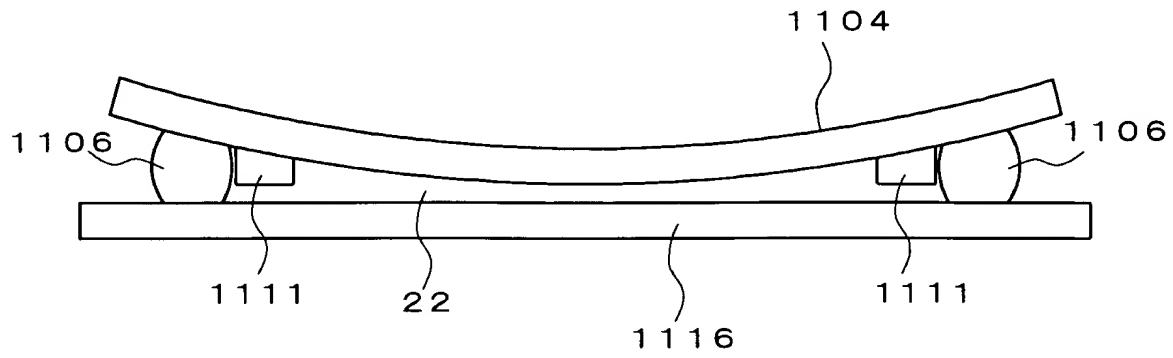


FIG. 114a

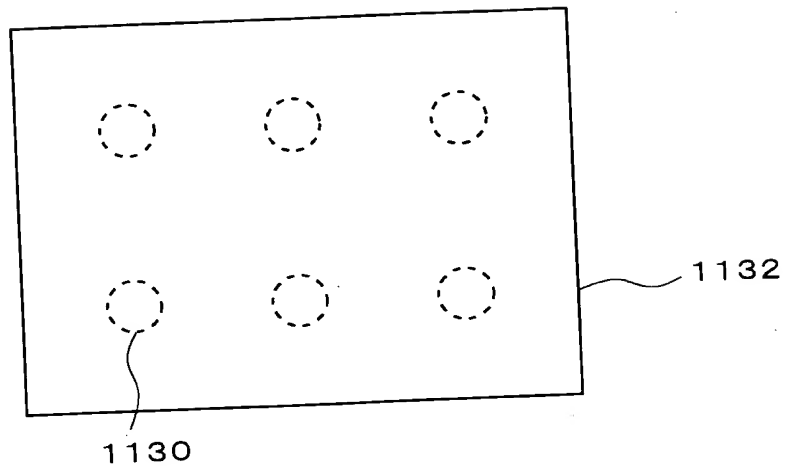


FIG. 114b

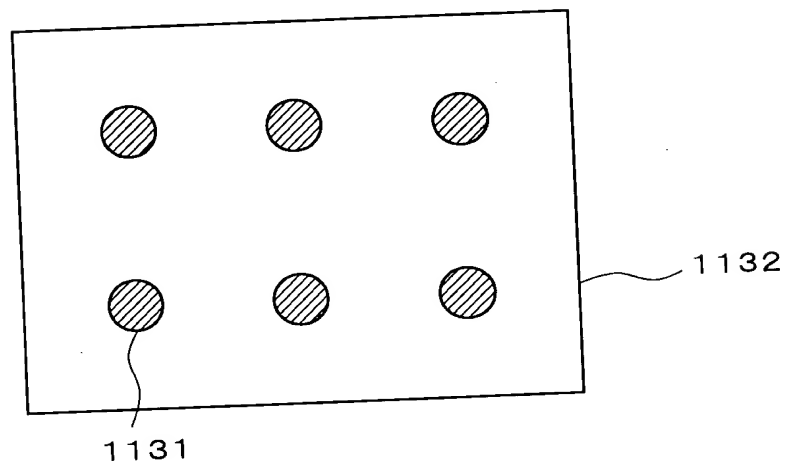


FIG. 114c

